ACSM1

Firmware Manual
ACSM1 Motion Control Program

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Table of contents

Table of contents

Introduction to the manual

Start-up

Drive programming using PC tools

Drive control and features

Default connections of the control unit

Parameters and firmware blocks

Parameter data

Fault tracing

Standard function blocks

Application program template

Control chain block diagrams

Appendix A – Fieldbus control

Appendix B – Drive-to-drive link

Appendix C – Homing modes

Introduction to the manual

What this chapter contains

The chapter includes a description of the contents of the manual. In addition it contains information about the compatibility, safety and intended audience.

Compatibility

The manual is compatible with ACSM1 Motion Control program version UMFI1480 and later. See signal 9.04 FIRMWARE VER or PC tool (View - Properties).

Safety instructions

Follow all safety instructions delivered with the drive.

- Read the **complete safety instructions** before you install, commission, or use the drive. The complete safety instructions are given at the beginning of the *Hardware Manual*.
- Read the **software function specific warnings and notes** before changing the default settings of the function. For each function, the warnings and notes are given in this manual in the section describing the related user-adjustable parameters.

Reader

The reader of the manual is expected to know the standard electrical wiring practices, electronic components, and electrical schematic symbols.

Contents

The manual consists of the following chapters:

- *Start-up* instructs in setting up the control program and how to control the drive through the I/O interface.
- *Drive programming using PC tools* introduces programming via PC tool (DriveStudio and/or DriveSPC).
- *Drive control and features* describes the control locations and operation modes of the drive, and the features of the application program.
- *Default connections of the control unit* presents the default connections of the JCU Control Unit.
- *Parameters and firmware blocks* describes the drive parameters and firmware function blocks.
- *Parameter data* contains more information on the parameters of the drive.
- *Fault tracing* lists the warning and fault messages with the possible causes and remedies.
- *Standard function blocks*
- *Application program template*
- *Control chain block diagrams*
- *Appendix A – Fieldbus control* describes the communication between the drive and a fieldbus.
- *Appendix B – Drive-to-drive link* describes the communication between drives connected together by the drive-to-drive link.
- *Appendix C – Homing modes* describes homing modes 1…35.

Product and service inquiries

Address any inquiries about the product to your local ABB representative, quoting the type code and serial number of the unit in question. A listing of ABB sales, support and service contacts can be found by navigating to www.abb.com/drives and selecting *Drives – Sales, Support and Service network*.

Product training

For information on ABB product training, navigate to www.abb.com/drives and select *Drives – Training courses*.

Providing feedback on ABB Drives manuals

Your comments on our manuals are welcome. Go to www.abb.com/drives and select *Document Library – Manuals feedback form (LV AC drives)*.

Start-up

What this chapter contains

This chapter describes the basic start-up procedure of the drive and instructs in how to control the drive through the I/O interface.

How to start up the drive

The drive can be operated:

- locally from PC tool or control panel
- externally via I/O connections or fieldbus interface.

The start-up procedure presented uses the DriveStudio PC tool program. Drive references and signals can be monitored with DriveStudio (Data Logger or Monitor Window). For instructions on how to use DriveStudio, see *DriveStudio User Manual* [3AFE68749026 (English)].

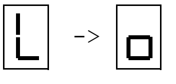
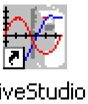
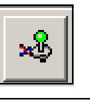
The start-up procedure includes actions which need to be performed only when the drive is powered up for the first time (e.g. entering the motor data). After the first start-up, the drive can be powered up without using these start-up functions. The start-up procedure can be repeated later if start-up data needs to be changed.

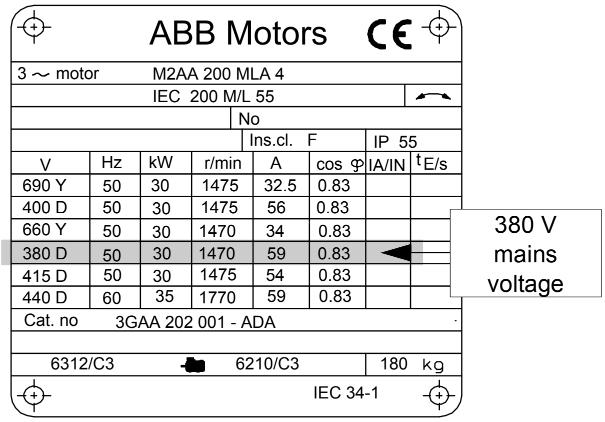
In addition to the PC tool commissioning and drive power-up, the start-up procedure includes the following steps:

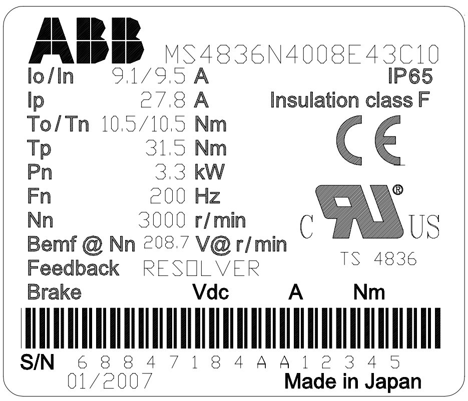
- entering the motor data and performing the motor identification run
- setting up the encoder/resolver communication
- checking the emergency stop and Safe Torque Off circuits
- setting up the voltage control
- setting the drive limits
- setting up the motor overtemperature protection
- tuning the speed controller
- setting up the fieldbus control.

If an alarm or a fault is generated during the start-up, see chapter *Fault tracing* for the possible causes and remedies. If problems continue, disconnect the main power and wait 5 minutes for the intermediate circuit capacitors to discharge and check the drive and motor connections.

Before you start, ensure you have the motor nameplate and encoder data (if needed) at your hand.

Safety		
	The start-up may only be carried out by a qualified electrician. The safety instructions must be followed during the start-up procedure. See the safety instructions on the first pages of the appropriate hardware manual.	
☐	Check the installation. See the installation checklist in the appropriate hardware manual.	
☐	Check that the starting of the motor does not cause any danger. **De-couple the driven machine** if - there is a risk of damage in case of an incorrect direction of rotation, or - a normal ID run (99.13 IDRUN MODE = (1) NORMAL) is required during the drive start-up, when the load torque is higher than 20% or the machinery is not able to withstand the nominal torque transient during the ID run.	
PC tool		
☐	Install the DriveStudio PC tool to the PC. For instruction, see *DriveStudio User Manual* [3AFE68749026 (English)].	
☐	Connect the drive to the PC: Connect the other end of the communication cable (OPCA-02, code: 68239745) to the panel link of the drive. Connect the other end of the communication cable via USB adapter or directly to the PC serial port.	
Power up		
☐	Switch the power on.	7-segment display: 
☐	Start the DriveStudio program by clicking the DriveStudio icon on the PC desktop.	 DriveStudio.exe
☐	Check whether an application program exists using the DriveSPC tool. If an application program already exists, NOTE that some of the drive functions may have been disabled. ENSURE, that the application program is suitable for your drive application.	
☐	Switch to local control to ensure that external control is disabled by clicking the Take/Release button of the PC tool control panel.	

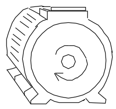
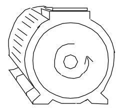
<table>
<tr><th colspan="3">Motor data entering</th></tr>
<tr><td>☐</td><td>Open the parameter and signal list by selecting the Parameter Browser of the appropriate drive.</td><td>Parameter Browser</td></tr>
<tr><td>☐</td><td>Select the language.
Parameters are set as follows:
Select the parameter group (in this case 99 START-UP DATA) by double-clicking it. Select the appropriate parameter by double-clicking it and set the new value.</td><td>99.01 LANGUAGE</td></tr>
<tr><td>☐</td><td>Select the motor type: asynchronous or permanent magnet motor.</td><td>99.04 MOTOR TYPE</td></tr>
<tr><td>☐</td><td>Select the motor control mode. DTC is suitable for most cases. For information on scalar control, see parameter 99.05 MOTOR CTRL MODE.</td><td>99.05 MOTOR CTRL MODE</td></tr>
<tr><td>☐</td><td>Enter the motor data from the motor nameplate.
Asynchronous motor nameplate example:


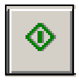
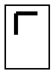
Permanent magnet motor nameplate example:


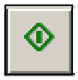
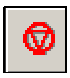
With DTC control (99.05 MOTOR CTRL MODE = (0) DTC) at least parameters 99.06…99.10 must be set. Better control accuracy can be achieved by setting also parameters 99.11…99.12.</td><td>Note: Set the motor data to exactly the same value as on the motor nameplate. For example, if the motor nominal speed is 1470 rpm on the nameplate, setting the value of parameter 99.09 MOT NOM SPEED to 1500 rpm results in wrong operation of the drive.</td></tr>
</table>

	- motor nominal current Allowed range: approximately 1/6 · I_{2n} … 2 · I_{2n} of the drive (0…2 · I_{2nd} if parameter 99.05 MOTOR CTRL MODE = (1) SCALAR). With multimotor drives, see section *Multimotor drives* on page 19.	99.06 MOT NOM CURRENT
	- motor nominal voltage Allowed range: 1/6 · U_N … 2 · U_N of the drive. (U_N refers to the highest voltage in each of the nominal voltage range, i.e. 480 V AC for ACSM1-04). With permanent magnet motors: The nominal voltage is the BackEMF voltage (at motor nominal speed). If the voltage is given as voltage per rpm, e.g. 60 V per 1000 rpm, the voltage for 3000 rpm nominal speed is 3 × 60 V = 180 V. Note that the nominal voltage is not equal to the equivalent DC motor voltage (E.D.C.M.) value given by some motor manufactures. The nominal voltage can be calculated by dividing the E.D.C.M. voltage by 1.7 (= square root of 3).	99.07 MOT NOM VOLTAGE
	- motor nominal frequency Range: 5…500 Hz. With multimotor drives, see section *Multimotor drives* on page 19. With permanent magnet motor: If the frequency is not given on the motor nameplate, it has to be calculated with the following formula: $f = n \times p / 60$ where p = number of pole pairs, n = motor nominal speed.	99.08 MOT NOM FREQ
	- motor nominal speed Range: 0…10000 rpm. With multimotor drives, see section *Multimotor drives* on page 19.	99.09 MOT NOM SPEED
	- motor nominal power Range: 0…10000 kW. With multimotor drives, see section *Multimotor drives* on page 19.	99.10 MOT NOM POWER
	- motor nominal cosφ (not applicable for permanent magnet motors). This value can be set for better DTC control accuracy. If value is not given by the motor manufacturer, use value 0 (i.e. default value). Range: 0…1.	99.11 MOT NOM COSFII
	- motor nominal shaft torque. This value can be set for better DTC control accuracy. If value is not given by the motor manufacturer, use value 0 (i.e. default value). Range: 0…2147483.647 Nm.	99.12 MOT NOM TORQUE
☐	After the motor parameters have been set, alarm ID-RUN is generated to inform that the ID run needs to be performed.	Alarm: ID-RUN

Multimotor drives **I.e. more than one motor is connected to one drive.**		
☐	Check that the motors have the same relative slip (only for asynchronous motors), nominal voltage and number of poles. If the manufacturer motor data is insufficient, use the following formulas to calculate the slip and the number of poles: $p = \mathrm{Int}\left(\frac{f_N \cdot 60}{n_N}\right)$ $n_s = \frac{f_N \cdot 60}{p}$ $s = \frac{n_S - n_N}{n_S} \cdot 100\%$ where p = number of pole pairs (= motor pole number / 2) f_N = motor nominal frequency [Hz] n_N = motor nominal speed [rpm] s = motor slip [%] n_S = motor synchronous speed [rpm].	
☐	Set the sum of the motor nominal currents.	99.06 MOT NOM CURRENT
☐	Set the nominal motor frequencies. Frequencies must be the same.	99.08 MOT NOM FREQ
☐	Set the sum of the motor nominal powers. If the motor powers are close to each other or the same but the nominal speeds vary slightly, parameter 99.09 MOT NOM SPEED can be set to an average value of the motor speeds.	99.10 MOT NOM POWER 99.09 MOT NOM SPEED
External mains choke		
☐	If the drive is equipped with an external choke (specified in *Hardware Manual)*, set parameter 95.02 EXTERNAL CHOKE to YES.	95.02 EXTERNAL CHOKE
Motor overtemperature protection (1)		
☐	Select how the drive reacts when motor overtemperature is detected.	45.01 MOT TEMP PROT
☐	Select the motor temperature protection: motor thermal model or motor temperature measurement. For motor temperature measurement connections, see section *Temperature sensors* on page 42.	45.02 MOT TEMP SOURCE
ID RUN (motor identification run)		
	WARNING! With Normal or Reduced ID run the motor will run at up to approximately 50…100% of the nominal speed during the ID run. ENSURE THAT IT IS SAFE TO RUN THE MOTOR BEFORE PERFORMING THE ID RUN!	

Note: Ensure that possible Safe Torque Off and emergency stop circuits are closed during the ID run.		
☐	Check the direction of rotation of the motor before starting the ID run. During the run (Normal or Reduced), the motor will rotate in the forward direction.	When drive output phases U2, V2 and W2 are connected to the corresponding motor terminals:  forward direction  reverse direction
☐	Select the motor identification method by parameter 99.13 IDRUN MODE. During the Motor ID run, the drive will identify the characteristics of the motor for optimum motor control. The ID run is performed at the next start of the drive. **Note:** The motor shaft must NOT be locked and the load torque must be < 10% during Normal or Reduced ID run. With permanent magnet motor this restriction applies also when Standstill ID run is selected. **Note:** Mechanical brake (if present) is not opened during the ID run. **Note:** The ID run cannot be performed if par. 99.05 MOTOR CTRL MODE = (1) SCALAR. NORMAL ID run should be selected whenever possible. **Note:** The driven machinery must be de-coupled from the motor with Normal ID run if • the load torque is higher than 20%, or • the machinery is not able to withstand the nominal torque transient during the ID run. The REDUCED ID run should be selected instead of the Normal ID run if the mechanical losses are higher than 20%, i.e. the motor cannot be de-coupled from the driven equipment, or full flux is required to keep the motor brake open (conical motor). The STANDSTILL ID run should be selected only if the Normal or Reduced ID run is not possible due to the restrictions caused by the connected mechanics (e.g. with lift or crane applications). AUTOPHASING can only be selected after the Normal/Reduced/ Standstill ID run has been performed once. Autophasing is used when an absolute encoder has been added/changed to a permanent magnet motor, but there is no need to perform the Normal/Reduced/ Standstill ID run again. See parameter 11.07 AUTOPHASING MODE on page 111 for information on autophasing modes, and section *Autophasing* on page 40.	99.13 IDRUN MODE 11.07 AUTOPHASING MODE

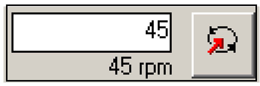
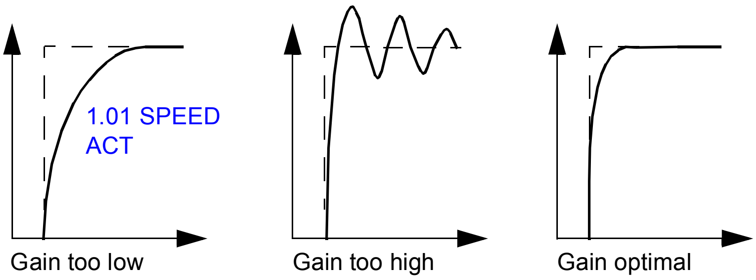
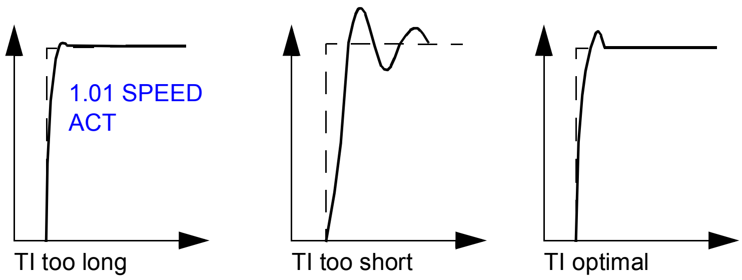
☐	Check the drive limits. The following must apply for all ID run methods: • 20.05 MAXIMUM CURRENT ≥ 99.06 MOT NOM CURRENT In addition, the following must apply for Reduced and Normal ID run: • 20.01 MAXIMUM SPEED > 55% of 99.09 MOT NOM SPEED • 20.02 MINIMUM SPEED ≤ 0 • supply voltage must be ≥ 65% of 99.07 MOT NOM VOLTAGE • 20.06 MAXIMUM TORQUE ≥ 100% (only for Normal ID run). When the ID run has been successfully completed, set the limit values as required by the application.	
☐	Start the motor to activate the ID run. **Note:** RUN ENABLE must be active. ID run is indicated by alarm ID-RUN and by a rotating display on the 7-segment display.	 10.09 RUN ENABLE Alarm: ID-RUN 7-segment display:  rotating display
☐	If the ID run is not successfully completed, fault ID-RUN FAULT is generated.	Fault ID-RUN FAULT
Speed measurement with encoder/resolver		
An encoder/resolver feedback can be used for more accurate motor control. Follow these instructions when encoder/resolver interface module FEN-xx is installed in drive option Slot 1 or 2. **Note:** Two encoder interface modules of the same type are not allowed.		
☐	Select the used encoder/resolver. For more information, see parameter group 90 ENC MODULE SEL on page 225.	90.01 ENCODER 1 SEL / 90.02 ENCODER 2 SEL
☐	Set other necessary encoder/resolver parameters: - Absolute encoder parameters (group 91, page 229) - Resolver parameters (group 92, page 234). - Pulse encoder parameters (group 93, page 235).	91.01…91.31 / 92.01…92.03 / 93.01…93.22
☐	Save new parameters settings into the permanent memory by setting parameter 16.07 PARAM SAVE to value (1) SAVE.	16.07 PARAM SAVE
☐	Set parameter 90.10 ENC PAR REFRESH to (1) CONFIGURE (or switch the drive power off and on again) so that the new parameter settings take effect.	90.10 ENC PAR REFRESH
Checking the encoder/resolver connection		
Follow these instructions when encoder/resolver interface module FEN-xx is installed in drive option Slot 1 or 2. **Note:** Two encoder interface modules of the same type are not allowed.		
☐	Set parameter 22.01 SPEED FB SEL to (0) ESTIMATED.	22.01 SPEED FB SEL

☐	Enter a small speed reference value (for example 3% of the nominal motor speed).	
☐	Start the motor.	
☐	Check that the estimated (1.14 SPEED ESTIMATED) and actual speed (1.08 ENCODER 1 SPEED / 1.10 ENCODER 2 SPEED) are equal. If the values differ, check the encoder/resolver parameter settings. **Hint:** If the actual speed (with absolute or pulse encoder) differs form the reference value by a factor of 2, check the pulse number setting (91.01 SINE COSINE NR / 93.01 ENC1 PULSE NR / 93.11 ENC2 PULSE NR).	1.14 SPEED ESTIMATED 1.08 ENCODER 1 SPEED / 1.10 ENCODER 2 SPEED
☐	If the direction of rotation is selected as forward, check that the actual speed (1.08 ENCODER 1 SPEED / 1.10 ENCODER 2 SPEED) is positive: • If the actual direction of the rotation is forward and the actual speed negative, the phasing of the pulse encoder wires is reversed. • If the actual direction of the rotation is reverse and the actual speed negative, the motor cables are incorrectly connected. Changing the connection: Disconnect the main power, and wait for 5 minutes for the intermediate circuit capacitors to discharge. Do the necessary changes. Switch the power on and start the motor again. Check that the estimated and actual speed values are correct. • If the direction of rotation is selected as reverse, the actual speed must be negative. **Note:** Resolver autotuning routines should always be performed after resolver cable connection has been modified. Autotuning routines can be activated by setting parameter 92.02 EXC SIGNAL AMPL or 92.03 EXC SIGNAL FREQ, and then setting parameter 90.10 ENC PAR REFRESH to (1) CONFIGURE. If the resolver is used with a permanent magnet motor, an AUTOPHASING ID run should be performed as well.	1.08 ENCODER 1 SPEED / 1.10 ENCODER 2 SPEED
☐	Stop the motor.	
☐	Set parameter 22.01 SPEED FB SEL to (1) ENC1 SPEED or (2) ENC2 SPEED. If the speed feedback cannot be used in motor control: In special applications parameter 40.06 FORCE OPEN LOOP must be set to TRUE.	22.01 SPEED FB SEL
☐	**Note:** Speed filtering needs to be adjusted especially when the encoder pulse number is small. See section *Speed filtering* on page 25.	

Emergency stop circuit		
☐	If there is an emergency stop circuit in use, check that the circuit functions (emergency stop signal is connected to the digital input which is selected as the source for the emergency stop activation).	10.10 EM STOP OFF3 or 10.11 EM STOP OFF1 (emergency stop control through fieldbus 2.12 FBA MAIN CW bits 2…4)
Safe Torque Off		
The Safe Torque Off function disables the control voltage of the power semiconductors of the drive output stage, thus preventing the inverter from generating the voltage required to rotate the motor. For Safe Torque Off wiring, see the appropriate hardware manual.		
☐	If there is a Safe Torque Off circuit in use, check that the circuit functions.	
☐	Selects how the drive reacts when the Safe Torque Off function is active (i.e. when the control voltage of the power semiconductors of the drive output stage is disabled).	46.07 STO DIAGNOSTIC
Voltage control		
If the DC voltage drops due to input power cut off, the undervoltage controller will automatically decrease the motor torque in order to keep the voltage above the lower limit. To prevent the DC voltage from exceeding the overvoltage control limit, the overvoltage controller automatically decreases the generating torque when the limit is reached. When the overvoltage controller is limiting the generating torque, quick deceleration of the motor is not possible. Thus electrical braking (brake chopper and brake resistor) is needed in some applications to allow the drive to dissipate regenerative energy. The chopper connects the brake resistor to the intermediate circuit of the drive whenever the DC voltage exceeds the maximum limit.		
☐	Check that the overvoltage and undervoltage controllers are active.	47.01 OVERVOLTAGE CTRL 47.02 UNDERVOLT CTRL
☐	If the application requires a brake resistor (the drive has a built-in brake chopper): • Set the brake chopper and resistor settings. **Note:** When a brake chopper and resistor are used, the overvoltage controller must be deactivated by parameter 47.01 OVERVOLTAGE CTRL. • Check that the connection functions. For more information on the brake resistor connection, see the appropriate hardware manual.	48.01…48.07 47.01 OVERVOLTAGE CTRL

Start function		
☐	Select the start function. Setting 11.01 START MODE to (2) AUTOMATIC selects a general-purpose start function. This setting also makes flying start (starting to a rotating motor) possible. The highest possible starting torque is achieved when 11.01 START MODE is set to (0) FAST (automatic optimised DC magnetising) or (1) CONST TIME (constant DC magnetising with user-defined magnetising time). **Note:** When 11.01 START MODE setting is (0) FAST or (1) CONST TIME, flying start (start to a rotating motor) is not possible.	11.01 START MODE
Limits		
☐	Set the operation limits according to the process requirements. **Note:** If load torque is suddenly lost when the drive is operating in torque control mode, the drive will rush to the defined negative or positive maximum speed. For safe operation, ensure the set limits are suitable for your application.	20.01…20.07
Motor overtemperature protection (2)		
☐	Set the alarm and fault limits for the motor overtemperature protection.	45.03 MOT TEMP ALM LIM 45.04 MOT TEMP FLT LIM
☐	Set the typical ambient temperature of the motor.	45.05 AMBIENT TEMP
☐	When 45.02 MOT TEMP SOURCE is set to (0) ESTIMATED, the motor thermal protection model must be configured as follows: - Set the maximum allowed operating load of the motor. - Set the zero speed load. A higher value can be used if the motor has an external motor fan to boost the cooling. - Set the break point frequency of the motor load curve. - Set the motor nominal temperature rise. - Set the time inside which the temperature has reached 63% of the nominal temperature.	45.06 MOT LOAD CURVE 45.07 ZERO SPEED LOAD 45.08 BREAK POINT 45.09 MOTNOMTEMPRISE 45.10 MOT THERM TIME
☐	If possible, perform the motor ID run again at this point (see page 19).	99.13 IDRUN MODE

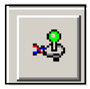
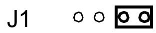
Speed filtering		
The measured speed always has a small ripple because of electrical and mechanical interferences, couplings and encoder resolution (i.e. small pulse number). A small ripple is acceptable as long as it does not affect the speed control chain. The interferences in the speed measurement can be filtered with a speed error filter or with an actual speed filter. Reducing the ripple with filters may cause speed controller tuning problems. A long filter time constant and fast acceleration time contradict one another. A very long filter time results in unstable control.		
☐	If the used speed reference changes rapidly (servo application), use the speed error filter to filter the possible interferences in the speed measurement. In this case the speed error filter is more suitable than the actual speed filter: - Set the filter time constant.	26.06 SPD ERR FTIME
☐	If the used speed reference remains constant, use the actual speed filter to filter the possible interferences in the speed measurement. In this case the actual speed filter is more suitable than the speed error filter: - Set the filter time constant. If there are substantial interferences in the speed measurement, the filter time constant should be proportional to the total inertia of the load and motor, i.e. approximately 10…30% of the mechanical time constant $t_{mech} = (n_{nom} / T_{nom}) \times J_{tot} \times 2\pi / 60$, where J_{tot} = total inertia of the load and motor (the gear ratio between the load and the motor must be taken into account) n_{nom} = motor nominal speed T_{nom} = motor nominal torque	22.02 SPEED ACT FTIME
Manual speed controller tuning		
☐	Select the following signals to be monitored with the DriveStudio Data Logger or Monitoring Window: - 1.01 SPEED ACT, filtered actual speed - 1.06 TORQUE, motor torque.	
☐	Start the motor with a small speed reference.	
Give a speed reference step and monitor the response. Repeat the test for few speed reference steps across the whole speed range:		
☐	Set the speed ramp time to a suitable value (according to the used application).	25.03 ACC TIME
☐	Set a suitable speed step (according to the used application): 10% or 20% of the maximum speed of the drive. Accept the new value by pressing the Set new reference button.	

☐	Optimise the P-part of the speed controller: Set the integration time to 0 to change the PI (proportional integral) controller into a P controller:	28.03 INTEGRATION TIME
☐	Give a step change up, for example 10% (of the maximum speed of the drive). When the speed is stabilised, give a step change down, for example -10% (of the maximum speed of the drive).	
☐	Increase the proportional gain until the response is sufficient: 	28.02 PROPORT GAIN
☐	Reduce the integration time (TI) until an overshoot is observed in the response. Adjust the integration time so that there is no overshoot or only a slight overshoot (depending on the drive application). The integral part is used to correct the error between the reference and actual value (which is caused by the proportional control) as quickly as possible. If the drive is stable and allows a high proportional gain, an overcompensated step response is obtained if the integration time is set short. 	28.03 INTEGRATION TIME
☐	Acceleration (deceleration) compensation can be used to improve the speed control dynamic reference change (when the speed ramp times > 0). In order to compensate inertia during acceleration, a derivative of the speed reference is added to the output of the speed controller. Set the derivation time for acceleration (deceleration) compensation. The value should be proportional to the total inertia of the load and motor, i.e. approximately 10…30% of the mechanical time constant (t_{mech}). See the mechanical time constant equation in section *Speed filtering* on page 25.	26.08 ACC COMP DERTIME

Fieldbus control		
Follow these instructions when the drive is controlled from a fieldbus control system via fieldbus adapter Fxxx. The adapter is installed in drive Slot 3.		
☐	Enable the communication between the drive and fieldbus adapter.	50.01 FBA ENABLE
☐	Connect the fieldbus control system to the fieldbus adapter module.	
☐	Set the communication and adapter module parameters: See section *Setting up communication through a fieldbus adapter module* on page 386.	
☐	Test that the communication functions.	

How to control the drive through the I/O interface

The table below instructs how to operate the drive through the digital and analogue inputs, when the default parameter settings are valid.

PRELIMINARY SETTINGS	
Ensure the original parameter settings (default) are valid.	16.04 PARAM RESTORE
Ensure the control connections are wired according to the connection diagram given in chapter *Default connections of the control unit.*	
Switch to external control by clicking the Take/Release button of the PC tool control panel.	
STARTING AND CONTROLLING THE SPEED OF THE MOTOR	
Start the drive by switching digital input DI1 on. Digital input status can be monitored with signal 2.01 DI STATUS.	2.01 DI STATUS
Check that analogue input AI1 is used as a voltage input (selected by jumper J1).	Voltage: 
Regulate the speed by adjusting the voltage of analogue input AI1.	
Check analogue input AI1 signal scaling. AI1 values can be monitored with signals 2.04 AI1 and 2.05 AI1 SCALED. When AI1 is used as a voltage input, the input is differential and the negative value corresponds to the negative speed and the positive value to the positive speed.	13.02…13.04 2.04 AI1 2.05 AI1 SCALED
STOPPING THE MOTOR	
Stop the drive by switching digital input DI1 off.	2.01 DI STATUS

Drive programming using PC tools

What this chapter contains

This chapter introduces the drive programming using the DriveStudio and DriveSPC applications. For more information, see *DriveStudio User Manual* [3AFE68749026 (English)] and *DriveSPC User Manual* [3AFE68836590 (English)].

General

The drive control program is divided into two parts:

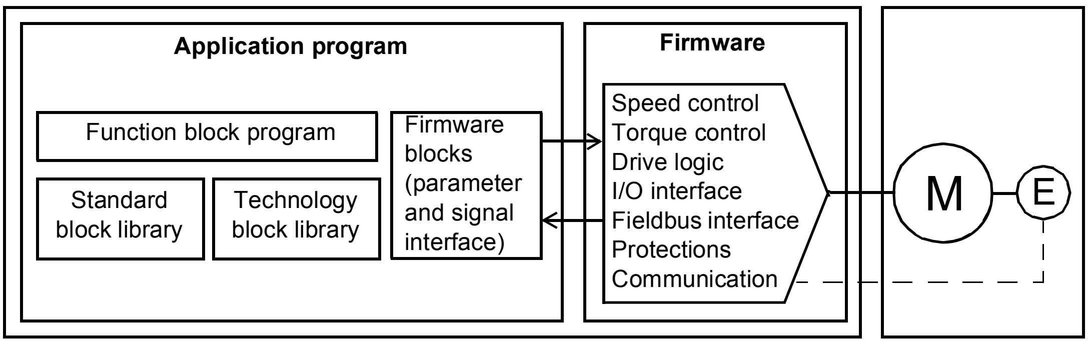
- firmware program
- application program.

The firmware program performs the main control functions, including speed and torque control, drive logic (start/stop), I/O, feedback, communication and protection functions. Firmware functions are configured and programmed with parameters. The functions of the firmware program can be extended with application programming. Application programs are built out of function blocks.

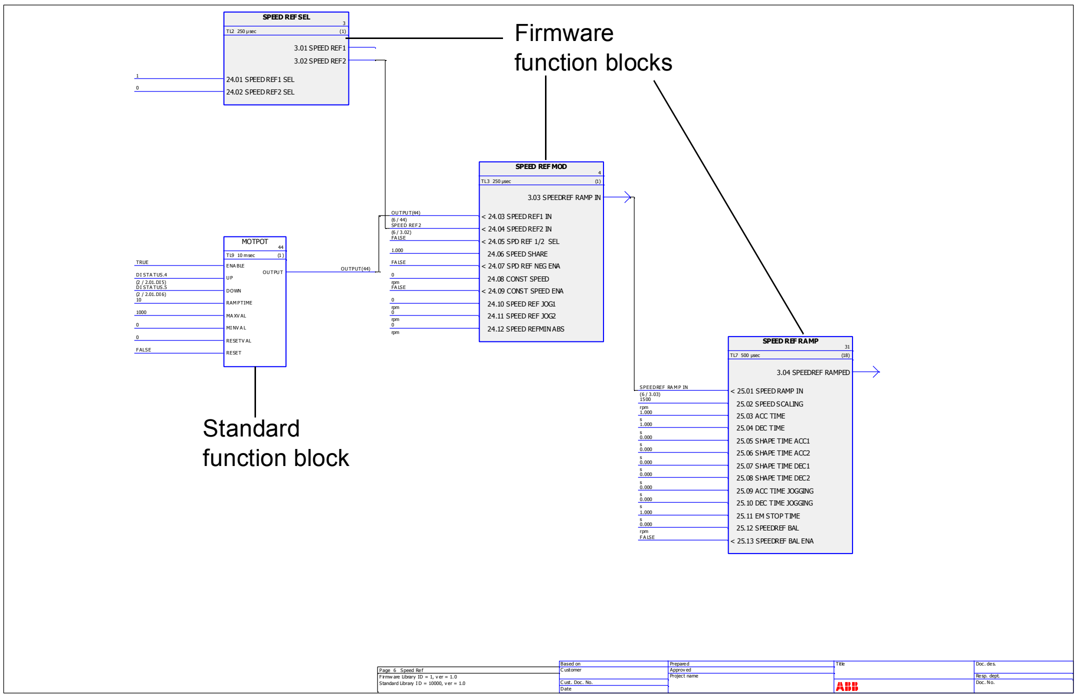
The drive supports two different programming methods:

- parameter programming
- application programming with function blocks (the blocks are based on the IEC-61131 standard).

Drive control program



The following picture presents a view from DriveSPC.



The application program template visible through DriveSPC is presented in chapter *Application program template* (page 363).

Programming via parameters

Parameters can be set via DriveStudio, drive control panel (keypad) or the fieldbus interface. All parameter settings are stored automatically to the permanent memory of the drive. (Exception: Parameters set via the fieldbus interface must be saved by par. 16.07 PARAM SAVE). Values are restored after the power switch-off. Default values can be restored by a parameter (16.04 PARAM RESTORE).

Because most parameters are used as firmware function block inputs, parameter values can also be modified via the DriveSPC tool.

Application programming

Application programs are created with the DriveSPC PC tool.

The normal delivery of the drive does not include an application program. The user can create an application program with the standard and firmware function blocks. ABB also offers customised application programs and technology function blocks for specific applications. For more information, contact your local ABB representative.

Function blocks

The application program uses three types of function blocks: firmware function blocks, standard function blocks and technology function blocks.

Firmware function blocks

Most of the firmware functions are represented as function blocks in the DriveSPC tool. Firmware function blocks are part of the drive control firmware, and used as an interface between the application and firmware programs. Drive parameters in groups 10…99 are used as function block inputs and parameters in groups 1…9 as function block outputs. Firmware function blocks are presented in chapter *Parameters and firmware blocks*.

Standard function blocks (library)

Standard function blocks (e.g. ADD, AND) are used to create an executable application program. Blocks are based on the IEC-61131 standard. Standard function blocks are presented in chapter *Standard function blocks*.

Standard function block library is always included in the drive delivery.

Technology function blocks

Several technology function block libraries are available for different types of applications. One technology library can be used at a time. Technology blocks are used in a similar way as the standard blocks.

Program execution

The application program is loaded to the permanent memory (non-volatile) of the memory unit (JMU). The execution of the downloaded program starts after the next reset of the drive control board. The program is executed in real time on the same Central Processing Unit (CPU of the drive control board) as the drive firmware. The program is executed with two cyclical tasks. The time level for these tasks can be defined by the programmer (≥ 1ms).

Note: Because the firmware and application programs use the same CPU, the programmer must ensure that the drive CPU is not overloaded. See parameter 1.21 CPU USAGE.

Operation modes

The DriveSPC tool offers the following operation modes:

Off-line

When the off-line mode is used without a drive connection, the user can

- open a application program file (if exists).
- modify and save the application program.
- print the program pages.

When the off-line mode is used with a drive(s) connection, the user can

- connect the selected drive to DriveSPC.
- upload a application program from the connected drive (an empty template which includes only the firmware blocks is available as default.)
- download the configured application program to the drive and start the program execution. The downloaded program contains the function block program and the parameter values set in DriveSPC.
- remove the program from the connected drive.

On-line

In the on-line mode, the user can

- modify firmware parameters (changes are stored directly to the drive memory).
- modify application program parameters (i.e. parameters created in DriveSPC).
- monitor the actual values of all function blocks in real time.

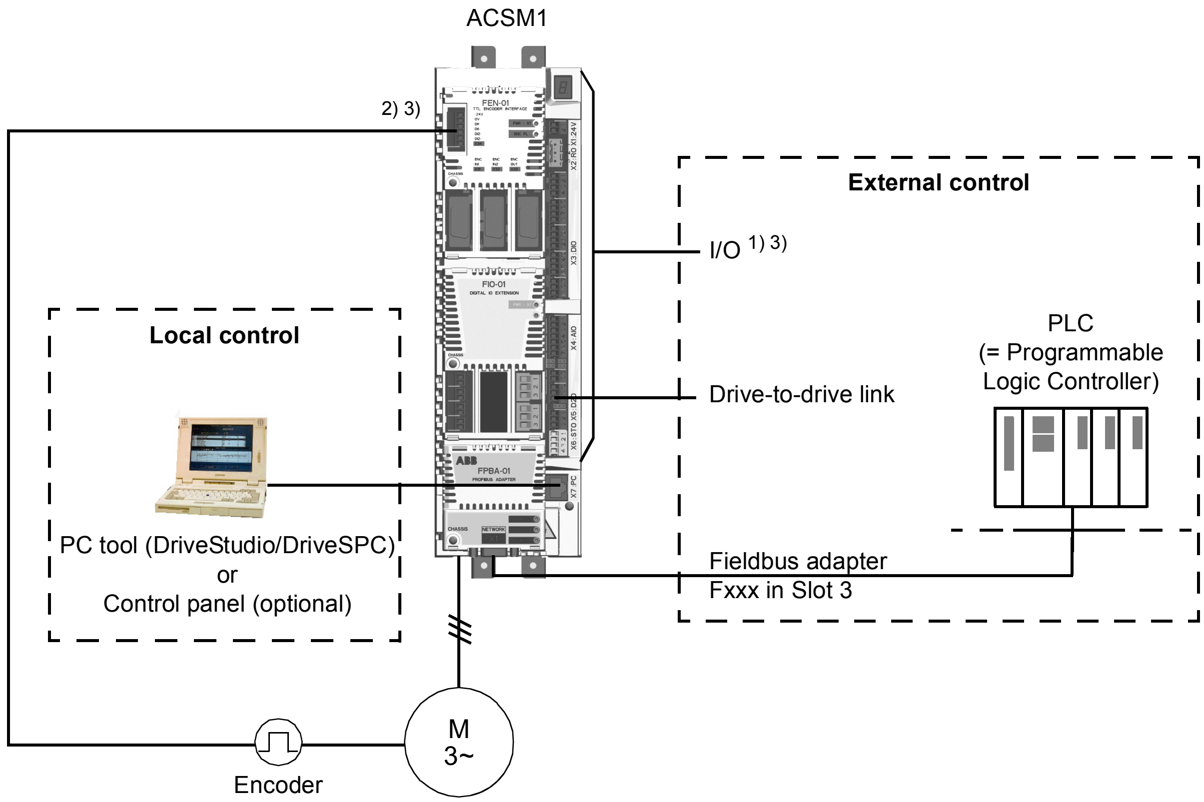
Drive control and features

What this chapter contains

This chapter describes the control locations and operation modes of the drive, and the features of the application program.

Local control vs. external control

The drive has two main control locations: external and local. The control location is selected with the PC tool (Take/Release button) or with the LOC/REM key on the control panel.



1) Extra inputs/outputs can be added by installing optional I/O extension modules (FIO-xx) in drive Slot 1/2.
2) Incremental or absolute encoder, or resolver interface module (FEN-xx) installed in drive Slot 1/2
3) Two encoder/resolver interface modules or two I/O extension modules of the same type are not allowed.

Local control

The control commands are given from a PC equipped with DriveStudio and/or DriveSPC, or from the control panel keypad when the drive is in local control. Speed and torque control modes are available for local control.

Local control is mainly used during commissioning and maintenance. The control panel always overrides the external control signal sources when used in local control. Changing the control location to local can be disabled by parameter 16.01 LOCAL LOCK.

The user can select by a parameter (46.03 LOCAL CTRL LOSS) how the drive reacts to a control panel or PC tool communication break.

External control

When the drive is in external control, control commands (start/stop and reference) are given through the fieldbus interface (via an optional fieldbus adapter module), the I/O terminals (digital and analogue inputs), optional I/O extension modules or the drive-to-drive link. External references are given through the fieldbus interface, analogue inputs, drive to drive link and encoder inputs.

Two external control locations, EXT1 and EXT2, are available. The user can select control signals (e.g. start/stop and reference) and control modes for both external control locations. Depending on the user selection, either EXT1 or EXT2 is active at a time. Selection between EXT1/EXT2 is done via digital inputs or fieldbus control word.

Operating modes of the drive

The drive can operate in speed and torque control modes as well as position, synchron, homing and profile velocity modes. Block diagrams of the control chain for speed and torque control as well as positioning are presented on page 38; more detailed diagrams are presented in chapter *Control chain block diagrams* (page 379).

Speed control mode

Motor rotates at a speed proportional to the speed reference given to the drive. This mode can be used either with estimated speed used as feedback, or with an encoder or resolver for better speed accuracy.

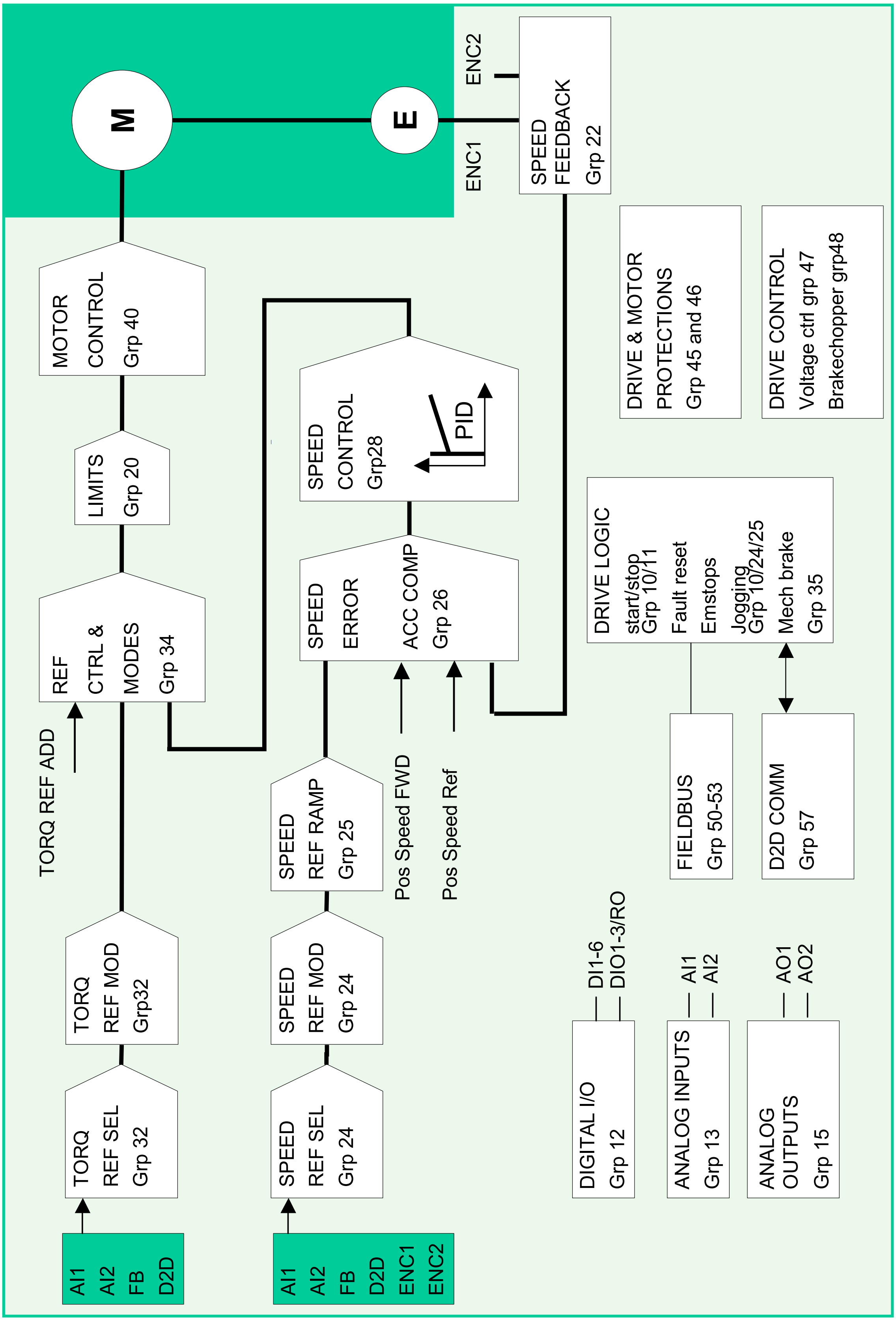
Speed control mode is available in both local and external control.

Torque control mode

Motor torque is proportional to the torque reference given to the drive. This mode can be used either with estimated speed used as feedback, or with an encoder or resolver for more accurate and dynamic motor control.

Torque control mode is available in both local and external control.

Drive control chain for speed and torque control



Position control

In position control, the load is positioned along a single axis from the start position to the defined target position. A position reference is given to the drive to indicate the target position. The path to the target position is calculated by the position profile generator, controlled by position reference sets.

Position feedback (encoder or resolver) must always be used in position control to determine the actual position of the load. The same encoder can also be used to provide speed feedback. It is also possible to have separate encoders for the load (position feedback) and motor sides (speed feedback).

Note: It is emphasised that all position relevant parameters are load side related, e.g. the setting of parameter 70.04 POS SPEED LIM (dynamic limiter speed limitation) of 300 rpm denotes that, with a load gear ratio of 1:10, the motor can run at up to 3000 rpm.

If the actual position exceeds the defined minimum and maximum position limits, fault POSITION ERROR MAX / POSITION ERROR MIN is created. The actual position monitoring is active in position, synchron, homing and profile velocity modes if the drive is enabled.

Synchron control

Synchron control is used to synchronise several mechanical systems (axes). The control is similar to position control, but in synchron control the position reference is taken from a moving target via an encoder, the master drive, the PLC, or from the virtual master function.

Position feedback (encoder or resolver) must always be used in synchron control to determine the actual position of the load.

Mechanical slippage etc. can be compensated using the cyclic correction functions.

Note: Synchron control is not available in local control mode.

Virtual master function

With the virtual master function, a physical master drive or PLC is not needed in a synchron-controlled follower. The follower will generate its own synchron reference by converting the speed reference selected by parameter 67.02 SPEED REF VIRT M into a position reference by integration.

Homing control

Homing establishes a correspondence between the actual position of the driven machinery and the drive internal zero position.

An encoder must always be used in homing control.

See section *Position correction* on page 56.

Note: Homing control is not available in local control mode.

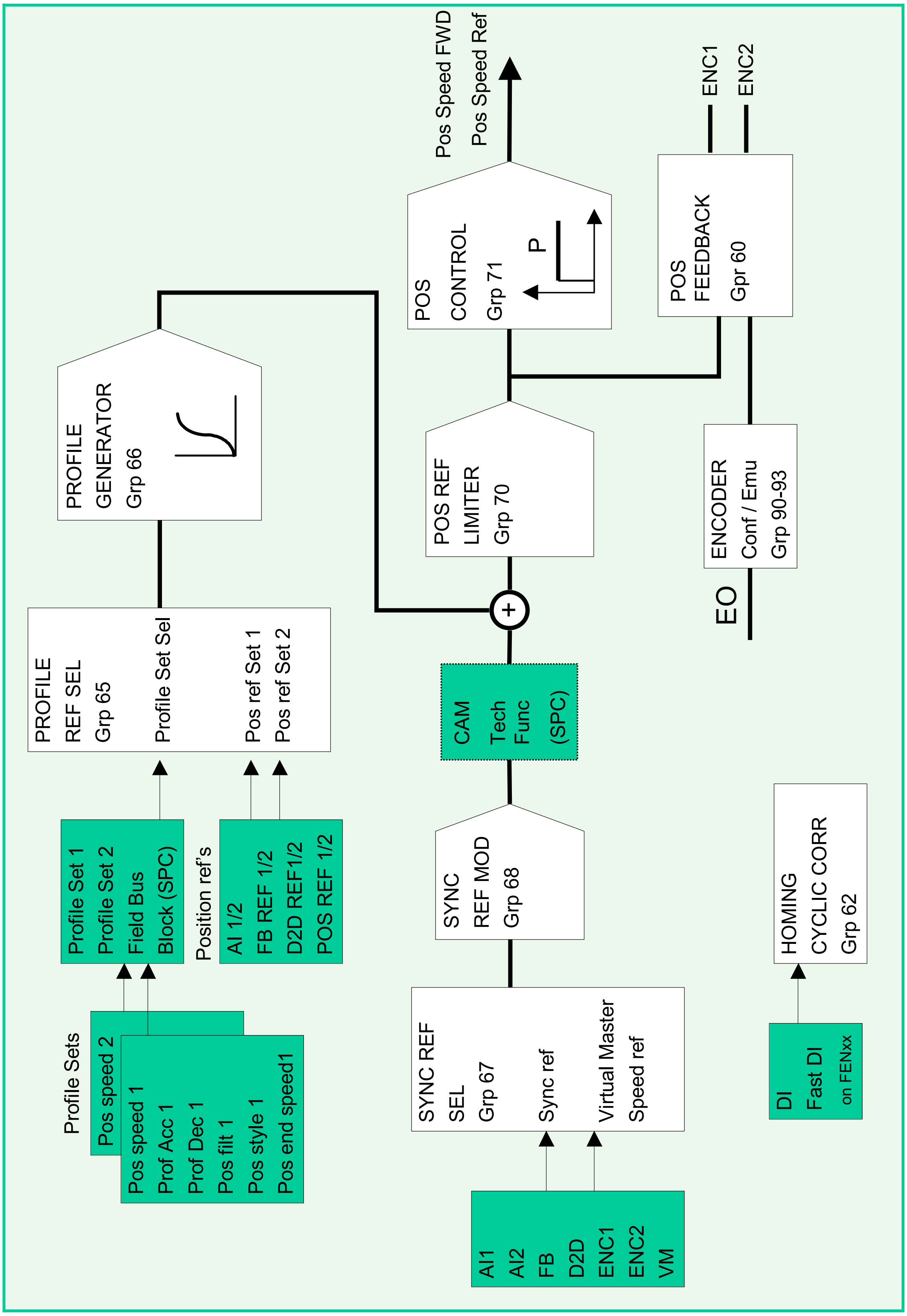
Profile velocity control

In profile velocity control, the motor rotates at a speed proportional to the speed reference given to the drive. The reference is given in position scale units (e.g. m/s) and handled by the position control reference chain (instead of the speed reference chain).

Profile velocity control is used e.g. with CANopen profile.

Note: Profile velocity control is not available in local control mode.

Drive control chain for positioning



Special control modes

In addition to the above-mentioned control modes, the following special control modes are available:

- Emergency Stop modes OFF1 and OFF3: Drive stops along the defined deceleration ramp and drive modulation stops.
- Jogging mode: Drive starts and accelerates to the defined speed when the jogging signal is activated.

For more information, see parameter group 10 START/STOP on page 103.

Motor control features

Scalar motor control

It is possible to select scalar control as the motor control method instead of Direct Torque Control (DTC). In scalar control mode, the drive is controlled with a frequency reference. However, the outstanding performance of DTC is not achieved in scalar control.

It is recommended to activate the scalar motor control mode in the following situations:

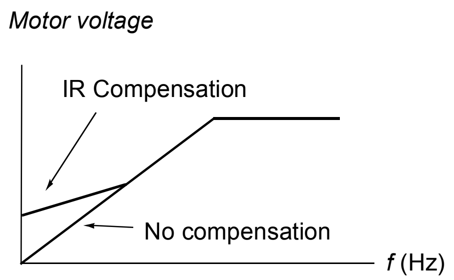
- In multimotor drives: 1) if the load is not equally shared between the motors, 2) if the motors are of different sizes, or 3) if the motors are going to be changed after motor identification (ID run)
- If the nominal current of the motor is less than 1/6 of the nominal output current of the drive
- If the drive is used without a motor connected (for example, for test purposes)
- If the drive runs a medium-voltage motor through a step-up transformer.

In scalar control, some standard features are not available.

IR compensation for a scalar controlled drive

IR compensation is active only when the motor control mode is scalar. When IR compensation is activated, the drive gives an extra voltage boost to the motor at low speeds. IR compensation is useful in applications that require a high break-away torque.

In direct torque control (DTC) mode, IR compensation is automatic and manual adjustment is not needed.



Autophasing

Autophasing is an automatic measurement routine to determine the angular position of the magnetic flux of a permanent magnet synchronous motor. The motor control requires the absolute position of the rotor flux in order to control motor torque accurately.

Autophasing is applicable to permanent magnet synchronous motors in these cases:

1. One-time measurement of the rotor and encoder position difference when an absolute encoder or resolver (one pole pair) is used
2. With open-loop motor control, repetitive measurement of the rotor position at every start.

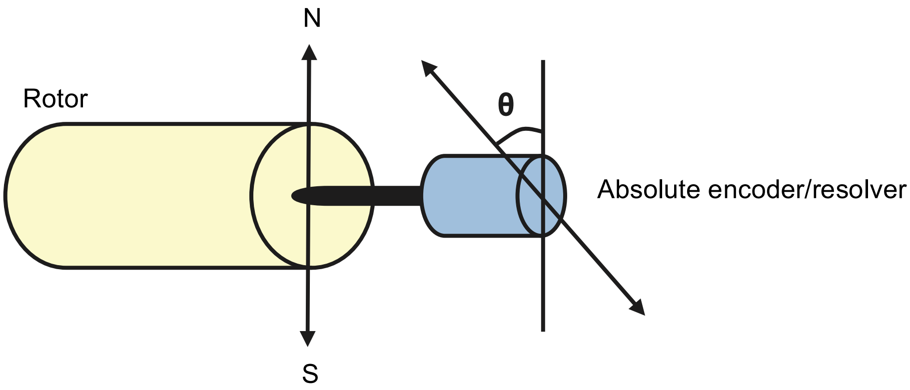
Several autophasing modes are available (see parameter 11.07 AUTOPHASING MODE).

The turning mode is recommended especially with case 1 as it is the most robust and accurate method. In turning mode, the motor shaft is turned back and forward

(±360/polepairs)° in order to determine the rotor position. In case 2 (open-loop control), the shaft is turned only in one direction and the angle is smaller.

The standstill modes can be used if the motor cannot be turned (for example, when the load is connected). As the characteristics of motors and loads differ, testing must be done to find out the most suitable standstill mode.

The drive is also capable of determining the rotor position when started to a running motor in open-loop or closed-loop modes. In this situation, the setting of 11.07 AUTOPHASING MODE has no effect.



Thermal motor protection

With the parameters in group 45 MOT THERM PROT, the user can set up the motor overtemperature protection and configure motor temperature measurement (if present). This block also shows the estimated and measured motor temperature.

The motor can be protected against overheating by

- the motor thermal protection model
- measuring the motor temperature with PTC or KTY84 sensors. This will result in a more accurate motor model.

Thermal motor protection model

The drive calculates the temperature of the motor on the basis of the following assumptions:

1) When power is applied to the drive for the first time, the motor is at ambient temperature (defined by parameter 45.05 AMBIENT TEMP). After this, when power is applied to the drive, the motor is assumed to be at the estimated temperature (value of 1.18 MOTOR TEMP EST, saved at power switch-off).

2) Motor temperature is calculated using the user-adjustable motor thermal time and motor load curve. The load curve should be adjusted in case the ambient temperature exceeds 30 °C.

It is possible to adjust the motor temperature supervision limits and select how the drive reacts when overtemperature is detected.

Note: The motor thermal model can be used when only one motor is connected to the inverter.

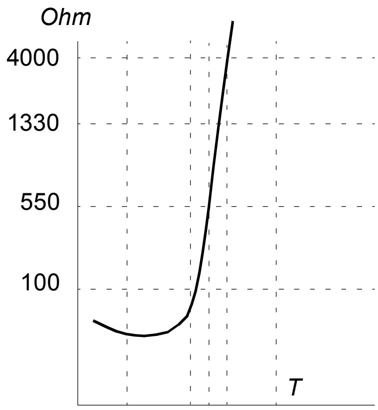
Temperature sensors

It is possible to detect motor overtemperature by connecting a motor temperature sensor to thermistor input TH of the drive or to optional encoder interface module FEN-xx.

Constant current is fed through the sensor. The resistance of the sensor increases as the motor temperature rises over the sensor reference temperature T_{ref}, as does the voltage over the resistor. The temperature measurement function reads the voltage and converts it into ohms.

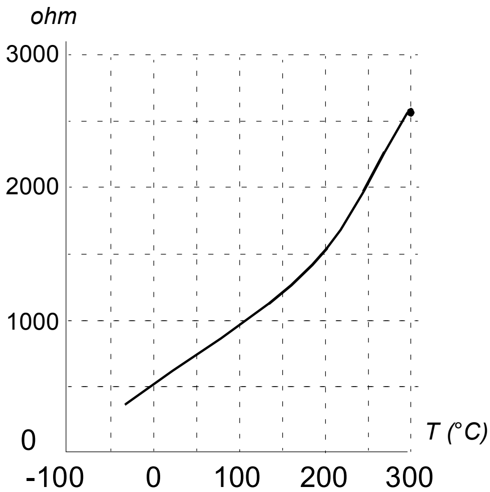
The figure below shows typical PTC sensor resistance values as a function of the motor operating temperature.

Temperature	PTC resistance
Normal	0…1.5 kohm
Excessive	≥ 4 kohm



The figure below shows typical KTY84 sensor resistance values as a function of the motor operating temperature.

KTY84 scaling
90 °C = 936 ohm
110 °C = 1063 ohm
130 °C = 1197 ohm
150 °C = 1340 ohm

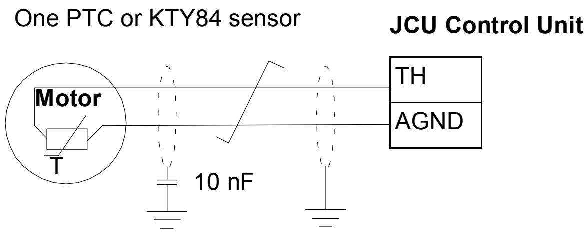


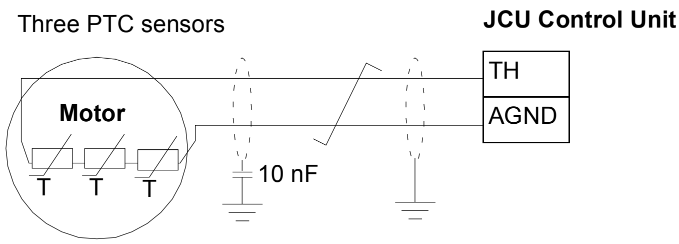
It is possible to adjust the motor temperature supervision limits and select how the drive reacts when overtemperature is detected.

WARNING! As the thermistor input on the JCU Control Unit is not insulated according to IEC 60664, the connection of the motor temperature sensor requires double or reinforced insulation between motor live parts and the sensor. If the assembly does not fulfil the requirement,
- the I/O board terminals must be protected against contact and must not be

connected to other equipment
or
- the temperature sensor must be isolated from the I/O terminals.

The figure below shows a motor temperature measurement when thermistor input TH is used.





For encoder interface module FEN-xx connection, see the *User's Manual* of the appropriate encoder interface module.

DC voltage control features

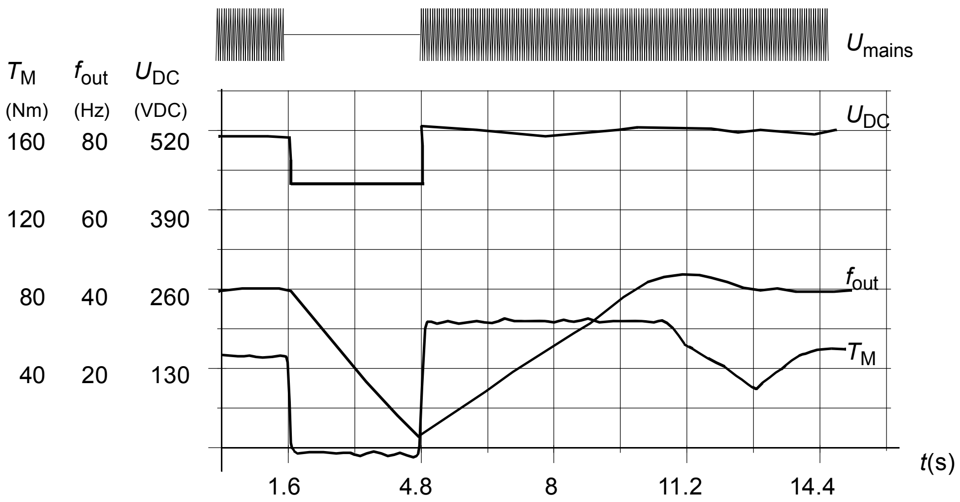
Overvoltage control

Overvoltage control of the intermediate DC link is needed with two-quadrant line-side converters when the motor operates within the generating quadrant. To prevent the DC voltage from exceeding the overvoltage control limit, the overvoltage controller automatically decreases the generating torque when the limit is reached.

Undervoltage control

If the incoming supply voltage is cut off, the drive will continue to operate by utilising the kinetic energy of the rotating motor. The drive will be fully operational as long as the motor rotates and generates energy to the drive. The drive can continue the operation after the break if the main contactor remained closed.

Note: Units equipped with main contactor option must be equipped with a hold circuit (e.g. UPS) which keeps the contactor control circuit closed during a short supply break.



U_{DC}= intermediate circuit voltage of the drive, f_{out} = output frequency of the drive,
T_M = motor torque

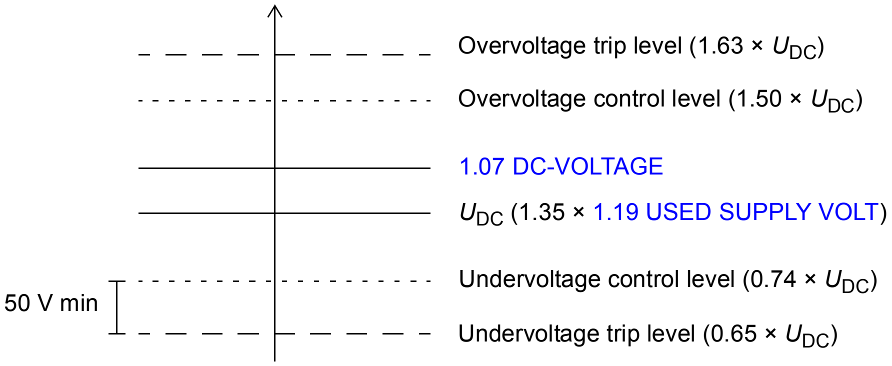
Loss of supply voltage at nominal load (f_{out} = 40 Hz). The intermediate circuit DC voltage drops to the minimum limit. The controller keeps the voltage steady as long as the mains is switched off. The drive runs the motor in generator mode. The motor speed falls but the drive is operational as long as the motor has enough kinetic energy.

Voltage control and trip limits

The control and trip limits of the intermediate DC voltage regulator are relative either to a supply voltage value provided by the user or to an automatically-determined supply voltage. The actual voltage used is shown by parameter 1.19 USED SUPPLY VOLT. The DC voltage (U_{DC}) equals 1.35 times this value.

Automatic identification of the supply voltage is performed every time the drive is powered. Automatic identification can be disabled by parameter 47.03

SUPPLVOLTAUTO-ID; the user can define the voltage manually at parameter 47.04 SUPPLY VOLTAGE.



The intermediate DC circuit is charged over an internal resistor which is bypassed when the correct level (80% of UDC) is reached and voltage is stabilised.

Braking chopper

The built-in braking chopper of the drive can be used to handle the energy generated by a decelerating motor.

When the braking chopper is enabled and a resistor connected, the chopper will start conducting when the DC link voltage of the drive reaches 780 V. The maximum braking power is achieved at 840 V.

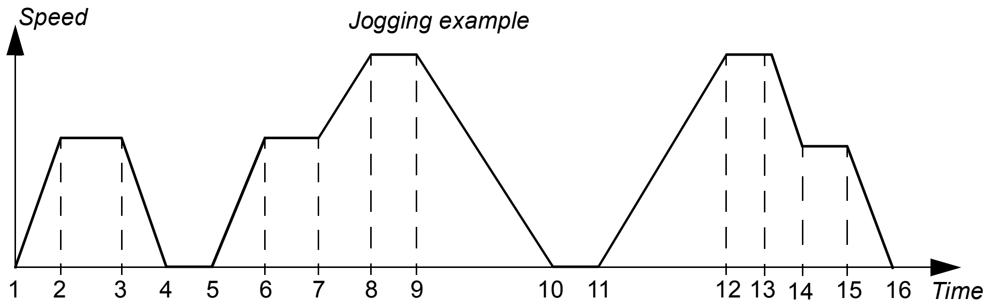
Speed control features

Jogging

Two jogging functions (1 or 2) are available. When a jogging function is activated, the drive starts and accelerates to the defined jogging speed along the defined jogging acceleration ramp. When the function is deactivated, the drive decelerates to a stop along the defined jogging deceleration ramp. One push button can be used to start and stop the drive during jogging. The jogging function is typically used during servicing or commissioning to control the machinery locally.

Jogging functions 1 and 2 are activated by a parameter or through fieldbus. For activation through fieldbus, see 2.12 FBA MAIN CW.

The figure and table below describe the operation of the drive during jogging. (Note that they cannot be directly applied to jogging commands through fieldbus as those require no enable signal; see parameter 10.15 JOG ENABLE.) They also represent how the drive shifts to normal operation (= jogging inactive) when the drive start command is switched on. Jog cmd = State of the jogging input; Jog enable = Jogging enabled by the source set by parameter 10.15 JOG ENABLE; Start cmd = State of the drive start command.



Phase	Jog cmd	Jog enable	Start cmd	Description
1-2	1	1	0	Drive accelerates to the jogging speed along the acceleration ramp of the jogging function.
2-3	1	1	0	Drive runs at the jogging speed.
3-4	0	1	0	Drive decelerates to zero speed along the deceleration ramp of the jogging function.
4-5	0	1	0	Drive is stopped.
5-6	1	1	0	Drive accelerates to the jogging speed along the acceleration ramp of the jogging function.
6-7	1	1	0	Drive runs at the jogging speed.
7-8	x	0	1	Jog enable is not active; normal operation continues.
8-9	x	0	1	Normal operation overrides the jogging. Drive follows the speed reference.
9-10	x	0	0	Drive decelerates to zero speed along the active deceleration ramp.
10-11	x	0	0	Drive is stopped.
11-12	x	0	1	Normal operation overrides the jogging. Drive accelerates to the speed reference along the active acceleration ramp.
12-13	1	1	1	Start command overrides the jog enable signal.
13-14	1	1	0	Drive decelerates to the jogging speed along the deceleration ramp of the jogging function.
14-15	1	1	0	Drive runs at the jogging speed.
15-16	x	0	0	Drive decelerates to zero speed along the deceleration ramp of the jogging function.

Notes:

- Jogging is not operational when the drive start command is on, or when the drive is in local control.
- Normal start is inhibited when jog enable is active.
- The ramp shape time is set to zero during jogging.

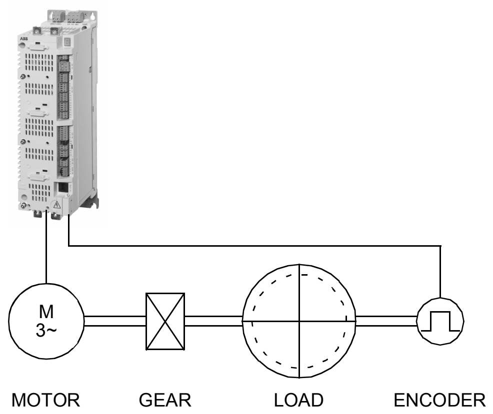
Motor feedback features

Motor encoder gear function

The drive provides motor encoder gear function for compensating of mechanical gears between the motor shaft, the encoder and the load.

Motor encoder gear application example:

Speed control uses the motor speed. If no encoder is mounted on the motor shaft, the motor encoder gear function must be applied in order to calculate the actual motor speed on the basis of the measured load speed.



The motor encoder gear parameters 22.03 MOTOR GEAR MUL and 22.04 MOTOR GEAR DIV are set as follows:

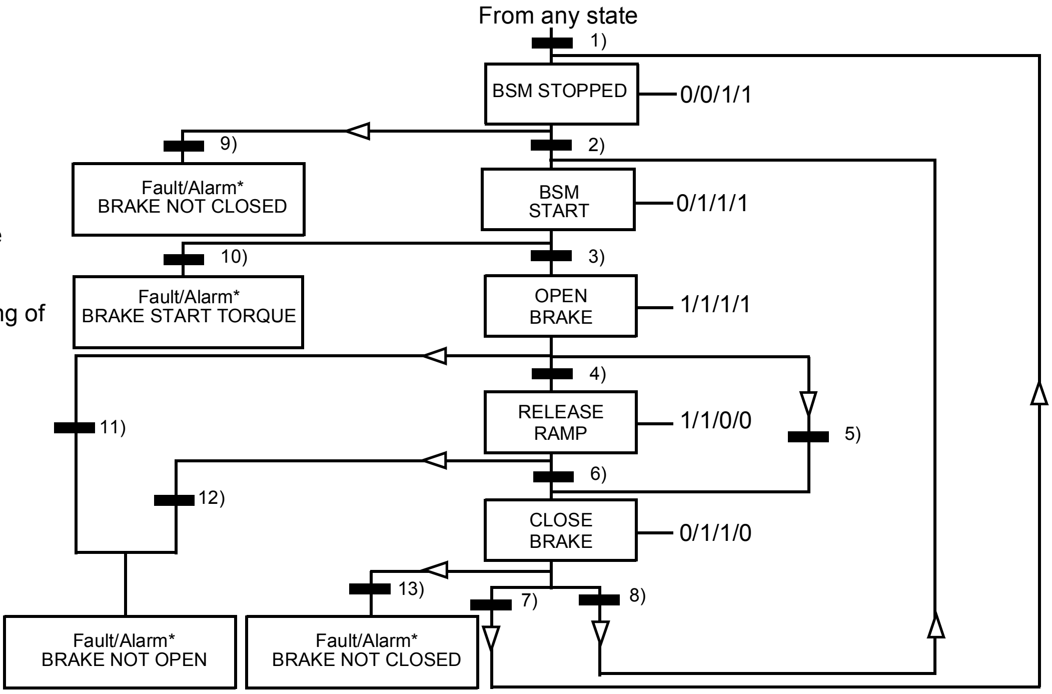
$$\frac{\text{22.03 MOTOR GEAR MUL}}{\text{22.04 MOTOR GEAR DIV}} = \frac{\text{Actual speed}}{\text{Encoder 1/2 speed or Estimated speed}}$$

Note: If the motor gear ratio differs from 1, the motor model uses an estimated speed instead of the speed feedback value.

Mechanical brake

The program supports the use of a mechanical brake to hold the motor and load at zero speed when the drive is stopped or not powered. The brake control is configured by the parameters in 35 MECH BRAKE CTRL (page 163).

Brake state diagram



BSM = Brake State Machine

* Depends on setting of par. 35.09 BRAKE FAULT FUNC.

State (Symbol NN —W/X/Y/Z)

- NN: State name
- W/X/Y/Z: State outputs/operations

W: 1 = Brake open command is active. 0 = Brake close command is active. (Controlled through selected digital/relay output with signal 3.15 BRAKE COMMAND.)

X: 1 = Forced start (inverter is modulating). The function keeps the internal Start on until the brake is closed in spite of the status of the external Stop. Effective only when ramp stop has been selected as the stop mode (11.03 STOP MODE). Run enable and faults override the forced start. 0 = No forced start (normal operation).

Y: 1 = Drive control mode is forced to speed/scalar.

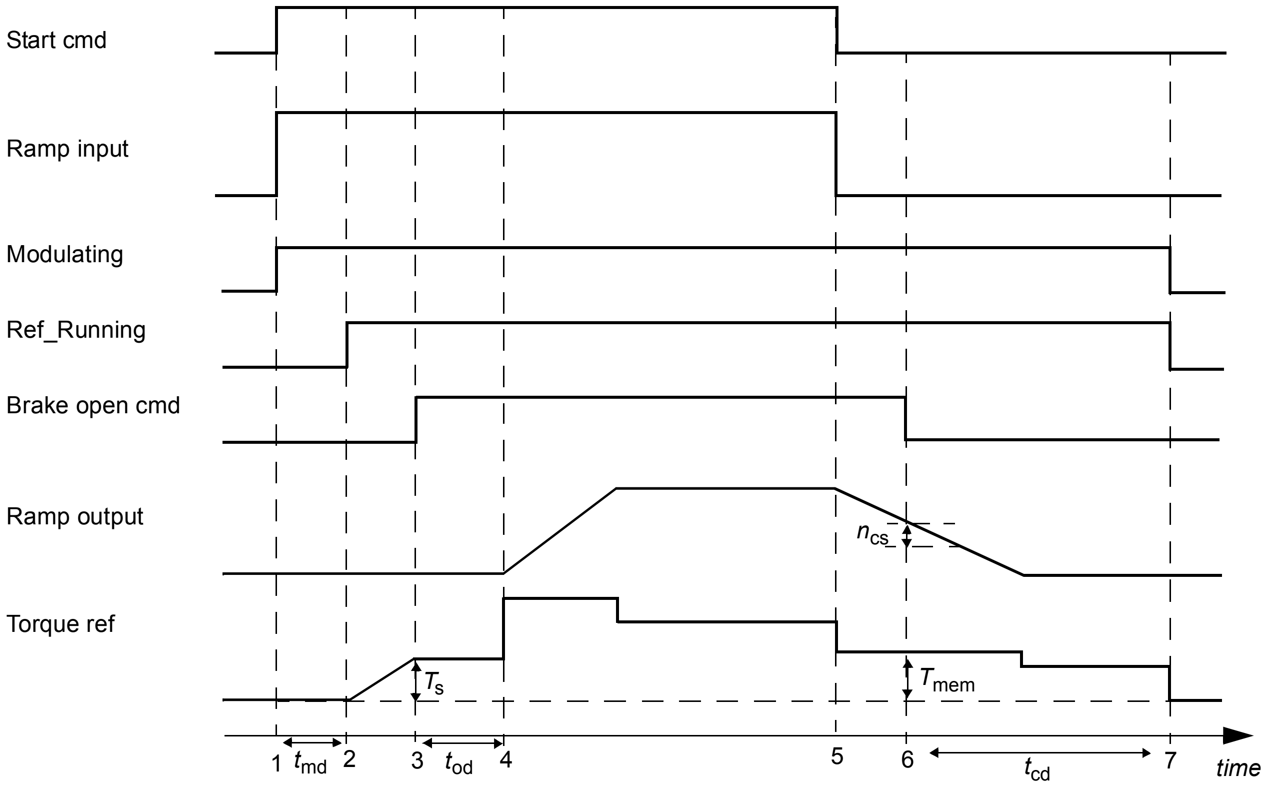
Z: 1 = Ramp generator output is forced to zero. 0 = Ramp generator output is enabled (normal operation).

State change conditions (Symbol ▬)

1)	Brake control is active (35.01 BRAKE CONTROL = (1) WITH ACK or (2) NO ACK) OR modulation of the drive is requested to stop. The drive control mode is forced to speed/scalar.
2)	External start command is on AND brake open request is on (35.07 BRAKE CLOSE REQ = 0).
3)	Starting torque required at brake release is reached (35.06 BRAKE OPEN TORQ) AND brake hold is not active (35.08 BRAKE OPEN HOLD). **Note:** With scalar control, the defined starting torque has no effect.
4)	Brake is open (acknowledgement = 1, selected by par. 35.02 BRAKE ACKNOWL) AND the brake open delay has passed (35.03 BRAKE OPEN DELAY). Start = 1.
5) 6)	Start = 0 OR brake close command is active AND actual motor speed < brake close speed (35.05 BRAKE CLOSE SPD).
7)	Brake is closed (acknowledgement = 0) AND brake close delay has passed (35.04 BRAKE CLOSE DLY). Start = 0.
8)	Start = 1.
9)	Brake is open (acknowledgement = 1) AND brake close delay has passed.
10)	Defined starting torque at brake release is not reached.
11)	Brake is closed (acknowledgement = 0) AND brake open delay has passed.
12)	Brake is closed (acknowledgement = 0).
13)	Brake is open (acknowledgement = 1) AND brake close delay has passed.

Operation time scheme

The simplified time scheme below illustrates the operation of the brake control function.



T_s	Start torque at brake release (parameter 35.06 BRAKE OPEN TORQ)
T_{mem}	Stored torque value at brake close (signal 3.14 BRAKE TORQ MEM)
t_{md}	Motor magnetising delay
t_{od}	Brake open delay (parameter 35.03 BRAKE OPEN DELAY)
n_{cs}	Brake close speed (parameter 35.05 BRAKE CLOSE SPD)
t_{cd}	Brake close delay (parameter 35.04 BRAKE CLOSE DLY)

Example

The figure below shows a brake control application example.

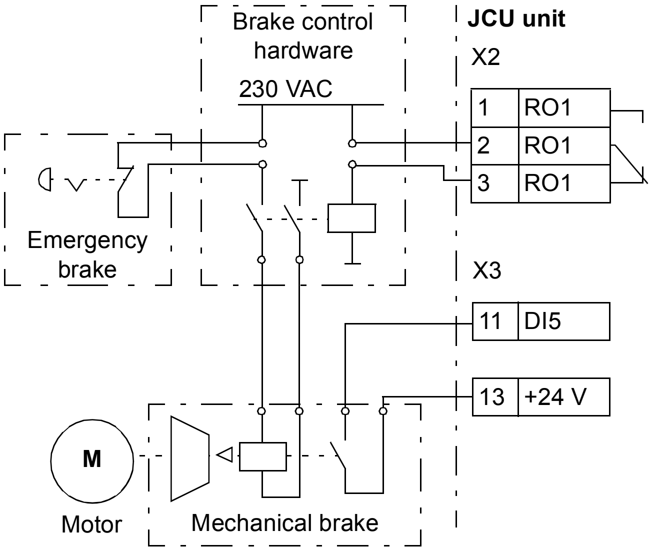
WARNING! Make sure that the machinery into which the drive with brake control function is integrated fulfils the personnel safety regulations. Note that the frequency converter (a Complete Drive Module or a Basic Drive Module, as defined in IEC 61800-2), is not considered as a safety device mentioned in the European Machinery Directive and related harmonised standards. Thus, the personnel safety of the complete machinery must not be based on a specific frequency converter feature (such as the brake control function), but it has to be implemented as defined in the application specific regulations.

The brake on/off is controlled via signal 3.15 BRAKE COMMAND. The source for the brake supervision is selected by parameter 35.02 BRAKE ACKNOWL.

The brake control hardware and wirings need to be done by the user.

- Brake on/off control through selected relay/digital output.
- Brake supervision through selected digital input.
- Emergency brake switch in the brake control circuit.

- Brake on/off control through relay output (i.e. parameter 12.12 RO1 OUT PTR is set to P.03.15 = 3.15 BRAKE COMMAND).
- Brake supervision through digital input DI5 (i.e. parameter 35.02 BRAKE ACKNOWL is set to P.02.01.04 = 2.01 DI STATUS bit 4)



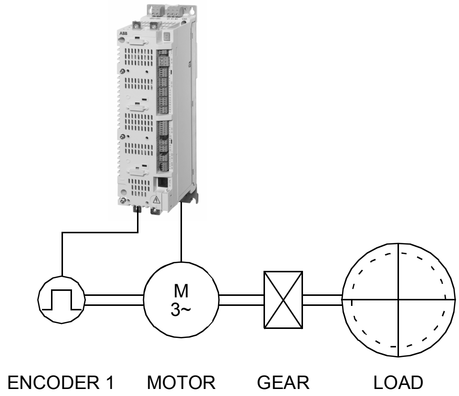
Position/synchron control features

Load encoder gear function

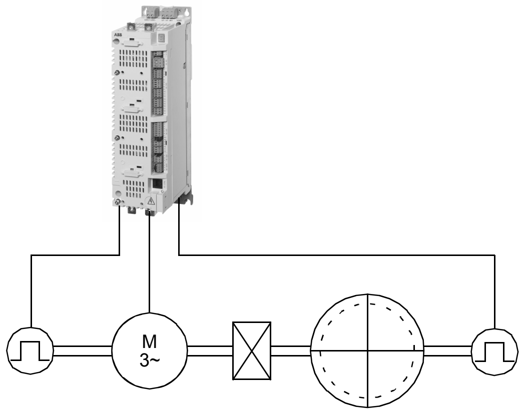
Positioning uses the measured speed and position of the load. The load encoder gear function calculates the actual load position on the basis of the measured motor shaft position.

Load encoder gear application examples:

Positioning uses the measured speed and position of the load. If no encoder is mounted on the load side, the load encoder gear function must be applied in order to calculate the actual load position on the basis of the measured motor shaft position.



A second encoder (encoder 2) mounted on the load side is used as the source for the actual position value. (Note: Inverted gear ratio is considered when the position control output (speed reference) is produced).



The load encoder gear parameters 60.03 LOAD GEAR MUL and 60.04 LOAD GEAR DIV are set as follows:

$$\frac{\text{60.03 LOAD GEAR MUL}}{\text{60.04 LOAD GEAR DIV}} = \frac{\text{Load speed}}{\text{Encoder 1/2 speed}}$$

Note: The sign of the programmed gear ratio has to match the sign of the mechanical gear ratio.

Because the drive speed control uses motor speed, a gear function between position control (load side) and speed control (motor side) is needed. This gear function is formed from the motor gear function and inverted load gear function. The gear function is applied to the position control output (speed reference) as follows:

$$\frac{\text{71.07 GEAR RATIO MUL}}{\text{71.08 GEAR RATIO DIV}} = \frac{\text{Motor speed}}{\text{Load speed}}$$

The equation quite often translates to

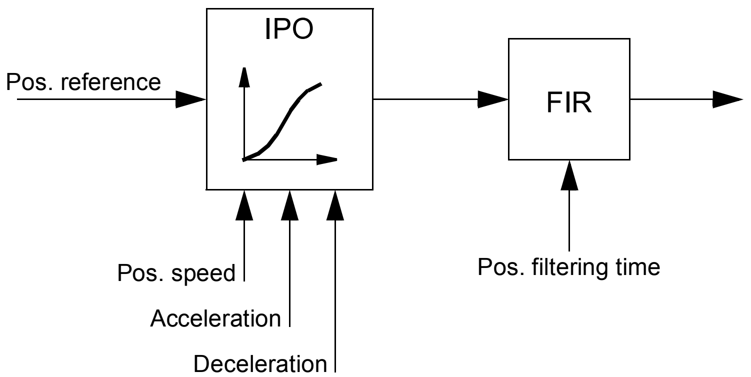
$$\frac{\text{71.07 GEAR RATIO MUL}}{\text{71.08 GEAR RATIO DIV}} = \frac{\text{22.03 MOTOR GEAR MUL} \times \text{60.04 LOAD GEAR DIV}}{\text{22.04 MOTOR GEAR DIV} \times \text{60.03 LOAD GEAR MUL}}$$

Parameters 71.07 GEAR RATIO MUL and 71.08 GEAR RATIO DIV are also inputs of the firmware block POS CONTROL (see page 222).

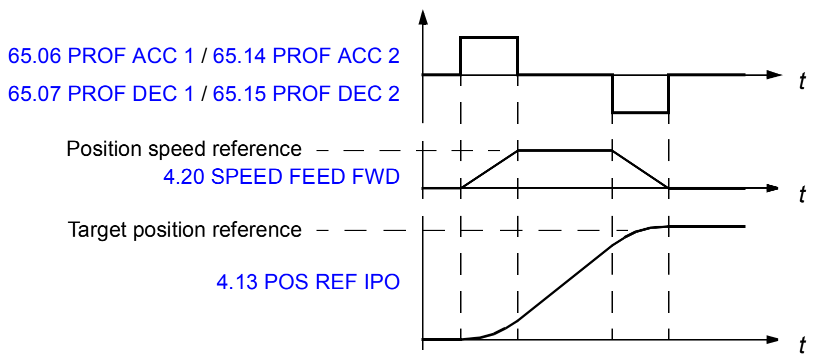
Note: It is emphasised that all position relevant parameters are load side related, e.g. the setting of parameter 70.04 POS SPEED LIM (dynamic limiter speed limitation) of 300 rpm denotes that, with a load gear ratio of 1:10, the motor can run at up to 3000 rpm.

Position profile generator

The position profile generator calculates the speed from which the drive can decelerate to a stop within the target distance using the defined deceleration reference. The calculated speed is used to generate an optimised position reference, which guides the drive to its target position.

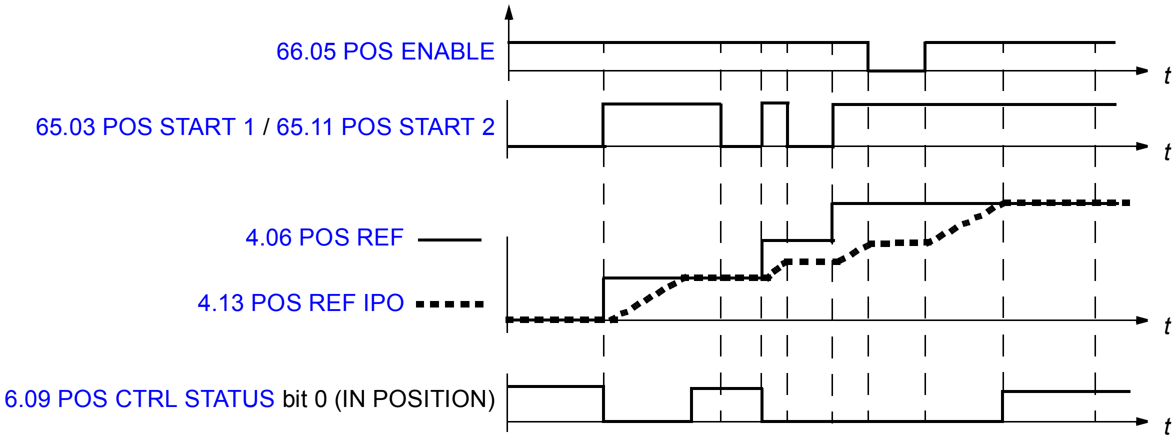


The following figure shows how the position profile generator generates a position reference.

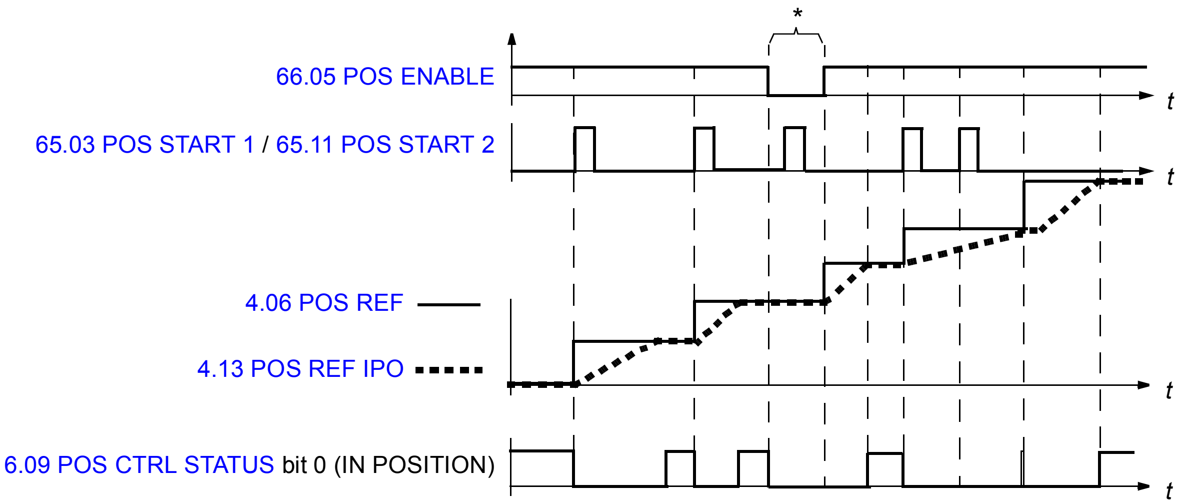


The position profile generator is also used to compensate synchronising errors.

Parameters 66.05 POS ENABLE and 65.03 POS START 1 / 65.11 POS START 2 control the operation of the position profile generator. The following figure shows the positioning commands and signals when parameter 65.24 POS START MODE is set to (0) NORMAL.



The following figure shows the positioning commands and signals when parameter 65.24 POS START MODE is set to (1) PULSE.



* If a pulse start (65.03 POS START 1 / 65.11 POS START 2) is received while the positioning enable signal (66.05 POS ENABLE) is 0, the start command is stored to drive memory and new positioning is started when the enable signal is set to 1. In this case, the positioning start can be cancelled only by changing the start mode (65.24 POS START MODE).

Position reference sets

The user can define two different positioning reference sets. Each reference set consists of

- position reference
- positioning speed reference
- positioning acceleration reference
- positioning deceleration reference
- positioning reference filter time
- positioning style
- positioning speed when target is reached.

One reference set is used at a time. The definition and selection of position reference sets are done using the parameters in Group 65 PROFILE REFERENCE.

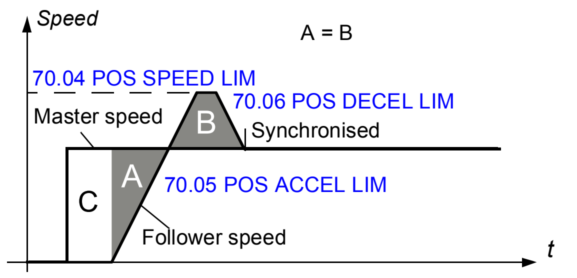
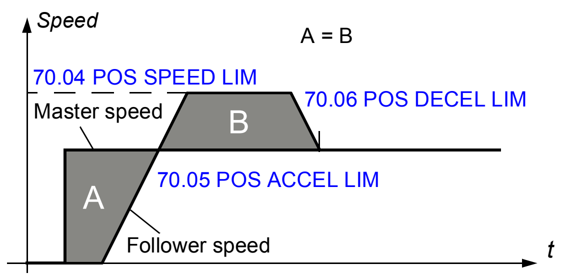
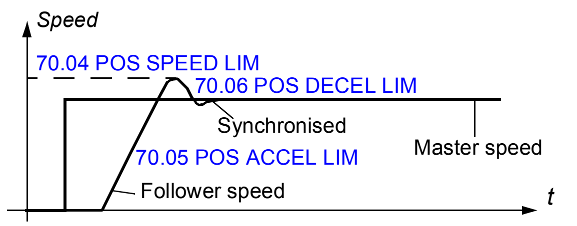
Dynamic position reference limiter

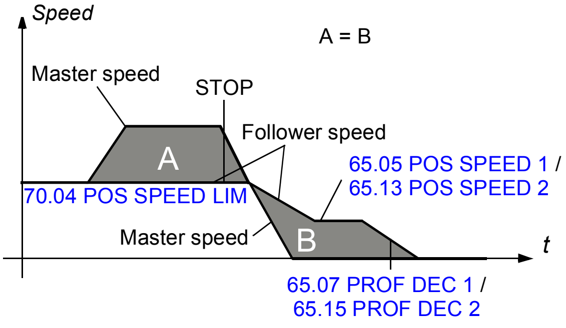
The dynamic limiter controls the position reference limitation in position control and synchron control modes. Dynamic limitation of the position reference causes a synchron error (4.18 SYNC ERROR). The error is accumulated and fed back to the position profile generator.

Start/stop examples with dynamic limiter

The speed curves of the master and follower during the start and stop are presented in the figures below.

When the follower is in synchron control, the reference can be taken from the encoder or from another drive. The master can be in any control mode.

Start: linear axis, relative synchronisation Used when the master is to be distance C ahead of the follower at start. 60.02 POS AXIS MODE is set to (0) LINEAR. 68.07 SYNCHRON MODE is set to (1) RELATIVE. To catch the master position, the follower accelerates up to its maximum allowed speed. Only master position changes which take place after the follower is started are taken into account.	
Start: linear axis, absolute synchronisation Used when the master and the follower are to be driven equal distances. 60.02 POS AXIS MODE is set to (0) LINEAR. 68.07 SYNCHRON MODE is set (0) ABSOLUTE. To catch the master position, the follower accelerates up to its maximum allowed speed. Master position changes which take place before and after the follower is started are taken into account.	
Start: rollover axis 60.02 POS AXIS MODE is set to (1) ROLLOVER. The follower accelerates until it has reached the master shaft position angle (position per one revolution, 0…360°). Rotations of the master are not counted.	

Stop: linear axis 60.02 POS AXIS MODE is set to (0) LINEAR. The figure shows how the dynamic limiter works together with the position profile generator when the drives are stopped: Before the stop command of the master, the speed of the follower is limited by the dynamic speed limiter (70.04 POS SPEED LIM), which results in a position error. When the master starts to decelerate, the follower uses positioning deceleration, and, eventually, positioning speed to overcome the position error.	

Position correction

Homing

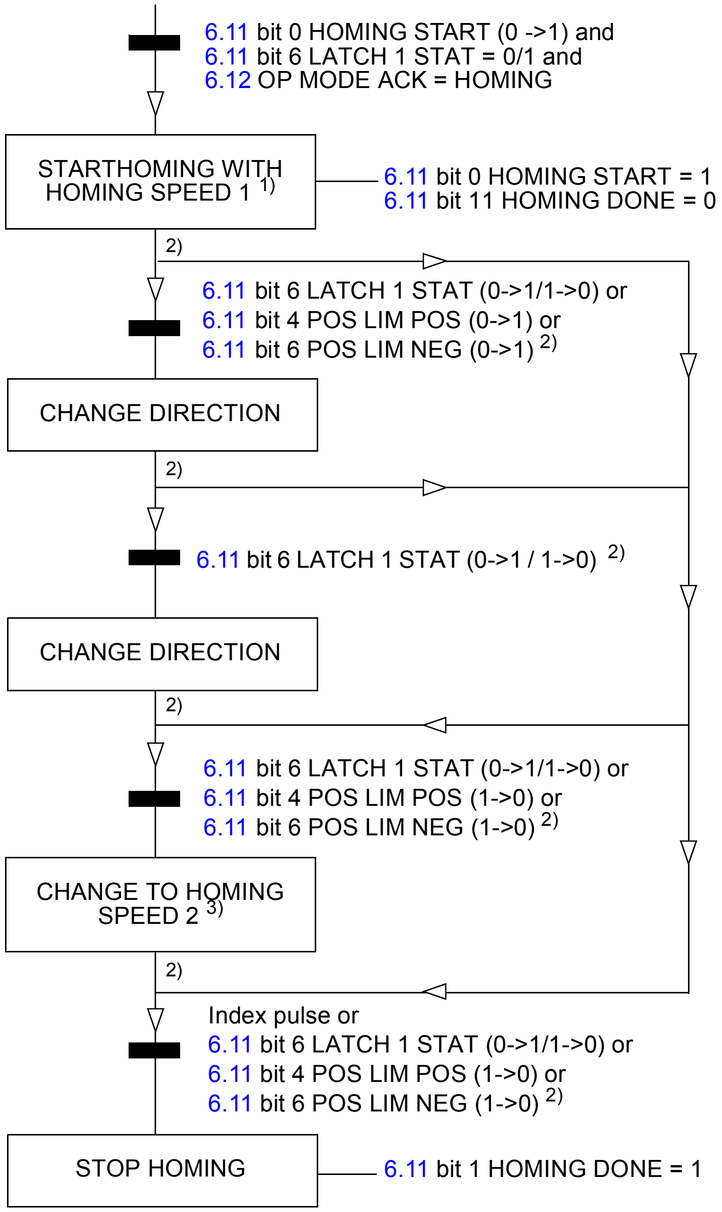
If an incremental encoder is used in position feedback, the drive actual position is set to zero at power-up. Normally, before first homing, the actual position of the driven machinery does not correspond to the internal zero position in the drive position control. Homing establishes a correspondence between these two positions. Because all homing functions use the same latching function, only one can be performed at a time.

Homing is implemented according to the CANopen Standard Proposal 402 for Device Profile Drives and Motion Control. The profile includes 35 different homing sequences (see the following homing mode table and chapter *Appendix C – Homing modes*). The start direction and used latch signals depend on the selected homing mode.

Homing sequence can be executed only in the homing control mode when the drive is modulating. When homing is activated by the homing start signal, the drive accelerates according to the active ramp time* to homing speed 1. The start direction depends on the selected homing method and the status of an external latch signal (home switch signal). During homing the direction can be changed by an external latch signal. Homing speed 1 is maintained until an external latch signal for homing speed 2 or for the home position is received. Homing is stopped with an index pulse or switch signal from an external latch and drive actual position is set as the zero position (or the user defined home position).

* Acceleration and deceleration ramp times are defined by the active position reference set (see parameter group 65 PROFILE REFERENCE on page 204).

The following state diagram presents the homing sequence.



1) The direction depends on the selected homing mode (par. 62.01 HOMING METHOD). The speed is defined by par. 62.07 HOMING SPEEDREF1.
2) Depends on the selected homing mode (par. 62.01 HOMING METHOD).
3) The speed is defined by par. 62.08 HOMING SPEEDREF2.

Source for the homing start signal is selected by par. 62.03 HOMING START.
Source for the latch signal (i.e. home switch) is selected by par. 62.04 HOME SWITCH TRIG.
Source for the positive limit switch is selected by par. 62.06 POS LIMIT SWITCH.
Source for the negative limit switch is selected by par. 62.05 NEG LIMIT SWITCH.

The following table presents homing modes 1…35. For more detailed descriptions, see chapter *Appendix C – Homing modes*.

No.	Latch state at start	Start direction	Change direction	Change to speed 2	Stop
1	Any	Negative	Negative limit switch: 0 -> 1	Negative limit switch: 1 -> 0	Index pulse
2	Any	Positive	Positive limit switch: 0 -> 1	Positive limit switch: 1 -> 0	Index pulse
3	0	Positive	Home switch: 0 -> 1	Home switch: 1 -> 0	Index pulse
	1	Negative	-	Home switch: 1 -> 0	Index pulse
4	0	Positive	-	Home switch: 0 -> 1	Index pulse
	1	Negative	Home switch: 1 -> 0	Home switch: 0 -> 1	Index pulse
5	0	Negative	Home switch: 0 -> 1	Home switch: 1 -> 0	Index pulse
	1	Positive	-	Home switch: 1 -> 0	Index pulse
6	0	Negative	-	Home switch: 0 -> 1	Index pulse
	1	Positive	Home switch: 1 -> 0	Home switch: 0 -> 1	Index pulse
7	0	Positive	Home switch: 0 -> 1	Home switch: 1 -> 0	Index pulse
	0	Positive	Positive limit switch: 0 -> 1	Home switch: 1 -> 0	Index pulse
	1	Negative	-	Home switch: 1 -> 0	Index pulse
8	0	Positive	-	Home switch: 0 -> 1	Index pulse
	0	Positive	1) Positive limit switch: 0 -> 1 2) Home switch: 1 -> 0	Home switch: 0 -> 1	Index pulse
	1	Negative	Home switch: 1 -> 0	Home switch: 0 -> 1	Index pulse
9	0	Positive	Home switch: 1 -> 0	Home switch: 0 -> 1	Index pulse
	0	Positive	Positive limit switch: 0 -> 1	Home switch: 0 -> 1	Index pulse
	1	Positive	Home switch: 1 -> 0	Home switch: 0 -> 1	Index pulse
10	0	Positive	-	Home switch: 1 -> 0	Index pulse
	0	Positive	1) Positive limit switch: 0 -> 1 2) Home switch: 0 -> 1	Home switch: 1 -> 0	Index pulse
	1	Positive	-	Home switch: 1 -> 0	Index pulse
11	0	Negative	Home switch: 0 -> 1	Home switch: 1 -> 0	Index pulse
	1	Positive	-	Home switch: 1 -> 0	Index pulse
	0	Negative	Negative limit switch: 0 -> 1	Home switch: 1 -> 0	Index pulse
12	0	Negative	-	Home switch: 0 -> 1	Index pulse
	1	Positive	Home switch: 1 -> 0	Home switch: 0 -> 1	Index pulse
	0	Negative	1) Negative limit switch: 0 -> 1 2) Home switch: 1 -> 0	Home switch: 0 -> 1	Index pulse

No.	Latch state at start	Start direction	Change direction	Change to speed 2	Stop
13	0	Negative	Home switch: 1 -> 0	Home switch: 0 -> 1	Index pulse
	0	Negative	Negative limit switch: 0 -> 1	Home switch: 0 -> 1	Index pulse
	1	Negative	Home switch: 1 -> 0	Home switch: 0 -> 1	Index pulse
14	0	Negative	-	Home switch: 1 -> 0	Index pulse
	0	Negative	1) Negative limit switch: 0 -> 1 2) Home switch: 0 -> 1	Home switch: 1 -> 0	Index pulse
	1	Negative	-	Home switch: 1 -> 0	Index pulse
15	-	-	-	-	-
16	-	-	-	-	-
17	Any	Negative	Negative limit switch: 0 -> 1	-	Negative limit switch: 1 -> 0
18	Any	Positive	Positive limit switch: 0 -> 1	-	Positive limit switch: 1 -> 0
19	0	Positive	Home switch: 0 -> 1	-	Home switch: 1 -> 0
	1	Negative	-	-	Home switch: 1 -> 0
20	0	Positive	-	-	Home switch: 0 -> 1
	1	Negative	Home switch: 1 -> 0	-	Home switch: 0 -> 1
21	0	Negative	Home switch: 0 -> 1	-	Home switch: 1 -> 0
	1	Positive	-	-	Home switch: 1 -> 0
22	0	Negative	-	-	Home switch: 0 -> 1
	1	Positive	Home switch: 1 -> 0	-	Home switch: 0 -> 1
23	0	Positive	Home switch: 0 -> 1	-	Home switch: 1 -> 0
	0	Positive	Positive limit switch: 0 -> 1	-	Home switch: 1 -> 0
	1	Negative	-	-	Home switch: 1 -> 0
24	0	Positive	-	-	Home switch: 0 -> 1
	1	Negative	Home switch: 1 -> 0	-	Home switch: 0 -> 1
	0	Positive	1) Positive limit switch: 0 -> 1 2) Home switch: 1 -> 0	-	Home switch: 0 -> 1
25	0	Positive	Home switch: 1 -> 0	-	Home switch: 0 -> 1
	0	Positive	Positive limit switch: 0 -> 1	-	Home switch: 0 -> 1
	1	Positive	Home switch: 1 -> 0	-	Home switch: 0 -> 1

No.	Latch state at start	Start direction	Change direction	Change to speed 2	Stop
26	0	Positive	-	-	Home switch: 1 -> 0
	1	Positive	-	-	Home switch: 1 -> 0
	0	Positive	1) Positive limit switch: 0 -> 1 2) Home switch: 0 -> 1	-	Home switch: 1 -> 0
27	0	Negative	Home switch: 0 -> 1	-	Home switch: 1 -> 0
	0	Negative	Negative limit switch: 0 -> 1	-	Home switch: 1 -> 0
	1	Positive	-	-	Home switch: 1 -> 0
28	0	Negative	-	-	Home switch: 0 -> 1
	0	Negative	1) Negative limit switch: 0 -> 1 2) Home switch: 1 -> 0	-	Home switch: 0 -> 1*
	1	Positive	Home switch: 1 -> 0	-	Home switch: 0 -> 1
29	0	Negative	Home switch: 1 -> 0	-	Home switch: 0 -> 1*
	0	Negative	Negative limit switch: 0 -> 1	-	Home switch: 0 -> 1
	1	Negative	Home switch: 1 -> 0	-	Home switch: 0 -> 1*
30	0	Negative	-	-	Home switch: 1 -> 0
	0	Negative	1) Negative limit switch: 0 -> 1 2) Home switch: 0 -> 1	-	Home switch: 1 -> 0
	1	Negative	-	-	Home switch: 1 -> 0
31	-	-	-	-	-
32	-	-	-	-	-
33	Any	Negative	-	-	Index pulse
34	Any	Positive	-	-	Index pulse
35	-	-	-	-	-

Negative direction = left. Positive direction = right.
Index pulse = encoder zero pulse.
Home switch: source selected by par. 62.04 HOME SWITCH TRIG
Negative limit switch: source selected by par. 62.05 NEG LIMIT SWITCH
Positive limit switch: source selected by par. 62.06 POS LIMIT SWITCH
* Stop is only possible after a falling edge of the home switch has been detected.

Preset functions

Preset functions are used to set the position system according to a parameter value (preset position) or actual position. The physical position of the driven machinery is not changed, but the new position value is used as home position. Preset functions can be used e.g. in synchron control to change the follower position without moving the master.

The preset function trigger signal is selected by parameter 62.12 PRESET TRIG.

There are three different preset functions:

- SYNCH REF: Preset drive synchron reference chain (4.16 SYNC REF GEARED) to the value of 62.12 PRESET POSITION.
- ACT TO SYNCH: Preset drive synchron reference chain (4.16 SYNC REF GEARED) to the value of actual position (1.12 POS ACT).
- WHOLE SYSTEM: Preset whole position system of the drive to the value of 62.12 PRESET POSITION.

In addition, homing mode 35 (selectable by parameter 62.01 HOMING METHOD), can be used to set the position reference chain (4.13 POS REF IPO, 4.16 SYNC REF GEARED, 4.17 POS REF LIMITED, 1.12 POS ACT) to the value of 62.09 HOME POSITION on a rising edge of 62.04 HOME SWITCH TRIG.

Cyclic position correction

Cyclic position correction functions are used to change or correct the system position continuously according to data measured by external probe signals, for example, if there is play in the machinery. There are five different cyclic position correction functions selectable by parameter 62.14 CYCLIC CORR MODE:

- CORR ACT POS: Drive actual position correction.
- CORR MAST REF: Synchronised master drive reference correction.
- CORR M/F DIST: Master and follower distance correction.
- 1 PROBE DIST: Drive actual position correction according to the distance between two latches from one probe.
- 2 PROBE DIST: Drive actual position correction according to the distance between two latches from two different probes.

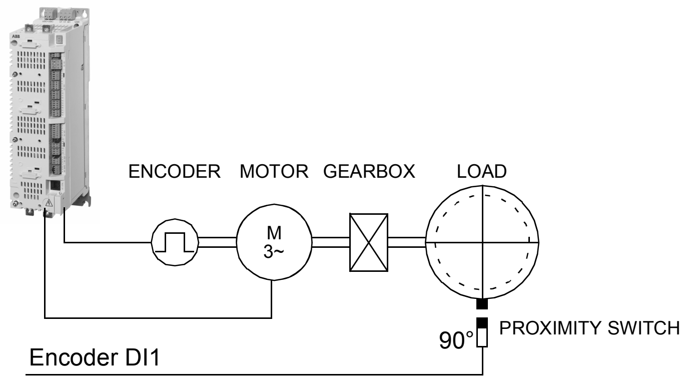
<u>Actual position correction</u>

The purpose of the actual position correction is to measure a position and compare it with the actual encoder position. If there is a deviation, a corresponding correction is carried out. The required transition is determined by the position profile generator parameters.

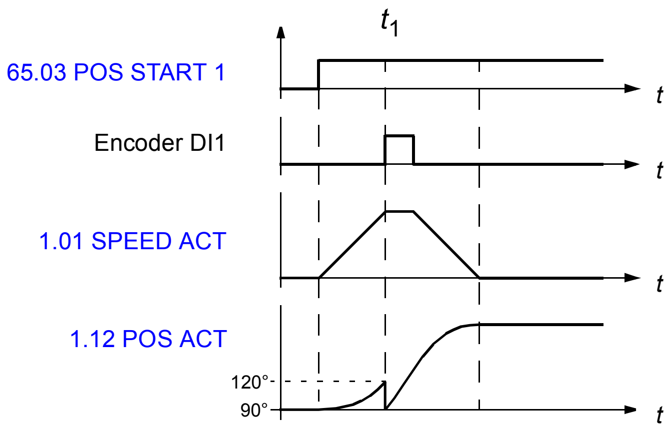
Note: Probe 1 settings must always be used for the actual position correction.

Example:

The following figure presents a roll-over application. The motor rotates a round table. There is a mechanical gear between the motor and load. The gear is prone to produce some drift on the load side. In order to compensate this drift, actual position correction is used. A proximity switch is located on the load side at 90°.



Parameter	Setting	Information
60.05 POS UNIT	(1) DEGREE	All position values are in degrees
62.14 CYCLIC CORR MODE	(1) COR ACT POS	Actual position correction
62.15 TRIG PROBE1	(1) ENC1 DI1 _-	Rising edge of encoder 1 digital input DI1. Source of the actual position latching command (proximity switch signal source)
60.02 POS AXIS MODE	(1) ROLLOVER	Positioning is between 0 and 1 revolutions, i.e. after 360°, the position calculation starts from 0° again.
62.16 PROBE1 POS	90°	Reference position for the actual position probe



t_1: Rising edge of encoder digital input DI1 signal (proximity switch signal) is detected when the load position should be 90°. The actual position of the encoder is 120° (stored to signal 4.03 PROBE1 POS MEAS).

Distance between the load position and the actual position is 90° - 120° = -30° (= 4.05 CYCLIC POS ERR). Actual position of the encoder, 1.12 POS ACT, is corrected according to 4.05 CYCLIC POS ERR using positioning parameter and dynamic limiter settings.

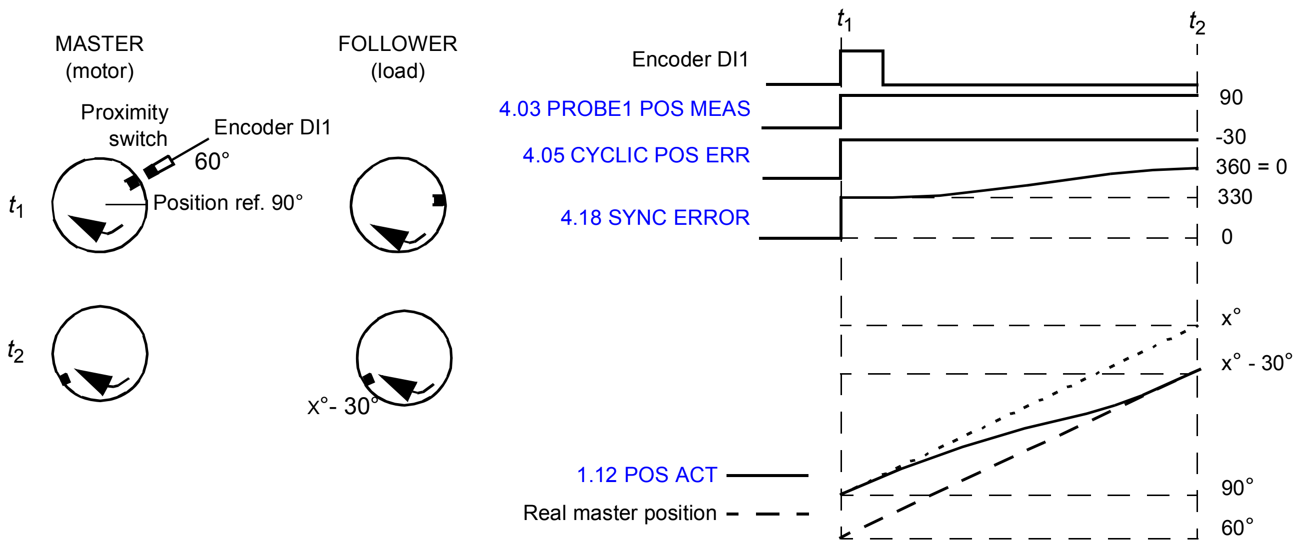
Master reference correction

The purpose of the master reference correction is to correct the difference between the master and reference positions.

Note: In master reference correction the follower must always be in synchron control mode.

Example:

Parameter	Setting	Information
60.05 POS UNIT	(1) DEGREE	All position values are in degrees
60.02 POS AXIS MODE	(1) ROLLOVER	Positioning is between 0 and 1 revolutions, i.e. after 360°, the position calculation starts from 0° again.
68.02 SYNC GEAR MUL	Same as for 68.03 SYNC GEAR DIV	Synchron gear ratio is 1.
62.14 CYCLIC CORR MODE	(2) COR MAS REF	Master (motor) reference correction
62.15 TRIG PROBE1	(1) ENC1 DI1 _-	Rising edge of encoder 1 digital input DI1. Source of the master (motor) position reference latching command (proximity switch signal source)
62.16 PROBE1 POS	60°	Reference position for the master (motor) reference position probe



t_1: Rising edge of encoder digital input DI1 signal (proximity switch signal) is detected when the master (motor) position should be 60°. The used position reference is 90° (stored to signal 4.03 PROBE1 POS MEAS).

The master reference correction function calculates the position error, 4.05 CYCLIC POS ERR, which is the difference between the master (motor) position and the reference position:

4.05 CYCLIC POS ERR = 62.16 PROBE1 POS - 4.03 PROBE1 POS MEAS = 60° - 90° = -30°

The position error is corrected using positioning parameter and dynamic limiter settings.

t_2: Error has been corrected and the follower (load) is in line with the master (motor). Cyclic function is ready for a new correction if necessary.

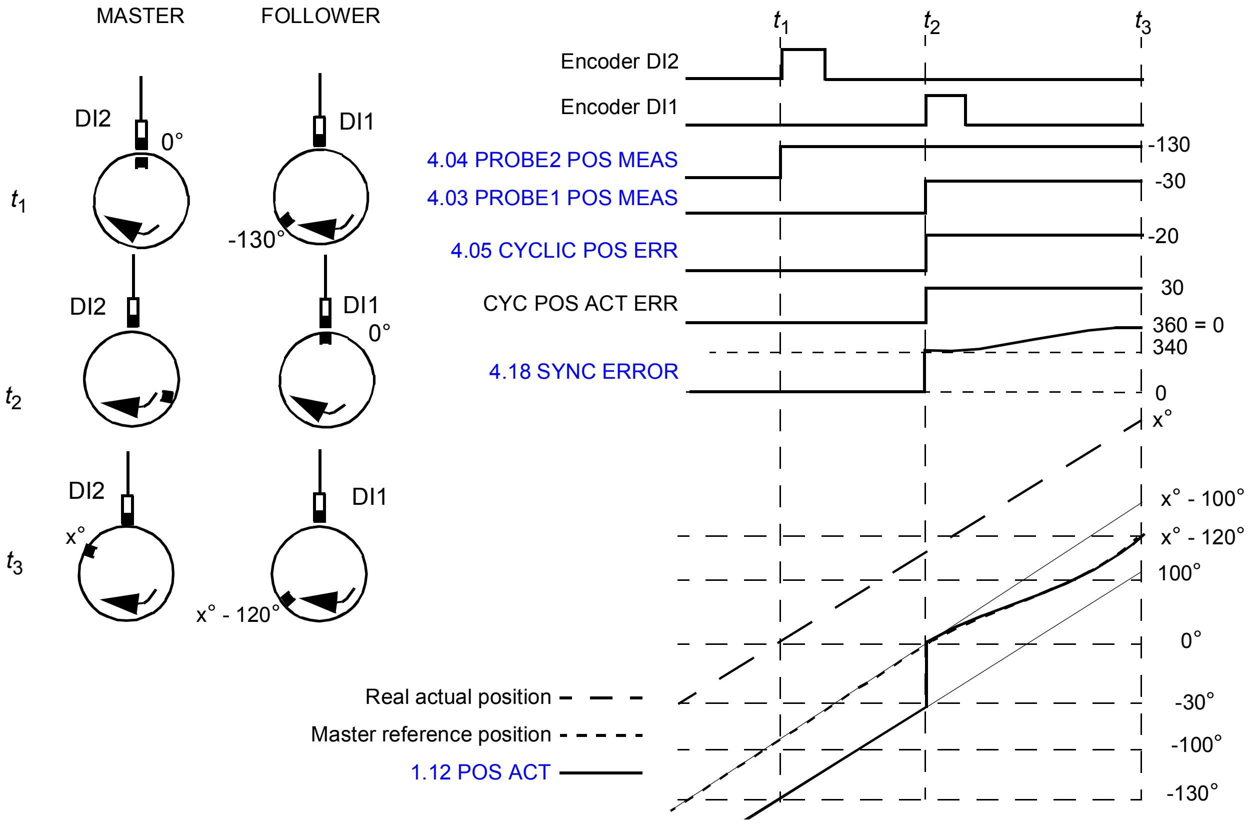
Master/Follower distance correction

The purpose of the master/follower distance correction is to measure the distance between two positions and compare it with the defined reference. If there is a deviation, a correction is carried out.

Note: In master/follower distance correction the follower must always be in synchron control mode.

Example 1: Rollover axis application. Master and follower proximity switches are located at 0°.

Parameter	Setting	Information
60.02 POS AXIS MODE	(1) ROLLOVER	Positioning is between 0 and 1 revolutions, i.e. after 360°, the position calculation starts from 0° again.
60.05 POS UNIT	(1) DEGREE	All position values are in degrees
68.02 SYNC GEAR MUL	Same as for 68.03 SYNC GEAR DIV	Synchron gear ratio is 1.
62.14 CYCLIC CORR MODE	(5) COR M/F DIST	Cyclic master/follower distance correction
62.15 TRIG PROBE1	(1) ENC1 DI1 _-	Rising edge of encoder 1 digital input DI1. Source of the actual position latching command (proximity switch signal source)
62.17 TRIG PROBE2	(3) ENC1 DI2 _-	Rising edge of encoder 1 digital input DI2. Source of the master position latching command (proximity switch signal source)
62.16 PROBE1 POS	0°	Reference position for the actual position probe
62.18 PROBE2 POS	-120°	Reference position for the master position probe i.e. follower is 120° [(0°-120°)-(0°-0°)] behind the master.



t_1: Rising edge of encoder DI2 signal (proximity switch signal) is detected when the master position is 0°. The follower position is -130° (stored to signal 4.04 PROBE2 POS MEAS).

t_2: Rising edge of encoder DI1 signal (proximity switch signal) is detected when the follower position is 0°. The actual position of the encoder is -30° (stored to signal 4.03 PROBE1 POS MEAS). Distance between the follower position and the actual position is 0° - (-30°) = 30°.

According to parameter 62.16 PROBE1 POS and 62.18 PROBE2 POS settings the follower should be 120° behind the master.

The following phase shift between the master and follower is calculated and stored as reference error 4.05 CYCLIC POS ERR.
(62.18 PROBE2 POS - 4.04 PROBE2 POS MEAS) - (62.16 PROBE1 POS - 4.03 PROBE1 POS MEAS) = [-120° - (-130°)] - [0° - (-30°)] = -20°

This error is added to 4.18 SYNC ERROR. The synchron error is corrected using positioning parameters.

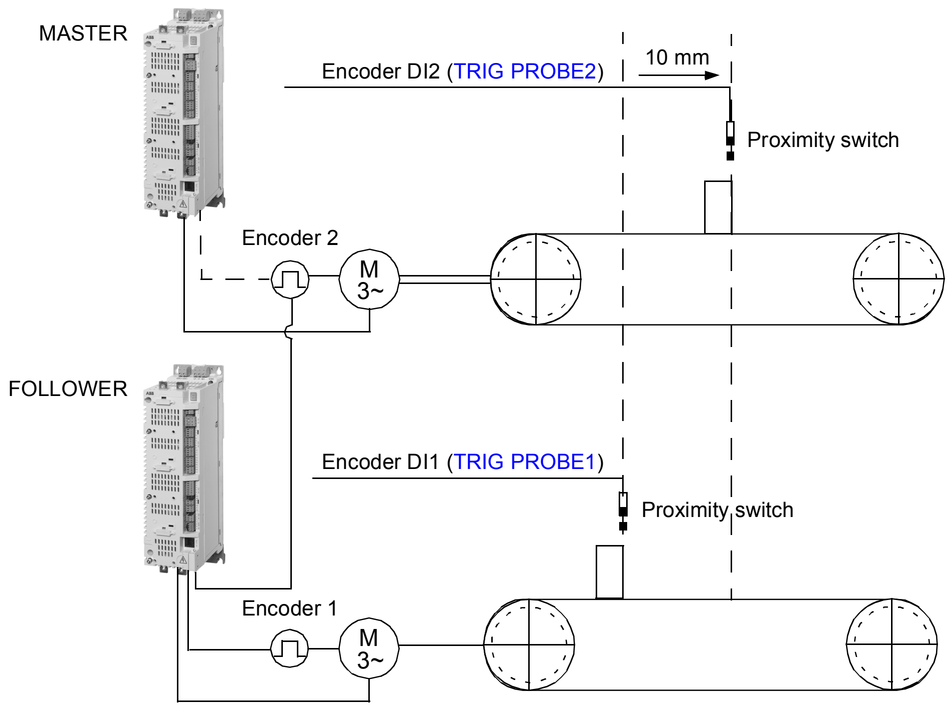
t_3: Error has been corrected and the follower is 120° behind the master. Cyclic function is ready for a new correction if necessary.

Note 1: Only after the active correction is finished is the next position latching enabled.
Note 2: The cyclic corrections are always performed along the shortest path. This must be taken into account in all rollover applications.
Note 3: In rollover applications, the correction range is limited to ±180°.

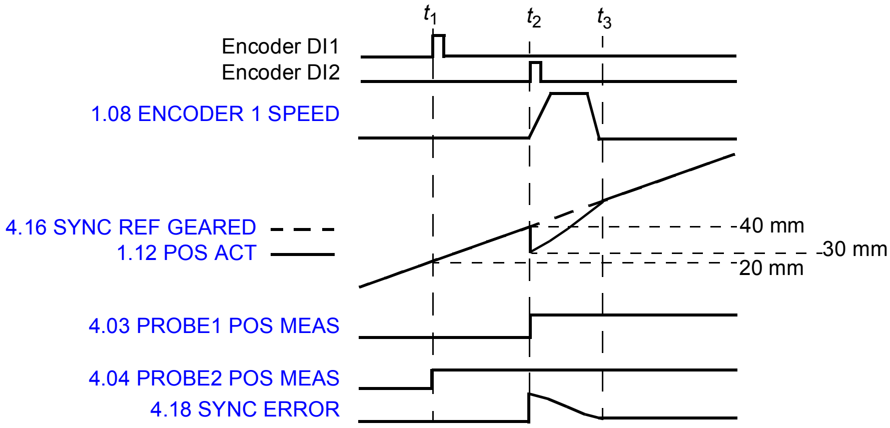
Example 2: Linear axis application

Two conveyer systems are synchronised using two encoders. The follower is in synchron control and follows the master encoder 2 position.

Note: In linear axis applications, only the difference between the master and follower positions is corrected.



Parameter	Setting	Information
60.02 POS AXIS MODE	(0) LINEAR	Positioning between minimum position 60.14 MINIMUM POS and maximum position 60.13 MAXIMUM POS
60.05 POS UNIT	(2) METER	All position values are in metres
67.01 SYNC REF SEL	(8) POS 2ND ENC	Synchron position reference (master position) from encoder 2.
68.07 SYNCHRON MODE	(0) ABSOLUTE	Absolute synchronisation of the follower. The follower follows the master position after start.
62.14 CYCLIC CORR MODE	(5) COR M/F DIST	Cyclic master follower distance correction
62.15 TRIG PROBE1	(1) ENC1 DI1 _-	Rising edge of encoder 1 digital input DI1. Source of the actual position latching command (proximity switch signal source)
62.17 TRIG PROBE2	(17) ENC2 DI2 _-	Rising edge of encoder 2 digital input DI2. Source of the master position reference latching command (proximity switch signal source)
62.16 PROBE1 POS	0,015 m	Reference position for the actual position probe
62.18 PROBE2 POS	0,025 m	Reference position for the master position probe



t_1: Rising edge of encoder digital input DI1 (proximity switch signal) is detected. The follower position is 20 mm (stored to signal 4.04 PROBE2 POS MEAS).

t_2: Rising edge of encoder digital input DI2 signal (proximity switch signal) is detected when the follower position is 40 mm (stored to signal 4.03 PROBE1 POS MEAS).

According to parameter 62.16 PROBE1 POS and 62.18 PROBE2 POS settings the follower should be 10 mm behind the master.

The following correction is calculated and stored as reference error 4.05 CYCLIC POS ERR:

(62.16 PROBE1 POS - 62.18 PROBE2 POS) - (4.04 PROBE2 POS MEAS - 4.03 PROBE1 POS MEAS)] = (15 mm - 25 mm) - (20 mm - 40 mm)] = 10 mm

This error is added to 4.18 SYNC ERROR. The synchron error is corrected using positioning parameters.

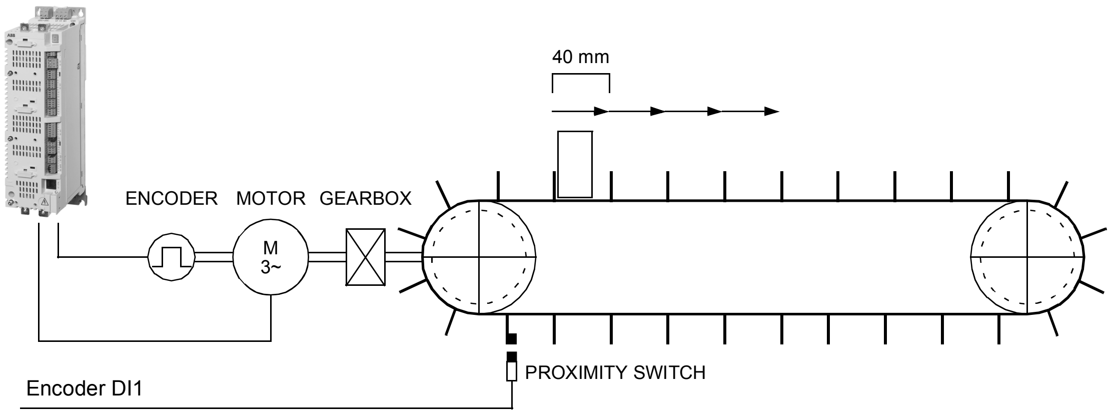
t_3: Error has been corrected and the follower is 10 mm behind the master. Cyclic function is ready for a new correction if necessary.

Distance correction with one probe

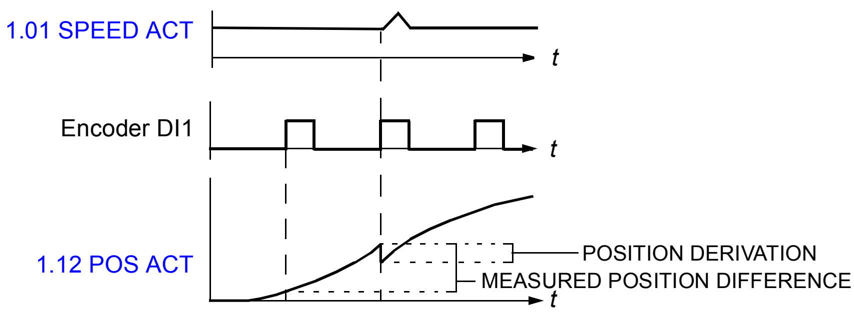
The purpose is to correct actual position according to the distance between the latched positions and measured positions when two consecutive latches from one probe are used. Both latches use the same latch signal source (e.g. encoder digital input DI1) and latch command (e.g. rising edge). If the application requires different latch commands, see section Distance correction with two probes on page 69.

Example:

The following figure shows a conveyer system where a box should be positioned. The conveyer belt is marked every 40 mm.



Parameter	Setting	Information
60.02 POS AXIS MODE	(0) LINEAR	Positioning between minimum position 60.14 MINIMUM POS and maximum position 60.13 MAXIMUM POS
60.05 POS UNIT	(2) METER	All position values are in metres
62.14 CYCLIC CORR MODE	(3) 1 PROBE DIST	Distance correction with one probe
62.15 TRIG PROBE1	(1) ENC1 DI1 _-	Rising edge of encoder 1 digital input DI1. Source of the actual position latching command (proximity switch signal source)
62.16 PROBE1 POS	0 m	Reference position for position probe 1
62.18 PROBE2 POS	0.040 m (= 40 mm)	Reference position for position probe 1



- Rising edge of encoder DI1 (proximity switch signal) is detected at the first mark of the belt. Position 0 mm is stored to signal 4.03 PROBE1 POS MEAS.
- Next rising edge of encoder DI1 (proximity switch signal) is detected at the second mark of the belt. Position 30 mm is stored to signal 4.04 PROBE2 POS MEAS.
- The reference distance between the marks is 40 mm and the measured distance between the marks is 30 mm, thus the error is 10 mm:

(62.18 PROBE2 POS - 62.16 PROBE1 POS) - (4.04 PROBE2 POS MEAS - 4.03 PROBE1 POS MEAS)] = (40 - 0) - (30 - 0) = 10 mm

The error is stored to 4.18 SYNC ERROR.

Actual position of the encoder 1.12 POS ACT is corrected according to 4.18 SYNC ERROR using positioning parameter and dynamic limiter settings.

Note: Only after the active correction is finished is the next position latching enabled.

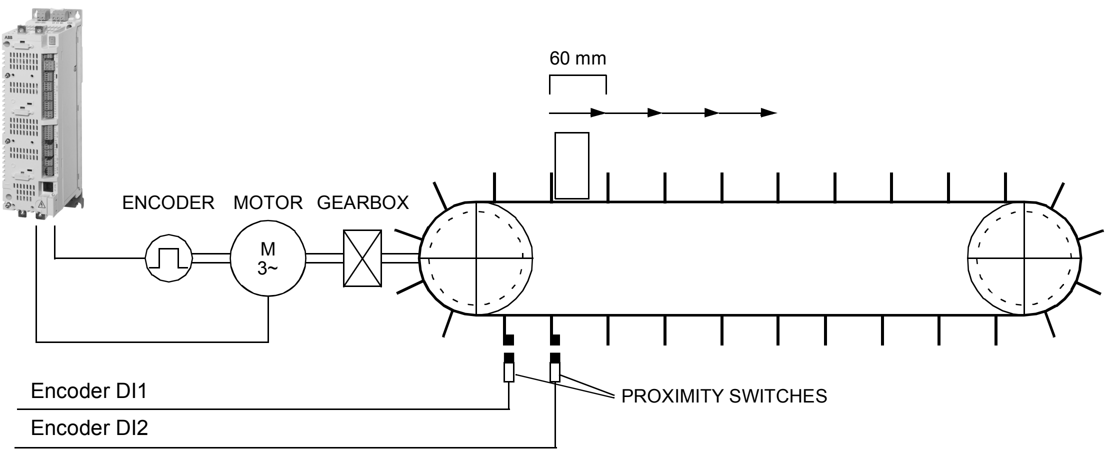
Distance correction with two probes

The purpose is to correct actual position according to the distance between the latched positions and measured positions when latches from two probes are used. The latches use different latch sources (e.g. encoder digital input DI1 and DI2) and latch commands (e.g. rising and falling edge).

In special applications, this correction function can also be executed by using two consecutive latches from one probe. The latches use the same latch source (e.g. encoder digital input DI1) and different latch commands (e.g. rising and falling edge).

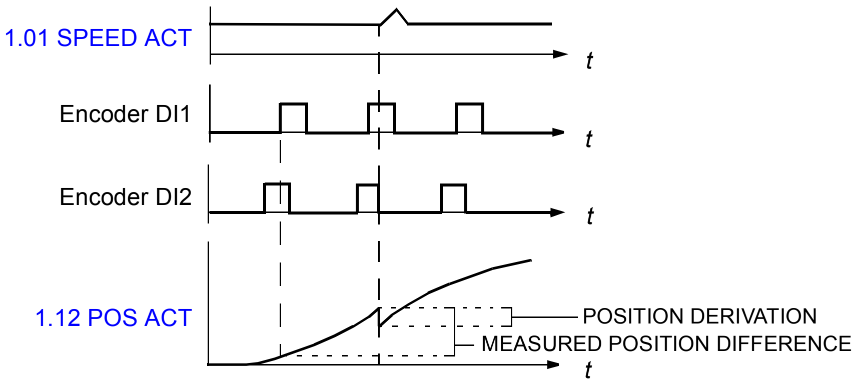
Example:

The following figure shows a conveyer system where a box should be positioned. The conveyer belt is marked every 60 mm.



Parameter	Setting	Information
60.02 POS AXIS MODE	(0) LINEAR	Positioning between minimum position 60.14 MINIMUM POS and maximum position 60.13 MAXIMUM POS
60.05 POS UNIT	(2) METER	All position values are in metres
62.14 CYCLIC CORR MODE	(4) 2 PROBE DIST	Distance correction with two probes
62.15 TRIG PROBE1	(1) ENC1 DI1 _-	Rising edge of encoder 1 digital input DI1. Source of the actual position latching command (proximity switch signal source)
62.17 TRIG PROBE2	(3) ENC1 DI2 _-	Falling edge of encoder 1 digital input DI2. Source of the actual position reference latching command (proximity switch signal source)
62.16 PROBE1 POS	0 m	Reference position for actual position probe 1

Parameter	Setting	Information
62.18 PROBE2 POS	0.060 m (=60 mm)	Reference position for actual position probe 2



- Rising edge of encoder DI1 (proximity switch signal) is detected at the first mark of the belt. Position 0 mm is stored to signal 4.03 PROBE1 POS MEAS.
- Falling edge of encoder DI2 (proximity switch signal) is detected at the second mark of the belt. Position 40 mm is stored to signal 4.04 PROBE2 POS MEAS.
- The reference distance between the marks is 60 mm and the measured distance between the marks is 40 mm, thus the error is 20 mm:

(62.18 PROBE2 POS - 62.16 PROBE1 POS) - (4.04 PROBE2 POS MEAS - 4.03 PROBE1 POS MEAS)] = (60 - 0) - (40 - 0) = 20 mm

The error is stored to 4.18 SYNC ERROR:

Actual position of the encoder 1.12 POS ACT is corrected according to 4.18 SYNC ERROR using positioning parameter and dynamic limiter settings.

Note: Only after the active correction is finished is the next position latching enabled.

Emergency stop

Note: The user is responsible for installing the emergency stop devices and all the additional devices needed for the emergency stop to fulfil the required emergency stop category classes.

The emergency stop signal is connected to the digital input which is selected as the source for the emergency stop activation (parameter 10.10 EM STOP OFF3 or 10.11 EM STOP OFF1). Emergency stop can also be activated through fieldbus (2.12 FBA MAIN CW).

Note: When an emergency stop signal is detected, the emergency stop function cannot be cancelled even though the signal is cancelled.

For more information, refer to *Application Guide: Functional Safety Solutions with ACSM1 Drives* (3AUA0000031517 [English]).

Default connections of the control unit

What this chapter contains

This chapter shows the default control connections of the JCU Control Unit.

More information on the connectivity of the JCU is given in the *Hardware Manual* of the drive.

Notes:

*Total maximum current: 200 mA

1) Selected by par. 12.01 DIO1 CONF.

2) Selected by par. 12.02 DIO2 CONF.

3) Selected by par. 12.03 DIO3 CONF.

4) Selected by jumper J1.

5) Selected by jumper J2.

Current:

J1/2

Voltage:

J1/2

		X1
External power input 24 V DC, 1.6 A	+24VI	1
	GND	2
		X2
Relay output: Brake close/open 250 V AC / 30 V DC 2 A	NO	1
	COM	2
	NC	3
		X3
+24 V DC*	+24VD	1
Digital I/O ground	DGND	2
Digital input 1: Stop/start (par. 10.02 and 10.05)	DI1	3
Digital input 2: EXT1/EXT2 (par. 34.01)	DI2	4
+24 V DC*	+24VD	5
Digital I/O ground	DGND	6
Digital input 3: Fault reset (par. 10.08)	DI3	7
Digital input 4: Positioning start (par. 65.03/65.11)	DI4	8
+24 V DC*	+24VD	9
Digital I/O ground	DGND	10
Digital input 5: Position reference set 1/2 (par. 65.02)	DI5	11
Digital input 6: Homing start (par. 62.03 and 34.02)	DI6	12
+24 V DC*	+24VD	13
Digital I/O ground	DGND	14
Digital input/output 1 1): Ready	DIO1	15
Digital input/output 2 2): Running	DIO2	16
+24 V DC*	+24VD	17
Digital I/O ground	DGND	18
Digital input/output 3 3): Fault	DIO3	19
		X4
Reference voltage (+)	+VREF	1
Reference voltage (–)	–VREF	2
Ground	AGND	3
Analogue input 1 (mA or V) 4): Speed reference (par. 24.01)	AI1+	4
	AI1–	5
Analogue input 2 (mA or V) 5): Torque reference (par. 32.01)	AI2+	6
	AI2–	7
AI1 current/voltage selection		J1
AI2 current/voltage selection		J2
Thermistor input	TH	8
Ground	AGND	9
Analogue output 1 (mA): Output current	AO1 (I)	10
Analogue output 2 (V): Actual speed	AO2 (U)	11
Ground	AGND	12
		X5
Drive-to-drive link termination		J3
Drive-to-drive link	B	1
	A	2
	BGND	3
		X6
Safe Torque Off. Both circuits must be closed for the drive to start. See the appropriate drive hardware manual.	OUT1	1
	OUT2	2
	IN1	3
	IN2	4
Control panel connection		**X7**

Parameters and firmware blocks

What this chapter contains

This chapter lists and describes the parameters provided by the firmware.

Types of parameters

Parameters are user-adjustable operation instructions of the drive (groups 10…99). There are four basic types of parameters: Actual signals, value parameters, value pointer parameters and bit pointer parameters.

Actual signal

Type of parameter that is the result of a measurement or calculation by the drive. Actual signals can be monitored, but not adjusted, by the user. Actual signals are typically contained within parameter groups 1…9.

For additional actual signal data, e.g. update cycles and fieldbus equivalents, see chapter *Parameter data*.

Value parameter

A value parameter has a fixed set of choices or a setting range.

Example 1: Motor phase loss supervision is activated by selecting (1) FAULT from the selection list of parameter 46.06 MOT PHASE LOSS.

Example 2: The motor nominal power (kW) is set by writing the appropriate value to parameter 99.10 MOT NOM POWER, e.g. 10.

Value pointer parameter

A value pointer parameter points to the value of another parameter. The source parameter is given in format **P.xx.yy**, where xx = Parameter group; yy = Parameter index. In addition, value pointer parameters often have a set of pre-selected choices.

Example: Motor current signal, 1.05 CURRENT PERC, is connected to analogue output AO1 by setting parameter 15.01 AO1 PTR to value P.01.05.

Bit pointer parameter

A bit pointer parameter points to the value of a bit in another parameter, or can be fixed to 0 (FALSE) or 1 (TRUE). In addition, bit pointer parameters often have a set of pre-selected choices.

When adjusting a bit pointer parameter on the optional control panel, CONST is selected in order to fix the value to 0 (displayed as “C.FALSE”) or 1 (“C.TRUE”). POINTER is selected to define a source from another parameter.

A pointer value is given in format **P.xx.yy.zz**, where xx = Parameter group, yy = Parameter index, zz = Bit number.

Example: Digital input DI5 status, 2.01 DI STATUS bit 4, is used for brake supervision by setting parameter 35.02 BRAKE ACKNOWL to value P.02.01.04.

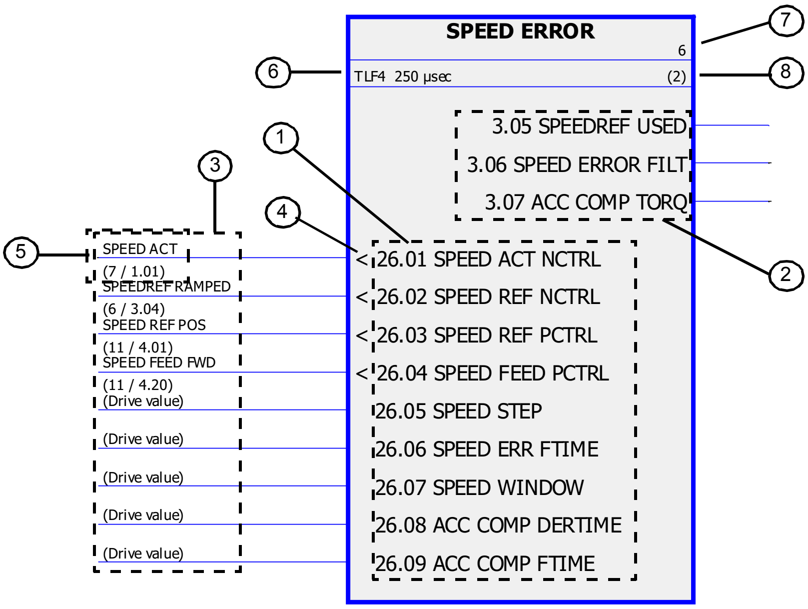
Note: Pointing to a nonexisting bit will be interpreted as 0 (FALSE).

For additional parameter data, e.g. update cycles and fieldbus equivalents, see chapter *Parameter data*.

Firmware blocks

Firmware blocks accessible from the DriveSPC PC tool are described in the parameter group most of the block inputs/outputs are included in. Whenever a block has inputs or outputs outside the current parameter group, a reference is given. Likewise, parameters have a reference to the firmware block they are included in (if any).

Note: Not all parameters are available through firmware blocks.



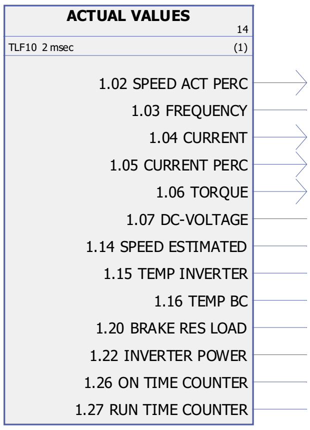
1	Inputs
2	Outputs
3	Input parameter values
4	Pointer parameter indicator “<“
5	Parameter 26.01 is set to value P.1.1, i.e. signal 1.01 SPEED ACT. The “7” means the signal can be found on page 7 of DriveSPC.
6	ID of the time level (TL4) and time level (250 µs). Time level, i.e. update cycle, is application-specific. See the time level of the block in DriveSPC.
7	Firmware block ID number in the application program
8	Firmware block execution order for the selected update cycle ID

Group 01 ACTUAL VALUES

This group contains basic actual signals for monitoring the drive.

01 ACTUAL VALUES

Firmware block:
ACTUAL VALUES
(1)



1.01	SPEED ACT	FW block: SPEED FEEDBACK (page 131)
	Filtered actual speed in rpm. Used speed feedback is defined by parameter 22.01 SPEED FB SEL. Filter time constant can be adjusted by parameter 22.02 SPEED ACT FTIME.	
1.02	SPEED ACT PERC	FW block: ACTUAL VALUES (see above)
	Actual speed in percent of the motor synchronous speed.	
1.03	FREQUENCY	FW block: ACTUAL VALUES (see above)
	Estimated drive output frequency in Hz.	
1.04	CURRENT	FW block: ACTUAL VALUES (see above)
	Measured motor current in A.	
1.05	CURRENT PERC	FW block: ACTUAL VALUES (see above)
	Motor current in percent of the nominal motor current.	
1.06	TORQUE	FW block: ACTUAL VALUES (see above)
	Motor torque in percent of the motor nominal torque.	
1.07	DC-VOLTAGE	FW block: ACTUAL VALUES (see above)
	Measured intermediate circuit voltage in V.	
1.08	ENCODER 1 SPEED	FW block: ENCODER (page 225)
	Encoder 1 speed in rpm.	
1.09	ENCODER 1 POS	FW block: ENCODER (page 225)
	Actual position of encoder 1 within one revolution.	

1.10	ENCODER 2 SPEED	FW block: ENCODER (page 225)
	Encoder 2 speed in rpm.	
1.11	ENCODER 2 POS	FW block: ENCODER (page 225)
	Actual position of encoder 2 within one revolution.	
1.12	POS ACT	FW block: POS FEEDBACK (page 192)
	Actual position of the encoder. The unit depends on parameter 60.05 POS UNIT selection.	
1.13	POS 2ND ENC	FW block: POS FEEDBACK (page 192)
	Scaled actual position of encoder 2 in revolutions.	
1.14	SPEED ESTIMATED	FW block: ACTUAL VALUES (see above)
	Estimated motor speed in rpm.	
1.15	TEMP INVERTER	FW block: ACTUAL VALUES (see above)
	Measured temperature of the heatsink in Celsius.	
1.16	TEMP BC	FW block: ACTUAL VALUES (see above)
	Brake chopper IGBT temperature in Celsius.	
1.17	MOTOR TEMP	FW block: MOT THERM PROT (page 169)
	Measured motor temperature in Celsius.	
1.18	MOTOR TEMP EST	FW block: MOT THERM PROT (page 169)
	Estimated motor temperature in Celsius.	
1.19	USED SUPPLY VOLT	FW block: VOLTAGE CTRL (page 176)
	Either the nominal supply voltage defined by parameter 47.04 SUPPLY VOLTAGE, or the automatically determined supply voltage if auto-identification is enabled by parameter 47.03 SUPPLVOLTAUTO-ID.	
1.20	BRAKE RES LOAD	FW block: ACTUAL VALUES (see above)
	Estimated temperature of the braking resistor. The value is given in percent of the temperature the resistor reaches when loaded with the power defined by parameter 48.04 BR POWER MAX CNT.	
1.21	CPU USAGE	FW block: None
	Microprocessor load in percent.	
1.22	INVERTER POWER	FW block: ACTUAL VALUES (see above)
	Drive output power in kilowatts.	
1.26	ON TIME COUNTER	FW block: ACTUAL VALUES (see above)
	This counter runs when the drive is powered. The counter can be reset using the DriveStudio tool.	

1.27	RUN TIME COUNTER	FW block: ACTUAL VALUES (see above)
	Motor run time counter. The counter run when the drive modulates. The counter can be reset using the DriveStudio tool.	
1.31	MECH TIME CONST	FW block: None
	Calculated mechanical time constant of the system as identified by the speed control autotuning routine.	

Group 02 I/O VALUES

This group contains information on the I/Os of the drive.

02 I/O VALUES		
2.01	DI STATUS	FW block: DI (page 114)
	Status word of the digital inputs. Example: 000001 = DI1 is on, DI2 to DI6 are off.	
2.02	RO STATUS	FW block: RO (page 114)
	Status of relay output. 1 = RO is energized.	
2.03	DIO STATUS	FW blocks: DIO1 (page 112), DIO2 (page 112), DIO3 (page 112)
	Status word of digital inputs/outputs DIO1…3. Example: 001 = DIO1 is on, DIO2 and DIO3 are off.	
2.04	AI1	FW block: AI1 (page 116)
	Analogue input AI1 value in V or mA. The type is selected with jumper J1 on the JCU Control Unit.	
2.05	AI1 SCALED	FW block: AI1 (page 116)
	Scaled value of analogue input AI1. See parameters 13.04 AI1 MAX SCALE and 13.05 AI1 MIN SCALE.	
2.06	AI2	FW block: AI2 (page 117)
	Analogue input AI2 value in V or mA. The type is selected with jumper J2 on the JCU Control Unit.	
2.07	AI2 SCALED	FW block: AI2 (page 117)
	Scaled value of analogue input AI2. See parameters 13.09 AI2 MAX SCALE and 13.10 AI2 MIN SCALE.	
2.08	AO1	FW block: AO1 (page 120)
	Analogue output AO1 value in mA	
2.09	AO2	FW block: AO2 (page 121)
	Analogue output AO2 value in V	
2.10	DIO2 FREQ IN	FW block: DIO2 (page 112)
	Frequency input value in Hz when DIO2 is used as frequency input (12.02 DIO2 CONF is set to (2) FREQ INPUT).	
2.11	DIO3 FREQ OUT	FW block: DIO3 (page 112)
	Frequency output value in Hz when DIO3 is used as frequency output (12.03 DIO3 CONF is set to (2) FREQ OUTPUT).	

2.12	FBA MAIN CW	FW block: FIELDBUS (page 180)

Control Word for fieldbus communication.

Log. = Logical combination (i.e. Bit AND/OR Selection parameter). Par. = Selection parameter. See *State diagram* on page *390.*

Bit	Name	Val.	Information	Log.	Par.
0	STOP*	1	Stop according to the stop mode selected by 11.03 STOP MODE or according to the requested stop mode (bits 2…6). **Note:** Simultaneous STOP and START commands result in a stop command.	OR	10.02, 10.03, 10.05, 10.06
		0	No operation		
1	START	1	Start. **Note:** Simultaneous STOP and START commands result in a stop command.	OR	10.02, 10.03, 10.05, 10.06
		0	No operation		
2	STPMODE EM OFF*	1	Emergency OFF2 (bit 0 must be 1): Drive is stopped by cutting off the motor power supply (the inverter IGBTs are blocked). The motor coasts to stop. The drive will restart only with the next rising edge of the start signal when the run enable signal is on.	AND	-
		0	No operation		
3	STPMODE EM STOP*	1	Emergency stop OFF3 (bit 0 must be 1): Stop within the time defined by 25.11 EM STOP TIME.	AND	10.10
		0	No operation		
4	STPMODE OFF1*	1	Emergency stop OFF1 (bit 0 must be 1): Stop along the currently active deceleration ramp.	AND	10.11
		0	No operation		
5	STPMODE RAMP*	1	Stop along the currently active deceleration ramp.	-	11.03
		0	No operation		
6	STPMODE COAST*	1	Coast to stop.	-	11.03
		0	No operation		
7	RUN ENABLE	1	Activate run enable.	AND	10.09
		0	Activate run disable.		
8	RESET	0->1	Fault reset if an active fault exists.	OR	10.08
		other	No operation		
9	JOGGING 1	1	Activate jogging function 1. See section *Jogging* on page *46*.	OR	10.07
		0	Jogging function 1 disabled		

* If all stop mode bits 2…6 are 0, stop mode is selected by 11.03 STOP MODE. Coast stop (bit 6) overrides the emergency stop (bit 2/3/4). Emergency stop overrides the normal ramp stop (bit 5).

2.12	FBA MAIN CW (continued from previous page)

Bit	Name	Val.	Information	Log.	Par.
10	JOGGING 2	1	Activate jogging function 2. See section *Jogging* on page *46*.	OR	10.14
		0	Jogging function 2 disabled		
11	REMOTE CMD	1	Fieldbus control enabled	-	-
		0	Fieldbus control disabled		
12	RAMP OUT 0	1	Force Ramp Function Generator output to zero. Drive ramps to a stop (current and DC voltage limits in force).	-	-
		0	No operation		
13	RAMP HOLD	1	Halt ramping (Ramp Function Generator output held).	-	-
		0	No operation		
14	RAMP IN 0	1	Force Ramp Function Generator input to zero.	-	-
		0	No operation		
15	EXT1/EXT2	1	Switch to external control location EXT2.	OR	34.01
		0	Switch to external control location EXT1.		
16	REQ STARTINH	1	Activate start inhibit.	-	-
		0	No start inhibit		
17	LOCAL CTL	1	Request local control for Control Word. Used when the drive is controlled via PC tool or panel or through local fieldbus. - Local fieldbus: Transfer to fieldbus local control (control via fieldbus control word or reference). Fieldbus steals the control. - Panel or PC tool: Transfer to local control.	-	-
		0	Request external control.		
18	FBLOCAL REF	1	Request fieldbus local control.	-	-
		0	No fieldbus local control		
19	ABS POSIT	1	Use absolute positioning.	OR	65.09, 65.17 bit 4
		0	Use relative positioning.		
20	POS START MODE	1	Select pulse start for positioning: Start by rising edge of a pulse.	OR	65.24
		0	Select normal start for positioning: Start by signal rising edge. The signal has to stay TRUE during the positioning task.		

2.12	FBA MAIN CW (continued from previous page)

Bit	Name	Val.	Information	Log.	Par.
21	POSITION-ING ENA	1	Enable position control.	OR	66.05
		0	Disable position control.		
22	PO REF LIM ENA	1	Enable position reference.	OR	70.03
		0	Disable position reference. Position reference speed limit is set to zero. Positioning task is rejected.		
23	Not in use				
24	CHG SET IMMED	1	Interrupt actual positioning and start next positioning.	-	-
		0	Finish actual positioning and then start next positioning.		
25	POS START	1	Activate positioning start. Operation depends on selected start mode (bit 20 POS START MODE).	OR	65.03, 65.11
		0	Deactivate positioning start.		
26	START HOMING	1	Start homing.	OR	62.03
		0	Normal operation.		
27	Not in use				
28	CW B28		Freely programmable control bits.	-	-
29	CW B29				
30	CW B30				
31	CW B31				

2.13	FBA MAIN SW	FW block: FIELDBUS (page 180)
	Status Word for fieldbus communication. See *State diagram* on page *390.*	

Bit	Name	Value	Information
0	READY	1	Drive is ready to receive start command.
		0	Drive is not ready.
1	ENABLED	1	External run enable signal is received.
		0	No external run enable signal is received.
2	RUNNING	1	Drive is modulating.
		0	Drive is not modulating.
3	REF RUNNING	1	Normal operation is enabled. Drive is running and following given reference.
		0	Normal operation is disabled. Drive is not following given reference (for example, modulating during magnetization).
4	EM OFF (OFF2)	1	Emergency OFF2 is active.
		0	Emergency OFF2 is inactive.
5	EM STOP (OFF3)	1	Emergency stop OFF3 (ramp stop) is active.
		0	Emergency OFF3 is inactive.
6	ACK STARTINH	1	Start inhibit is active.
		0	Start inhibit is inactive.
7	ALARM	1	An alarm is active. See chapter *Fault tracing*.
		0	No alarm is active.
8	AT SETPOINT	1	Drive is at setpoint. Actual value equals reference value (i.e. the difference between the actual speed and the speed reference is within the speed window defined by 26.07 SPEED WINDOW).
		0	Drive has not reached setpoint.
9	LIMIT	1	Operation is limited by torque limit (any torque limit).
		0	Operation is within torque limits.
10	ABOVE LIMIT	1	Actual speed exceeds the defined limit, 22.07 ABOVE SPEED LIM.
		0	Actual speed is within the defined limits.
11	EXT2 ACT	1	External control location EXT2 is active.
		0	External control location EXT1 is active.
12	LOCAL FB	1	Fieldbus local control is active.
		0	Fieldbus local control is inactive.
13	ZERO SPEED	1	Drive speed is below limit set by par. 22.05 ZERO SPEED LIMIT.
		0	Drive has not reached zero speed limit.
14	REV ACT	1	Drive is running in reverse direction.
		0	Drive is running in forward direction.
15	Not in use		
16	FAULT	1	Fault is active. See chapter *Fault tracing*.
		0	No fault is active.
17	LOCAL PANEL	1	Local control is active, i.e. drive is controlled from PC tool or control panel.
		0	Local control is inactive.

2.13	FBA MAIN SW (continued from previous page)

Bit	Name	Value	Information
18	FOLLOWING ERROR	1	The difference between the reference and the actual position is within the defined following error window 71.09 FOLLOW ERR WIN.
		0	The difference between the reference and the actual position is outside the defined following error window.
19	TGT REACHED	1	Target position is reached.
		0	Target position is not reached.
20	HOMING DONE	1	Homing sequence is completed.
		0	Homing sequence is not completed.
21	TRAV TASK ACK	1	New positioning task or setpoint is accepted.
		0	No operation
22	MOVING	1	Positioning task is active. Drive speed is < > 0.
		0	Positioning task is completed or drive is at standstill.
23	IP MODE ACTIVE	1	Position reference generator is active.
		0	Position reference generator is inactive.
24	REG LEVEL	1	Position latch signal 1 is active (source selected by parameter 62.15 TRIG PROBE1).
		0	Position latch signal 1 is inactive.
25	POSITIVE LIMIT	1	Positive limit switch is active (source selected by parameter 62.06 POS LIMIT SWITCH).
		0	Positive limit switch is inactive.
26	NEGATIVE LIMIT	1	Negative limit switch is active (source selected by parameter 62.05 NEG LIMIT SWITCH).
		0	Negative limit switch is inactive.
27	REQUEST CTL	1	Control word is requested from fieldbus.
		0	Control word is not requested from fieldbus.
28	SW B28		Programmable status bits (unless fixed by the used profile). See parameters 50.08…50.11 and the user manual of the fieldbus adapter.
29	SW B29		
30	SW B30		
31	SW B31		

2.14	FBA MAIN REF1	FW block: FIELDBUS (page 180)
	Scaled fieldbus reference 1. See parameter 50.04 FBA REF1 MODESEL.	
2.15	FBA MAIN REF2	FW block: FIELDBUS (page 180)
	Scaled fieldbus reference 2. See parameter 50.05 FBA REF2 MODESEL.	
2.16	FEN DI STATUS	FW block: ENCODER (page 225)
	Status of digital inputs of FEN-xx encoder interfaces in drive option Slots 1 and 2. Examples: 000001 (01h) = DI1 of FEN-xx in Slot 1 is ON, all others are OFF. 000010 (02h) = DI2 of FEN-xx in Slot 1 is ON, all others are OFF. 010000 (10h) = DI1 of FEN-xx in Slot 2 is ON, all others are OFF. 100000 (20h) = DI2 of FEN-xx in Slot 2 is ON, all others are OFF.	

2.17	D2D MAIN CW	FW block: D2D COMMUNICATION (page 187)
	Drive-to-drive control word received through the drive-to-drive link. See also actual signal 2.18 below.	

Bit	Information
0	Stop.
1	Start.
2	Reserved.
3	Reserved.
4	Reserved.
5	Reserved.
6	Reserved.
7	Run enable. By default, not connected in a follower drive.
8	Reset. By default, not connected in a follower drive.
9	Freely assignable through bit pointer parameters.
10	Freely assignable through bit pointer parameters.
11	Freely assignable through bit pointer parameters.
12	Freely assignable through bit pointer parameters.
13	Freely assignable through bit pointer parameters.
14	Freely assignable through bit pointer parameters.
15	EXT1/EXT2 selection. 0 = EXT1 active, 1 = EXT2 active. By default, not connected in a follower drive.

2.18	D2D FOLLOWER CW	FW block: DRIVE LOGIC (page 103)
	Drive-to-drive control word sent to the followers by default. See also firmware block D2D COMMUNICATION on page 187.	

Bit	Information
0	Stop.
1	Start.
2…6	Reserved.
7	Run enable.
8	Reset.
9…14	Reserved.
15	EXT1/EXT2 selection. 0 = EXT1 active, 1 = EXT2 active.

2.19	D2D REF1	FW block: D2D COMMUNICATION (page 187)
	Drive-to-drive reference 1 received through the drive-to-drive link.	
2.20	D2D REF2	FW block: D2D COMMUNICATION (page 187)
	Drive-to-drive reference 2 received through the drive-to-drive link.	

Group 03 CONTROL VALUES

Actual signals containing information on e.g. the reference.

03 CONTROL VALUES		
3.01	SPEED REF1	FW block: SPEED REF SEL (page 135)
	Speed reference 1 in rpm.	
3.02	SPEED REF2	FW block: SPEED REF SEL (page 135)
	Speed reference 2 in rpm.	
3.03	SPEEDREF RAMP IN	FW block: SPEED REF MOD (page 136)
	Used speed reference ramp input in rpm.	
3.04	SPEEDREF RAMPED	FW block: SPEED REF RAMP (page 139)
	Ramped and shaped speed reference in rpm.	
3.05	SPEEDREF USED	FW block: SPEED ERROR (page 143)
	Used speed reference in rpm (reference before the speed error calculation).	
3.06	SPEED ERROR FILT	FW block: SPEED ERROR (page 143)
	Filtered speed error value in rpm.	
3.07	ACC COMP TORQ	FW block: SPEED ERROR (page 143)
	Output of the acceleration compensation (torque in %).	
3.08	TORQ REF SP CTRL	FW block: SPEED CONTROL (page 148)
	Limited speed controller output torque in %.	
3.09	TORQ REF1	FW block: TORQ REF SEL (page 153)
	Torque reference 1 in %.	
3.10	TORQ REF RAMPED	FW block: TORQ REF MOD (page 154)
	Ramped torque reference in %.	
3.11	TORQ REF RUSHLIM	FW block: TORQ REF MOD (page 154)
	Torque reference limited by the rush control (value in %). Torque is limited to ensure that the speed is between the defined minimum and maximum speed limits (parameters 20.01 MAXIMUM SPEED and 20.02 MINIMUM SPEED).	
3.12	TORQUE REF ADD	FW block: TORQ REF SEL (page 153)
	Torque reference additive in %.	

3.13	TORQ REF TO TC	FW block: REFERENCE CTRL (page 160)
	Torque reference in % for the torque control. When 99.05 MOTOR CTRL MODE is set to (1) SCALAR, this value is forced to 0.	
3.14	BRAKE TORQ MEM	FW block: MECH BRAKE CTRL (page 163)
	Torque value (in %) stored when the mechanical brake close command is issued.	
3.15	BRAKE COMMAND	FW block: MECH BRAKE CTRL (page 163)
	Brake on/off command. 0 = Close. 1 = Open. For brake on/off control, connect this signal to a relay output (can also be connected to a digital output). See section *Mechanical brake* on page *49*.	
3.16	FLUX REF USED	FW block: MOTOR CONTROL (page 166)
	Used flux reference in percent.	
3.17	TORQUE REF USED	FW block: MOTOR CONTROL (page 166)
	Used/limited torque reference in percent.	

Group 04 POS CTRL VALUES

Actual signals containing positioning information.

04 POS CTRL VALUES		
4.01	SPEED REF POS	FW block: POS CONTROL (page 222)
	Position controller output (speed reference) for the speed controller in rpm.	
4.02	SPEED ACT LOAD	FW block: POS FEEDBACK (page 192)
	Filtered actual speed of the load. The unit depends on parameter 60.05 POS UNIT selection. If the load gear ratio is 1:1, 4.02 SPEED ACT LOAD equals 1.01 SPEED ACT.	
4.03	PROBE1 POS MEAS	FW block: HOMING (page 196)
	Measured position (triggered according to latch setting 62.15 TRIG PROBE1). The unit depends on parameter 60.05 POS UNIT selection.	
4.04	PROBE2 POS MEAS	FW block: HOMING (page 196)
	Measured position (triggered according to latch setting 62.17 TRIG PROBE2). The unit depends on parameter 60.05 POS UNIT selection. Used only with cyclic corrections.	
4.05	CYCLIC POS ERR	FW block: HOMING (page 196)
	Calculated cyclic position error for the cyclic correction function (error = reference latch position - measured latch position). The unit depends on parameter 60.05 POS UNIT selection. The error is added to synchron error (4.18 SYNC ERROR). Used only with cyclic corrections.	
4.06	POS REF	FW block: PROFILE REF SEL (page 204)
	Used position reference. The unit depends on parameter 60.05 POS UNIT selection.	
4.07	PROF SPEED	FW block: PROFILE REF SEL (page 204)
	Used positioning speed. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
4.08	PROF ACC	FW block: PROFILE REF SEL (page 204)
	Used positioning acceleration. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
4.09	PROF DEC	FW block: PROFILE REF SEL (page 204)
	Used positioning deceleration. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
4.10	PROF FILT TIME	FW block: PROFILE REF SEL (page 204)
	Used position reference filter time in ms.	
4.11	POS STYLE	FW block: PROFILE REF SEL (page 204)
	Used positioning behaviour. Defined by parameter 65.09 POS STYLE 1 / 65.17 POS STYLE 2.	

4.12	POS END SPEED	FW block: PROFILE REF SEL (page 204)
	Positioning speed used after the target has been reached.The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
4.13	POS REF IPO	FW block: PROFILE GENERATOR (page 212)
	Position reference from the position profile generator. The unit depends on parameter 60.05 POS UNIT selection.	
4.14	DIST TGT	FW block: PROFILE GENERATOR (page 212)
	Position profile generator distance to target. The unit depends on parameter 60.05 POS UNIT selection.	
4.15	SYNC REF UNGEAR	FW block: SYNC REF SEL (page 214)
	Ungeared synchron reference input. By default, this signal is connected to the input of the SYNC REF MOD firmware block (see page 217). The unit depends on parameter 60.05 POS UNIT selection.	
4.16	SYNC REF GEARED	FW block: SYNC REF MOD (page 217)
	Position reference in synchron control mode (output of the synchron reference chain). The unit depends on parameter 60.05 POS UNIT selection.	
4.17	POS REF LIMITED	FW block: POS REF LIM (page 219)
	Limited position reference. The unit depends on parameter 60.05 POS UNIT selection.	
4.18	SYNC ERROR	FW block: POS REF LIM (page 219)
	Synchronising error, caused by the dynamic limitations or the position correction, fed to the position profile generator. The unit depends on parameter 60.05 POS UNIT selection.	
4.19	POS ERROR	FW block: POS CONTROL (page 222)
	Position error. The unit depends on parameter 60.05 POS UNIT selection.	
4.20	SPEED FEED FWD	FW block: POS CONTROL (page 222)
	Position speed reference in rpm (from the dynamic limiter for the speed controller) multiplied with the speed feed forward gain (71.04 P CTRL FEED GAIN). To improve speed control, this reference is added to the position error (difference between the position reference and actual position).	

Group 06 DRIVE STATUS

Status words.

06 DRIVE STATUS		
6.01	STATUS WORD 1	FW block: DRIVE LOGIC (page 103)

Status word 1.

Bit	Name	Val.	Information
0	READY	1	Drive is ready to receive start command.
		0	Drive is not ready.
1	ENABLED	1	External run enable signal is received.
		0	No external run enable signal is received.
2	STARTED	1	Drive has received start command.
		0	Drive has not received start command.
3	RUNNING	1	Drive is modulating.
		0	Drive is not modulating.
4	EM OFF (OFF2)	1	Emergency OFF2 is active.
		0	Emergency OFF2 is inactive.
5	EM STOP (OFF3)	1	Emergency stop OFF3 (ramp stop) is active.
		0	Emergency OFF3 is inactive.
6	ACK STARTINH	1	Start inhibit is active.
		0	Start inhibit is inactive.
7	ALARM	1	An alarm is active. See chapter *Fault tracing*.
		0	No alarm
8	EXT2 ACT	1	External control EXT2 is active.
		0	External control EXT1 is active.
9	LOCAL FB	1	Fieldbus local control is active.
		0	Fieldbus local control is inactive.
10	FAULT	1	A fault is active. See chapter *Fault tracing*.
		0	No fault
11	LOCAL PANEL	1	Local control is active, i.e. drive is controlled from PC tool or control panel.
		0	Local control is inactive.
12…15	Reserved		

6.02	STATUS WORD 2	FW block: DRIVE LOGIC (page 103)

Status word 2.

Bit	Name	Val.	Information
0	START ACT	1	Drive start command is active.
		0	Drive start command is inactive.
1	STOP ACT	1	Drive stop command is active.
		0	Drive stop command is inactive.
2	READY RELAY	1	Ready to function: run enable signal on, no fault, emergency stop signal off, no ID run inhibition. Connected by default to DIO1 by par. 12.04 DIO1 OUT PTR. (Can be freely connected anywhere.)
		0	Not ready to function
3	MODULATING	1	Modulating: IGBTs are controlled, i.e. the drive is RUNNING.
		0	No modulation: IGBTs are not controlled.
4	REF RUNNING	1	Normal operation is enabled. Running. Drive follows the given reference.
		0	Normal operation is disabled, Drive is not following the given reference (e.g. in magnetisation phase drive is modulating).
5	JOGGING	1	Jogging function 1 or 2 is active.
		0	Jogging function is inactive.
6	OFF1	1	Emergency stop OFF1 is active.
		0	Emergency stop OFF1 is inactive.
7	START INH MASK	1	Maskable (by par. 10.12 START INHIBIT) start inhibit is active.
		0	No start inhibit (maskable)
8	START INH NOMASK	1	Non-maskable start inhibit is active.
		0	No start inhibit (non-maskable)
9	CHRG REL CLOSED	1	Charging relay is closed.
		0	Charging relay is open.
10	STO ACT	1	Safe Torque Off function is active. See parameter 46.07 STO DIAG-NOSTIC.
		0	Safe Torque Off function is inactive.
11	Reserved		
12	RAMP IN 0	1	Ramp Function Generator input is forced to zero.
		0	Normal operation
13	RAMP HOLD	1	Ramp Function Generator output is held.
		0	Normal operation
14	RAMP OUT 0	1	Ramp Function Generator output is forced to zero.
		0	Normal operation
15	Reserved		

6.03	SPEED CTRL STAT	FW block: DRIVE LOGIC (page 103)

Speed control status word.

Bit	Name	Val.	Information
0	SPEED ACT NEG	1	Actual speed is negative.
1	ZERO SPEED	1	Actual speed has reached the zero speed limit (22.05 ZERO SPEED LIMIT).
2	ABOVE LIMIT	1	Actual speed has exceeded the supervision limit (22.07 ABOVE SPEED LIM).
3	AT SETPOINT	1	The difference between the actual speed and the unramped speed reference is within the speed window (26.07 SPEED WIN-DOW).
4	BAL ACTIVE	1	Speed controller output balancing is active (28.09 SPEEDCTRL BAL EN).
5…15	Reserved		

6.05	LIMIT WORD 1	FW block: DRIVE LOGIC (page 103)

Limit word 1.

Bit	Name	Val.	Information
0	TORQ LIM	1	Drive torque is being limited by the motor control (undervoltage control, overvoltage control, current control, load angle control or pull-out control), or by parameter 20.06 MAXIMUM TORQUE or 20.07 MINIMUM TORQUE.
1	SPD CTL TLIM MIN	1	Speed controller output minimum torque limit is active. The limit is defined by parameter 28.10 MIN TORQ SP CTRL.
2	SPD CTL TLIM MAX	1	Speed controller output maximum torque limit is active. The limit is defined by parameter 28.11 MAX TORQ SP CTRL.
3	TORQ REF MAX	1	Torque reference (3.09 TORQ REF1) maximum limit is active. The limit is defined by parameter 32.04 MAXIMUM TORQ REF.
4	TORQ REF MIN	1	Torque reference (3.09 TORQ REF1) minimum limit is active. The limit is defined by parameter 32.05 MINIMUM TORQ REF.
5	TLIM MAX SPEED	1	Torque reference maximum value is limited by the rush control, because of maximum speed limit 20.01 MAXIMUM SPEED.
6	TLIM MIN SPEED	1	Torque reference minimum value is limited by the rush control, because of minimum speed limit 20.02 MINIMUM SPEED.
7…15	Reserved		

6.07	TORQ LIM STATUS	FW block: DRIVE LOGIC (page 103)		
	Torque controller limitation status word.			
	Bit	**Name**	**Val.**	**Information**
	0	UNDERVOLTAGE	1	Intermediate circuit DC undervoltage *
	1	OVERVOLTAGE	1	Intermediate circuit DC overvoltage *
	2	MINIMUM TORQUE	1	Torque reference minimum limit is active. The limit is defined by parameter 20.07 MINIMUM TORQUE. *
	3	MAXIMUM TORQUE	1	Torque reference maximum limit is active. The limit is defined by parameter 20.06 MAXIMUM TORQUE. *
	4	INTERNAL CURRENT	1	An inverter current limit is active. The limit is identified by bits 8…11.
	5	LOAD ANGLE	1	For permanent magnet motor only: Load angle limit is active, i.e. the motor cannot produce more torque.
	6	MOTOR PULLOUT	1	For asynchronous motor only: Motor pull-out limit is active, i.e. the motor cannot produce more torque.
	7	Reserved		
	8	THERMAL	1	Bit 4 = 0: Input current is limited by main circuit thermal limit. Bit 4 = 1: Output current is limited by main circuit thermal limit.
	9	SOA CURRENT	1	Internal Safe Operating Area current limit is active (limits the drive output current). **
	10	USER CURRENT	1	Maximum inverter output current limit is active. The limit is defined by parameter 20.05 MAXIMUM CURRENT. **
	11	THERMAL IGBT	1	Calculated thermal current value limits the inverter output current. Thermal current limitation is enabled by parameter 20.08 THERM CURR LIM. **
	12…15	Reserved		
	* Only one of bits 0…3 can be on simultaneously. The bit typically indicates the limit that is exceeded first. ** Only one of bits 9…11 can be on simultaneously. The bit typically indicates the limit that is exceeded first.			

6.09	POS CTRL STATUS	FW block: DRIVE LOGIC (page 103)

Position control status word.

Bit	Name	Val.	Information
0	IN POSITION	1	Position reference generator has reached the used position reference.
		0	Position reference generator is active, i.e. calculating the position reference.
1	IN POS WIN	1	Position is within the defined position window, 66.04 POS WIN.
		0	Position reference is outside the defined position window.
2	POS START	1	Positioning start command is active. Source for the start signal is selected by parameter 65.03 POS START 1 / 65.11 POS START 2.
		0	Position start command is inactive.
3	POS ENABLED	1	Position control is enabled by parameter 66.05 POS ENABLE or by fieldbus control word 2.12 FBA MAIN CW bit 21.
		0	Position control is not enabled.
4	MOVING	1	Positioning task is active. Drive speed is <> 0.
		0	Positioning task is completed or drive is at standstill.
5	TRAVERSE ACK	1	New positioning task or setpoint is accepted.
		0	No operation
6	IP MODE ACT	1	Position reference generator is active.
		0	Position reference generator is inactive.
7	FOLLOW ERR	1	The difference between the reference and the actual position is within the defined following error window 71.09 FOLLOW ERR WIN.
		0	The difference between the reference and the actual position is outside the defined following error window.
8	ABOVE MAX	1	Actual position (1.12 POS ACT) exceeds the defined maximum position, 60.13 MAXIMUM POS.
		0	Actual position does not exceed the maximum value.
9	BELOW MIN	1	Actual position (1.12 POS ACT) exceeds the defined minimum position, 60.14 MINIMUM POS.
		0	Actual position does not exceed the minimum value.
10	ABOVE THRES	1	Actual position (1.12 POS ACT) exceeds the position threshold supervision limit. The limit is defined by parameter 60.15 POS THRESHOLD.
		0	Actual position does not exceed the position threshold supervision limit.
11	Reserved		
12	PREF SPD LIM	1	Position reference speed is limited to the value defined by parameter 70.04 POS SPEED LIM.
		0	Position reference speed is not limited.
13	PREF ACC LIM	1	Position reference acceleration is limited to the value defined by parameter 70.05 POS ACCEL LIM.
		0	Position reference acceleration is not limited.
14	PREF DEC LIM	1	Position reference deceleration is limited to the value defined by parameter 70.06 POS DECEL LIM.
		0	Position reference deceleration is not limited.
15	Reserved		

6.10	POS CTRL STATUS2	FW block: DRIVE LOGIC (page 103)

Additional position control status word.

Bit	Name	Val.	Information
0*	IN SYNC POS	1	Position profile generator distance to target is below the absolute value of the synchron error limit, i.e. value of actual signal 4.14 DIST TGT is smaller than value of parameter 70.07 SYNC ERR LIM.
		0	Distance to target is greater than synchron error limit.
1*	IN SYNC	1	The difference of synchronous speed and drive load speed (4.02 SPEED ACT LOAD) is below the defined velocity window (70.08 SYNC VEL WINDOW).
		0	The system is not in synchron as defined by the synchron velocity window (70.08 SYNC VEL WINDOW).
2	END SPEED ACTIVE	1	Positioning end speed (defined by parameter 65.10 POS END SPEED 1 or 65.18 POS END SPEED 2 depending on selected position reference set) has been reached.
		0	Positioning end speed has not been reached or end speed is defined as zero.
3…15	Reserved		

* Active in synchron control.

6.11	POS CORR STATUS	FW block: DRIVE LOGIC (page 103)

Position correction status word.

Bit	Name	Val.	Information
0	HOMING START	1	Homing start is active. Source for the homing start is selected by parameter 62.03 HOMING START.
		0	Homing start is inactive.
1	HOMING DONE	1	Homing has been performed.
		0	Homing has not been performed (if bit 2 = 0) or homing is being executed.
2	HOM DONE ONCE	1	Homing has been performed at least once.
		0	Homing has not been performed after power up or there is an error with the actual position encoder.
3	COR DONE ONCE	1	Cyclic correction has been performed at least once (62.14 CYCLIC CORR MODE).
		0	Cyclic correction has not been performed after power up or there is an error with the actual position encoder.
4	POS LIM POS	1	Positive limit switch is active (source selected by parameter 62.06 POS LIMIT SWITCH).
		0	Positive limit switch is inactive.
5	POS LIM NEG	1	Negative limit switch is active (source selected by parameter 62.05 NEG LIMIT SWITCH).
		0	Negative limit switch is inactive.
6	LATCH1 STAT	1	Position latch signal 1 is active (source selected by parameter 62.15 TRIG PROBE1).
		0	Position latch signal 1 is inactive.
7	LATCH2 STAT	1	Position latch signal 2 is active (source selected by parameter 62.17 TRIG PROBE2).
		0	Position latch signal 2 is inactive.
8	LATCH1 DONE	1	Position has been latched according to parameter 62.15 TRIG PROBE1 setting.
		0	No position latch has occurred.
9	LATCH2 DONE	1	Position has been latched according to parameter 62.17 TRIG PROBE2 setting.
		0	No position latch has occurred.
10	Reserved		
11	POSIT AFTER HOM	1	Drive is executing absolute positioning according to par. 62.10 HOME POS OFFSET after home position has been found and set.
		0	The drive has not reached home position yet.
12	CYC CORR ACTIV	1	Cyclic correction is active.
		0	Cyclic correction is inactive.
13…15	Reserved		

6.12	OP MODE ACK	FW block: REFERENCE CTRL (page 160)

Operation mode acknowledge: 0 = STOPPED, 1 = SPEED, 2 = TORQUE, 3 = MIN, 4 = MAX, 5 = ADD, 6 = POSITION, 7 = SYNCHRON, 8 = HOMING, 9 = PROF VEL, 10 = SCALAR, 11 = FORCED MAGN (i.e. DC Hold).

6.14	SUPERV STATUS	FW block: SUPERVISION (page 156)

Supervision status word. See also parameter group 33 SUPERVISION (page 156).

Bit	Name	Val.	Information
0	SUPERV FUNC1 STATUS	1	Supervision function 1 is active (below low limit or over high limit)
1	SUPERV FUNC2 STATUS	1	Supervision function 2 is active (below low limit or over high limit)
2	SUPERV FUNC3 STATUS	1	Supervision function 3 is active (below low limit or over high limit)
3…15	Reserved		

Group 08 ALARMS & FAULTS

Signals containing alarm and fault information.

08 ALARMS & FAULTS		
8.01	ACTIVE FAULT	FW block: FAULT FUNCTIONS (page 173)
	Fault code of the latest (active) fault.	
8.02	LAST FAULT	FW block: FAULT FUNCTIONS (page 173)
	Fault code of the 2nd latest fault.	
8.03	FAULT TIME HI	FW block: FAULT FUNCTIONS (page 173)
	Time (real time or power-on time) at which the active fault occurred in format dd.mm.yy (=day.month.year).	
8.04	FAULT TIME LO	FW block: FAULT FUNCTIONS (page 173)
	Time (real time or power-on time) at which the active fault occurred in format hh.mm.ss (hours.minutes.seconds).	
8.05	ALARM WORD 1	FW block: FAULT FUNCTIONS (page 173)
	Alarm word 1. For possible causes and remedies, see chapter *Fault tracing*.	

Bit	**Alarm**
0	BRAKE START TORQUE
1	BRAKE NOT CLOSED
2	BRAKE NOT OPEN
3	SAFE TORQUE OFF
4	STO MODE CHANGE
5	MOTOR TEMPERATURE
6	EMERGENCY OFF
7	RUN ENABLE
8	ID-RUN
9	EMERGENCY STOP
10	POSITION SCALING
11	BR OVERHEAT
12	BC OVERHEAT
13	DEVICE OVERTEMP
14	INTBOARD OVERTEMP
15	BC MOD OVERTEMP

8.06	ALARM WORD 2	FW block: FAULT FUNCTIONS (page 173)

Alarm word 2. For possible causes and remedies, see chapter *Fault tracing*.

Bit	**Alarm**
0	IGBT OVERTEMP
1	FIELDBUS COMM
2	LOCAL CTRL LOSS
3	AI SUPERVISION
4	Reserved
5	NO MOTOR DATA
6	ENCODER 1 FAILURE
7	ENCODER 2 FAILURE
8	LATCH POS 1 FAILURE
9	LATCH POS 2 FAILURE
10	ENC EMULATION FAILURE
11	FEN TEMP MEAS FAILURE
12	ENC EMUL MAX FREQ
13	ENC EMUL REF ERROR
14	RESOLVER AUTOTUNE ERR
15	ENCODER 1 CABLE

8.07	ALARM WORD 3	FW block: FAULT FUNCTIONS (page 173)

Alarm word 3. For possible causes and remedies, see chapter *Fault tracing*.

Bit	**Alarm**
0	ENCODER 2 CABLE
1	D2D COMMUNICATION
2	D2D BUF OVLOAD
3	PS COMM
4	RESTORE
5	CUR MEAS CALIBRATION
6	AUTOPHASING
7	EARTH FAULT
8	Reserved
9	MOTOR NOM VALUE
10	D2D CONFIG
11…14	Reserved
15	SPEED FEEDBACK

8.08	ALARM WORD 4	FW block: FAULT FUNCTIONS (page 173)

Alarm word 4. For possible causes and remedies, see chapter *Fault tracing*.

Bit	**Alarm**
0	OPTION COMM LOSS
1…15	Reserved

Group 09 SYSTEM INFO

Drive type, firmware version, option slot information.

09 SYSTEM INFO		
9.01	DRIVE TYPE	FW block: None
	Displays the drive application type. **(1)** ACSM1 SPEED: Speed and torque control application **(2)** ACSM1 MOTION: Motion control application	
9.02	DRIVE RATING ID	FW block: None
	Displays the inverter type of the drive. **(0)** UNCONFIGURED, **(1)** ACSM1-xxAx-02A5-4, **(2)** ACSM1-xxAx-03A0-4, **(3)** ACSM1-xxAx-04A0-4, **(4)** ACSM1-xxAx-05A0-4, **(5)** ACSM1-xxAx-07A0-4, **(6)** ACSM1-xxAx-09A5-4, **(7)** ACSM1-xxAx-012A-4, **(8)** ACSM1-xxAx-016A-4, **(9)** ACSM1-xxAx-024A-4, **(10)** ACSM1-xxAx-031A-4, **(11)** ACSM1-xxAx-040A-4, **(12)** ACSM1-xxAx-046A-4, **(13)** ACSM1-xxAx-060A-4, **(14)** ACSM1-xxAx-073A-4, **(15)** ACSM1-xxAx-090A-4, **(20)** ACSM1-xxAx-110A-4, **(21)** ACSM1-xxAx-135A-4, **(22)** ACSM1-xxAx-175A-4, **(23)** ACSM1-xxAx-210A-4, **(24)** ACSM1-xxCx-024A-4, **(25)** ACSM1-xxCx-031A-4, **(26)** ACSM1-xxCx-040A-4, **(27)** ACSM1-xxCx-046A-4, **(28)** ACSM1-xxCx-060A-4, **(29)** ACSM1-xxCx-073A-4, **(30)** ACSM1-xxCx-090A-4, **(31)** ACSM1-xxLx-110A-4, **(32)** ACSM1-xxLx-135A-4, **(33)** ACSM1-xxLx-175A-4, **(34)** ACSM1-xxLx-210A-4, **(35)** ACSM1-xxLx-260A-4	
9.03	FIRMWARE ID	FW block: None
	Displays the firmware name. E.g. UMFI.	
9.04	FIRMWARE VER	FW block: None
	Displays the version of the firmware package in the drive, e.g. 0x1460 (1460 hex).	
9.05	FIRMWARE PATCH	FW block: None
	Displays the version of the firmware patch in the drive.	
9.10	INT LOGIC VER	FW block: None
	Displays the version of the logic in the power unit interface.	
9.20	OPTION SLOT 1	FW block: None
	Displays the type of the optional module in option Slot 1. **(0)** NO OPTION, **(1)** NO COMM, **(2)** UNKNOWN, **(3)** FEN-01, **(4)** FEN-11, **(5)** FEN-21, **(6)** FIO-01, **(7)** FIO-11, **(8)** FPBA-01, **(9)** FPBA-02, **(10)** FCAN-01, **(11)** FDNA-01, **(12)** FENA-01, **(13)** FENA-02, **(14)** FLON-01, **(15)** FRSA-00, **(16)** FMBA-01, **(17)** FFOA-01, **(18)** FFOA-02, **(19)** FSEN-01, **(20)** FEN-31, **(21)** FIO-21	
9.21	OPTION SLOT 2	FW block: None
	Displays the type of the optional module in option Slot 2. See signal 9.20 OPTION SLOT 1.	

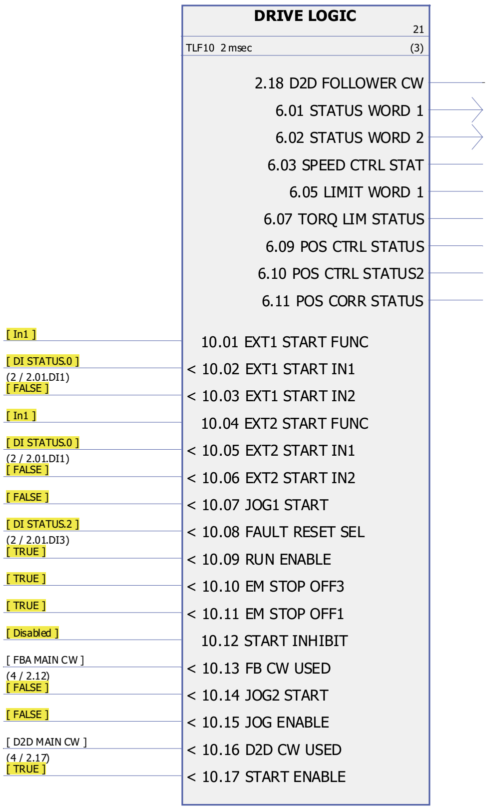
9.22	OPTION SLOT 3	FW block: None
	Displays the type of the optional module in option Slot 3. See signal 9.20 OPTION SLOT 1.	

Group 10 START/STOP

Settings for

- selecting start/stop/direction signal sources for external control locations EXT1 and EXT2
- selecting sources for external fault reset, run enable and start enable signals
- selecting sources for emergency stop (OFF1 and OFF3)
- selecting source for jogging function activation signal
- enabling the start inhibit function.

See also section *Jogging* on page 46.

10 START/STOP	
Firmware block: **DRIVE LOGIC** (10) This block • selects the sources for the start/stop/direction signals for external control locations EXT1 and EXT2 • selects the sources for external fault reset, run enable and start enable signals • selects the sources for emergency stop (OFF1 and OFF3) • selects the source for jogging activation signal • enables the start inhibit function.	

<table>
<tr><td colspan="2">Block outputs located in other parameter groups</td><td>2.18 D2D FOLLOWER CW (page 86)
6.01 STATUS WORD 1 (page 91)
6.02 STATUS WORD 2 (page 92)
6.03 SPEED CTRL STAT (page 93)
6.05 LIMIT WORD 1 (page 93)
6.07 TORQ LIM STATUS (page 94)
6.09 POS CTRL STATUS (page 95)
6.10 POS CTRL STATUS2 (page 96)
6.11 POS CORR STATUS (page 97)</td></tr>
<tr><td>10.01</td><td>EXT1 START FUNC</td><td>FW block: DRIVE LOGIC (see above)</td></tr>
<tr><td></td><td colspan="2">Selects the source for the start and stop control in external control location EXT1.
Note: This parameter cannot be changed while the drive is running.</td></tr>
<tr><td></td><td>(0) NOT SEL</td><td>No source selected.</td></tr>
<tr><td></td><td>(1) IN1</td><td>Source of the start and stop commands are selected by parameter 10.02 EXT1 START IN1. The start/stop is controlled as follows:
<table>
<tr><th>Par. 10.02</th><th>Command</th></tr>
<tr><td>0 -> 1</td><td>Start</td></tr>
<tr><td>1 -> 0</td><td>Stop</td></tr>
</table></td></tr>
<tr><td></td><td>(2) 3-WIRE</td><td>Source of the start and stop commands are selected by parameters 10.02 EXT1 START IN1 and 10.03 EXT1 START IN2. The start/stop is controlled as follows:
<table>
<tr><th>Par. 10.02</th><th>Par. 10.03</th><th>Command</th></tr>
<tr><td>0 -> 1</td><td>1</td><td>Start</td></tr>
<tr><td>Any</td><td>1 -> 0</td><td>Stop</td></tr>
<tr><td>Any</td><td>0</td><td>Stop</td></tr>
</table></td></tr>
<tr><td></td><td>(3) FBA</td><td>Start and stop control from the source selected by parameter 10.13 FB CW USED.</td></tr>
<tr><td></td><td>(4) D2D</td><td>Start and stop control from another drive via D2D Control Word.</td></tr>
<tr><td></td><td>(5) IN1F IN2R</td><td>The source selected by 10.02 EXT1 START IN1 is the forward start signal, the source selected by 10.03 EXT1 START IN2 is the reverse start signal.
<table>
<tr><th>Par. 10.02</th><th>Par. 10.03</th><th>Command</th></tr>
<tr><td>0</td><td>0</td><td>Stop</td></tr>
<tr><td>1</td><td>0</td><td>Start forward</td></tr>
<tr><td>0</td><td>1</td><td>Start reverse</td></tr>
<tr><td>1</td><td>1</td><td>Stop</td></tr>
</table></td></tr>
<tr><td></td><td>(6) IN1S IN2DIR</td><td>The source selected by 10.02 EXT1 START IN1 is the start signal (0 = stop, 1 = start), the source selected by 10.03 EXT1 START IN2 is the direction signal (0 = forward, 1 = reverse).</td></tr>
</table>

<table>
<tr><td>10.02</td><td>EXT1 START IN1</td><td>FW block: DRIVE LOGIC (see above)</td></tr>
<tr><td></td><td colspan="2">Selects the source 1 for the start and stop commands in external control location EXT1. See parameter 10.01 EXT1 START FUNC selections (1) IN1 and (2) 3-WIRE.
Note: This parameter cannot be changed while the drive is running.</td></tr>
<tr><td></td><td colspan="2">Bit pointer: Group, index and bit</td></tr>
<tr><td>10.03</td><td>EXT1 START IN2</td><td>FW block: DRIVE LOGIC (see above)</td></tr>
<tr><td></td><td colspan="2">Selects the source 2 for the start and stop commands in external control location EXT1. See parameter 10.01 EXT1 START FUNC selection (2) 3-WIRE.
Note: This parameter cannot be changed while the drive is running.</td></tr>
<tr><td></td><td colspan="2">Bit pointer: Group, index and bit</td></tr>
<tr><td>10.04</td><td>EXT2 START FUNC</td><td>FW block: DRIVE LOGIC (see above)</td></tr>
<tr><td></td><td colspan="2">Selects the source for the start and stop control in external control location EXT2.
Note: This parameter cannot be changed while the drive is running.</td></tr>
<tr><td></td><td>(0) NOT SEL</td><td>No source selected.</td></tr>
<tr><td></td><td>(1) IN1</td><td>Source of the start and stop commands are selected by parameter 10.05 EXT2 START IN1. The start/stop is controlled as follows:
<table>
<tr><th>Par. 10.05</th><th>Command</th></tr>
<tr><td>0 -> 1</td><td>Start</td></tr>
<tr><td>1 -> 0</td><td>Stop</td></tr>
</table></td></tr>
<tr><td></td><td>(2) 3-WIRE</td><td>Source of the start and stop commands are selected by parameters 10.05 EXT2 START IN1 and 10.06 EXT2 START IN2. The start/stop is controlled as follows:
<table>
<tr><th>Par. 10.05</th><th>Par. 10.06</th><th>Command</th></tr>
<tr><td>0 -> 1</td><td>1</td><td>Start</td></tr>
<tr><td>Any</td><td>1 -> 0</td><td>Stop</td></tr>
<tr><td>Any</td><td>0</td><td>Stop</td></tr>
</table></td></tr>
<tr><td></td><td>(3) FBA</td><td>Start and stop control from the source selected by parameter 10.13 FB CW USED.</td></tr>
<tr><td></td><td>(4) D2D</td><td>Start and stop control from another drive via D2D Control Word.</td></tr>
<tr><td></td><td>(5) IN1F IN2R</td><td>The source selected by 10.05 EXT2 START IN1 is the forward start signal, the source selected by 10.06 EXT2 START IN2 is the reverse start signal.
<table>
<tr><th>Par. 10.05</th><th>Par. 10.06</th><th>Command</th></tr>
<tr><td>0</td><td>0</td><td>Stop</td></tr>
<tr><td>1</td><td>0</td><td>Start forward</td></tr>
<tr><td>0</td><td>1</td><td>Start reverse</td></tr>
<tr><td>1</td><td>1</td><td>Stop</td></tr>
</table></td></tr>
</table>

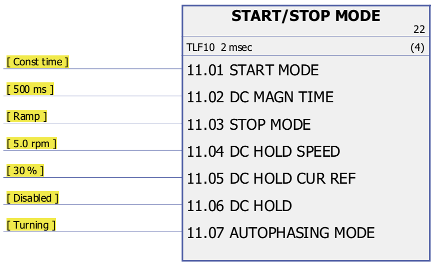
	(6) IN1S IN2DIR	The source selected by 10.05 EXT2 START IN1 is the start signal (0 = stop, 1 = start), the source selected by 10.06 EXT2 START IN2 is the direction signal (0 = forward, 1 = reverse).
10.05	EXT2 START IN1	FW block: DRIVE LOGIC (see above)
	Selects the source 1 for the start and stop commands in external control location EXT2. See parameter 10.04 EXT2 START FUNC selections (1) IN1 and (2) 3-WIRE. **Note:** This parameter cannot be changed while the drive is running.	
	Bit pointer: Group, index and bit	
10.06	EXT2 START IN2	FW block: DRIVE LOGIC (see above)
	Selects the source 2 for the start and stop commands in external control location EXT2. See parameter 10.04 EXT2 START FUNC selection (2) 3-WIRE. **Note:** This parameter cannot be changed while the drive is running.	
	Bit pointer: Group, index and bit	
10.07	JOG1 START	FW block: DRIVE LOGIC (see above)
	If enabled by parameter 10.15 JOG ENABLE, selects the source for the activation of jogging function 1. 1 = Active. (Jogging function 1 can also be activated through fieldbus regardless of parameter 10.15.) See section *Jogging* on page *46*. See also other jogging function parameters: 10.14 JOG2 START, 10.15 JOG ENABLE, 24.03 SPEED REF1 IN / 24.04 SPEED REF2 IN, 24.10 SPEED REF JOG1, 24.11 SPEED REF JOG2, 25.09 ACC TIME JOGGING, 25.10 DEC TIME JOGGING and 22.06 ZERO SPEED DELAY. **Note:** This parameter cannot be changed while the drive is running.	
	Bit pointer: Group, index and bit	
10.08	FAULT RESET SEL	FW block: DRIVE LOGIC (see above)
	Selects the source for the external fault reset signal. The signal resets the drive after a fault trip if the cause of the fault no longer exists. 1 = Fault reset.	
	Bit pointer: Group, index and bit	
10.09	RUN ENABLE	FW block: DRIVE LOGIC (see above)
	Selects the source for the run enable signal. If the run enable signal is switched off, the drive will not start or stops if the drive is running. 1 = Run enable. **Note:** This parameter cannot be changed while the drive is running.	
	Bit pointer: Group, index and bit	
10.10	EM STOP OFF3	FW block: DRIVE LOGIC (see above)
	Selects the source for the emergency stop OFF3. 0 = OFF3 active: The drive is stopped along the emergency stop ramp time, 25.11 EM STOP TIME. Emergency stop can also be activated through fieldbus (2.12 FBA MAIN CW). See section *Emergency stop* on page 71. **Note:** This parameter cannot be changed while the drive is running.	
	Bit pointer: Group, index and bit	

10.11	EM STOP OFF1	FW block: DRIVE LOGIC (see above)
	Selects the source for the emergency stop OFF1. 0 = OFF1 active: The drive is stopped with the active deceleration time. Emergency stop can also be activated through fieldbus (2.12 FBA MAIN CW). See section *Emergency stop* on page 71. **Note:** This parameter cannot be changed while the drive is running.	
	Bit pointer: Group, index and bit	
10.12	START INHIBIT	FW block: DRIVE LOGIC (see above)
	Enables the start inhibit function. The start inhibit function prevents drive restart (i.e. protects against unexpected start) if • drive trips on a fault and fault is reset. • run enable signal activates while the start command is active. See parameter 10.09 RUN ENABLE. • control changes from local to remote. • external control switches from EXT1 to EXT2 or from EXT2 to EXT1. An active start inhibit can be reset with a stop command. Note that in certain applications it is necessary to allow the drive to restart.	
	(0) DISABLED	Start inhibit function disabled.
	(1) ENABLED	Start inhibit function enabled.
10.13	FB CW USED	FW block: DRIVE LOGIC (see above)
	Selects the source for the control word when fieldbus (FBA) is selected as the external start and stop control location (see parameters 10.01 EXT1 START FUNC and 10.04 EXT2 START FUNC). By default, the source is parameter 2.12 FBA MAIN CW. **Note:** This parameter cannot be changed while the drive is running.	
	Value pointer: Group and index	
10.14	JOG2 START	FW block: DRIVE LOGIC (see above)
	If enabled by parameter 10.15 JOG ENABLE, selects the source for the activation of jogging function 2. 1 = Active. (Jogging function 2 can also be activated through fieldbus regardless of parameter 10.15.) **Note:** This parameter cannot be changed while the drive is running.	
	Bit pointer: Group, index and bit	
10.15	JOG ENABLE	FW block: DRIVE LOGIC (see above)
	Selects the source for enabling parameters 10.07 JOG1 START and 10.14 JOG2 START. **Note:** Jogging can be enabled using this parameter only when no start command from an external control location is active. On the other hand, if jogging is already enabled, the drive cannot be started from an external control location apart from jog commands through fieldbus.	
	Bit pointer: Group, index and bit	
10.16	D2D CW USED	FW block: DRIVE LOGIC (see above)
	Selects the source for the control word for drive-to-drive communication. By default, the source is parameter 2.17 D2D MAIN CW.	

	Value pointer: Group and index	
10.17	START ENABLE	FW block: DRIVE LOGIC (see above)
	Selects the source for the start enable signal. If the start enable signal is switched off, the drive will not start or stops if the drive is running. 1 = Start enable. **Note:** This parameter cannot be changed while the drive is running.	
	Bit pointer: Group, index and bit	

Group 11 START/STOP MODE

These parameters select the start and stop functions as well as the autophasing mode, define the DC magnetising time of the motor, and configure the DC hold function.

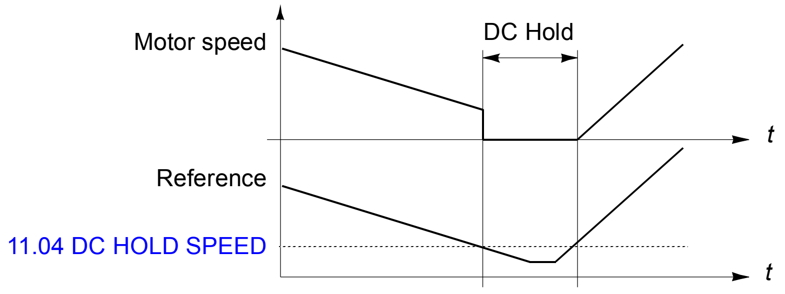
11 START/STOP MODE		
Firmware block: **START/STOP MODE** (11)		
11.01	START MODE	FW block: START/STOP MODE (see above)
	Selects the motor start function. **Notes:** • Selections (0) FAST and (1) CONST TIME are ignored if parameter 99.05 MOTOR CTRL MODE is set to (1) SCALAR. • Starting to a rotating machine is not possible when DC magnetising is selected ((0) FAST or (1) CONST TIME). • With permanent magnet motors, automatic start must be used. • This parameter cannot be changed while the drive is running.	
	(0) FAST	DC magnetising should be selected if a high break-away torque is required. The drive pre-magnetises the motor before the start. The pre-magnetising time is determined automatically, being typically 200 ms to 2 s depending on the motor size.
	(1) CONST TIME	Constant DC magnetising should be selected instead of DC magnetising if constant pre-magnetising time is required (e.g. if the motor start must be simultaneous with a mechanical brake release). This selection also guarantees the highest possible break-away torque when the pre-magnetising time is set long enough. The pre-magnetising time is defined by parameter 11.02 DC MAGN TIME. **WARNING!** The drive will start after the set magnetising time has passed even if motor magnetisation is not completed. In applications where a full break-away torque is essential, ensure that the constant magnetising time is long enough to allow generation of full magnetisation and torque.
	(2) AUTOMATIC	Automatic start guarantees optimal motor start in most cases. It includes the flying start function (starting to a rotating machine) and the automatic restart function (stopped motor can be restarted immediately without waiting the motor flux to die away). The drive motor control program identifies the flux as well as the mechanical state of the motor and starts the motor instantly under all conditions. **Note:** If parameter 99.05 MOTOR CTRL MODE is set to (1) SCALAR, no flying start or automatic restart is possible by default.

<table>
<tr><td>11.02</td><td>DC MAGN TIME</td><td>FW block: START/STOP MODE (see above)</td></tr>
<tr><td></td><td colspan="2">Defines the constant DC magnetising time. See parameter 11.01 START MODE. After the start command, the drive automatically premagnetises the motor the set time.
To ensure full magnetising, set this value to the same value as or higher than the rotor time constant. If not known, use the rule-of-thumb value given in the table below:
<table>
<tr><th>Motor rated power</th><th>Constant magnetising time</th></tr>
<tr><td>< 1 kW</td><td>≥ 50 to 100 ms</td></tr>
<tr><td>1 to 10 kW</td><td>≥ 100 to 200 ms</td></tr>
<tr><td>10 to 200 kW</td><td>≥ 200 to 1000 ms</td></tr>
<tr><td>200 to 1000 kW</td><td>≥ 1000 to 2000 ms</td></tr>
</table>
Note: This parameter cannot be changed while the drive is running.</td></tr>
<tr><td></td><td>0…10000 ms</td><td>DC magnetising time.</td></tr>
<tr><td>11.03</td><td>STOP MODE</td><td>FW block: START/STOP MODE (see above)</td></tr>
<tr><td></td><td colspan="2">Selects the motor stop function.</td></tr>
<tr><td></td><td>(1) COAST</td><td>Stop by cutting of the motor power supply. The motor coasts to a stop.
WARNING! If the mechanical brake is used, ensure it is safe to stop the drive by coasting. For more information on mechanical brake function, see parameter group 35 MECH BRAKE CTRL.</td></tr>
<tr><td></td><td>(2) RAMP</td><td>Stop along ramp. See parameter group 25 SPEED REF RAMP.</td></tr>
<tr><td>11.04</td><td>DC HOLD SPEED</td><td>FW block: START/STOP MODE (see above)</td></tr>
<tr><td></td><td colspan="2">Defines the DC hold speed. See parameter 11.06 DC HOLD.</td></tr>
<tr><td></td><td>0…1000 rpm</td><td>DC hold speed.</td></tr>
<tr><td>11.05</td><td>DC HOLD CUR REF</td><td>FW block: START/STOP MODE (see above)</td></tr>
<tr><td></td><td colspan="2">Defines the DC hold current in percent of the motor nominal current. See parameter 11.06 DC HOLD.</td></tr>
<tr><td></td><td>0…100%</td><td>DC hold current.</td></tr>
</table>

11.06	DC HOLD	FW block: START/STOP MODE (see above)

Enables the DC hold function. The function makes it possible to lock the rotor at zero speed.

When both the reference and the speed drop below the value of parameter 11.04 DC HOLD SPEED, the drive will stop generating sinusoidal current and start to inject DC into the motor. The current is set by parameter 11.05 DC HOLD CUR REF. When the reference speed exceeds parameter 11.04 DC HOLD SPEED, normal drive operation continues.



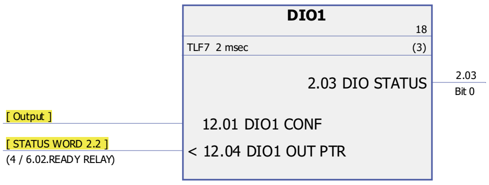
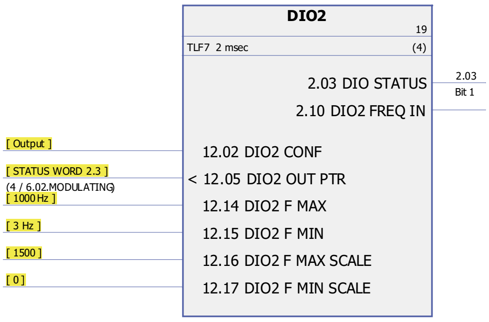
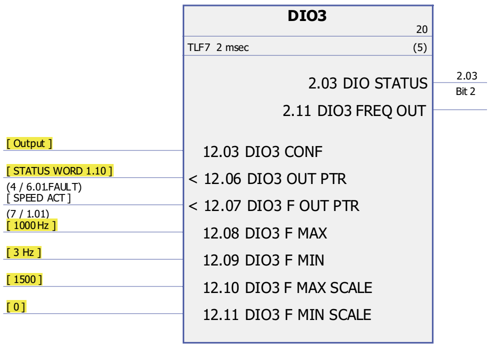
Notes:

- The DC hold function has no effect if the start signal is switched off.
- The DC hold function can only be activated in speed control mode.
- The DC hold function cannot be activated if par. 99.05 MOTOR CTRL MODE is set to (1) SCALAR.
- Injecting DC current into the motor causes the motor to heat up. In applications where long DC hold times are required, externally ventilated motors should be used. If the DC hold period is long, the DC hold cannot prevent the motor shaft from rotating if a constant load is applied to the motor.

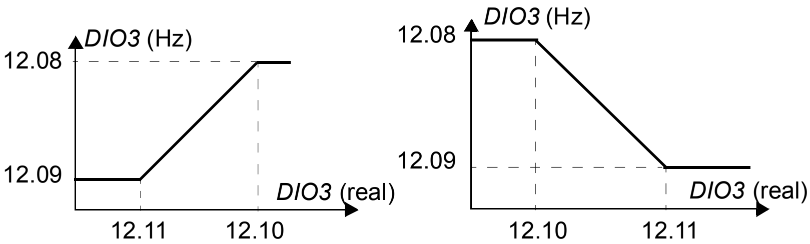
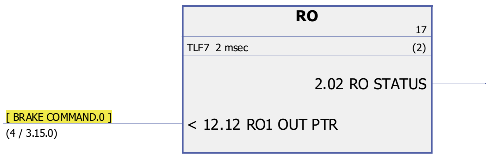
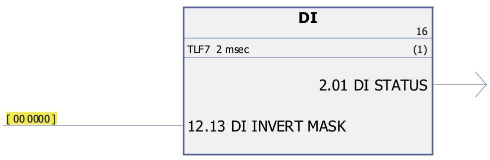
	(0) DISABLED	DC hold function disabled.
	(1) ENABLED	DC hold function enabled.
11.07	AUTOPHASING MODE	FW block: START/STOP MODE (see above)
	Selects the way autophasing is performed during the ID run. See also section *Autophasing* on page 40.	
	(0) TURNING	This mode gives the most accurate autophasing result. This mode can be used, and is recommended, if it is allowed for the motor to rotate during the ID run and the start-up is not time-critical. **Note:** This mode will cause the motor to rotate during the ID run.
	(1) STANDSTILL 1	Faster than the (0) TURNING mode, but not as accurate. The motor will not rotate.
	(2) STANDSTILL 2	An alternative standstill autophasing mode that can be used if the TURNING mode cannot be used, and the (1) STANDSTILL 1 mode gives erratic results. However, this mode is considerably slower than (1) STANDSTILL 1.

Group 12 DIGITAL IO

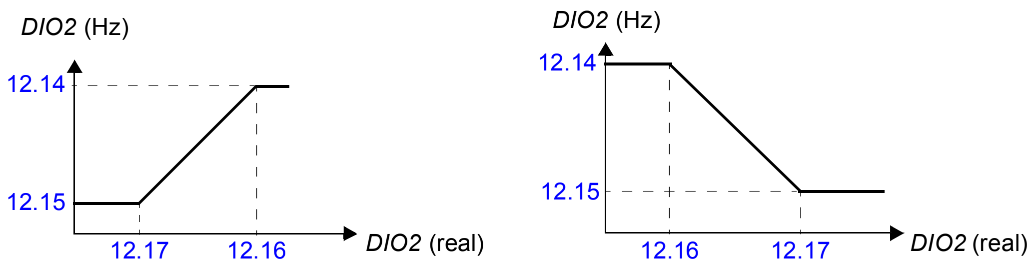
Settings for the digital inputs and outputs, and the relay output.

12 DIGITAL IO		
Firmware block: **DIO1** (6) Selects whether DIO1 is used as a digital input or as a digital output and connects an actual signal to the digital output. The block also shows the DIO status.		
Block outputs located in other parameter groups		2.03 DIO STATUS (page 80)
Firmware block: **DIO2** (7) Selects whether DIO2 is used as a digital or frequency input or as a digital output and connects an actual signal to the digital output. The block also shows the DIO status. Frequency input can be scaled with standard function blocks. See chapter *Standard function blocks*.		
Block outputs located in other parameter groups		2.03 DIO STATUS (page 80) 2.10 DIO2 FREQ IN (page 80)
Firmware block: **DIO3** (8) Selects whether DIO3 is used as a digital input or as a digital/frequency output, connects an actual signal to the digital/frequency output and scales the frequency output. The block also shows the DIO status.		
Block outputs located in other parameter groups		2.03 DIO STATUS (page 80) 2.11 DIO3 FREQ OUT (page 80)
12.01	DIO1 CONF	FW block: DIO1 (see above)
	Selects whether DIO1 is used as a digital input or as a digital output.	
	(0) OUTPUT	DIO1 is used as a digital output.

	(1) INPUT	DIO1 is used as a digital input.
12.02	DIO2 CONF	FW block: DIO2 (see above)
	Selects whether DIO2 is used as a digital input, as a digital output or as a frequency input.	
	(0) OUTPUT	DIO2 is used as a digital output.
	(1) INPUT	DIO2 is used as a digital input.
	(2) FREQ INPUT	DIO2 is used as a frequency input.
12.03	DIO3 CONF	FW block: DIO3 (see above)
	Selects whether DIO3 is used as a digital input, as a digital output or as a frequency output.	
	(0) OUTPUT	DIO2 is used as a digital output.
	(1) INPUT	DIO2 is used as a digital input.
	(2) FREQ OUTPUT	DIO2 is used as a frequency output.
12.04	DIO1 OUT PTR	FW block: DIO1 (see above)
	Selects a drive signal to be connected to digital output DIO1 (when 12.01 DIO1 CONF is set to (0) OUTPUT).	
	Bit pointer: Group, index and bit	
12.05	DIO2 OUT PTR	FW block: DIO2 (see above)
	Selects a drive signal to be connected to digital output DIO2 (when 12.02 DIO2 CONF is set to (0) OUTPUT).	
	Bit pointer: Group, index and bit	
12.06	DIO3 OUT PTR	FW block: DIO3 (see above)
	Selects a drive signal to be connected to digital output DIO3 (when 12.03 DIO3 CONF is set to (0) OUTPUT).	
	Bit pointer: Group, index and bit	
12.07	DIO3 F OUT PTR	FW block: DIO3 (see above)
	Selects a drive signal to be connected to frequency output (when 12.03 DIO3 CONF is set to (2) FREQ OUTPUT).	
	Value pointer: Group and index	
12.08	DIO3 F MAX	FW block: DIO3 (see above)
	Defines the maximum value for frequency output (when 12.03 DIO3 CONF is set to (2) FREQ OUTPUT).	
	3…32768 Hz	Maximum DIO3 output frequency.

12.09	DIO3 F MIN	FW block: DIO3 (see above)
	Defines the minimum value for frequency output (when 12.03 DIO3 CONF is set to (2) FREQ OUTPUT).	
	3…32768 Hz	Minimum DIO3 output frequency.
12.10	DIO3 F MAX SCALE	FW block: DIO3 (see above)
	Defines the real value that corresponds to the maximum frequency output value defined by parameter 12.08 DIO3 F MAX. 	
	0…32768	Real value corresponding to value of parameter 12.08.
12.11	DIO3 F MIN SCALE	FW block: DIO3 (see above)
	Defines the real value that corresponds to the minimum frequency output value defined by parameter 12.09 DIO3 F MIN. See parameter 12.10 DIO3 F MAX SCALE.	
	0…32768	Real value corresponding to value of parameter 12.09.
Firmware block: **RO** (5) Connects an actual signal to the relay output. The block also shows the relay output status.		
Block outputs located in other parameter groups		2.02 RO STATUS (page 80)
12.12	RO1 OUT PTR	FW block: RO (see above)
	Selects a drive signal to be connected to relay output RO1.	
	Bit pointer: Group, index and bit	
Firmware block: **DI** (4) Shows the status of the digital inputs. Inverts the status of any DI if desired.		
Block outputs located in other parameter groups		2.01 DI STATUS (page 80)

12.13	DI INVERT MASK	FW block: DI (see above)
	Inverts status of digital inputs as reported by 2.01 DI STATUS. For example, a value of 0b000100 inverts the status of DI3 in the signal.	
	0b000000…0b111111	DI status inversion mask.
12.14	DIO2 F MAX	FW block: DIO2 (see above)
	Defines the maximum value for frequency input (when 12.02 DIO2 CONF is set to (2) FREQ INPUT).	
	3…32768 Hz	Maximum DIO2 input frequency.
12.15	DIO2 F MIN	FW block: DIO2 (see above)
	Defines the minimum value for frequency input (when 12.02 DIO2 CONF is set to (2) FREQ INPUT).	
	3…32768 Hz	Minimum DIO2 input frequency.
12.16	DIO2 F MAX SCALE	FW block: DIO2 (see above)
	Defines the real value that corresponds to the maximum frequency input value defined by parameter 12.14 DIO2 F MAX.	



	-32768…32768	Real value corresponding to value of parameter 12.14.
12.17	DIO2 F MIN SCALE	FW block: DIO2 (see above)
	Defines the real value that corresponds to the minimum frequency input value defined by parameter 12.15 DIO2 F MIN. See parameter 12.16 DIO2 F MAX SCALE.	
	-32768…32768	Real value corresponding to value of parameter 12.15.

Group 13 ANALOGUE INPUTS

Settings for the analogue inputs.

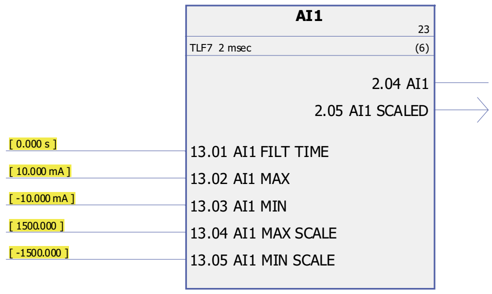
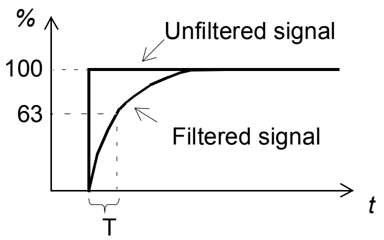
The drive offers two programmable analogue inputs, AI1 and AI2. Both inputs can be used either as a voltage or a current input (-11…11 V or -22…22 mA). The input type is selected with jumpers J1 and J2 respectively on the JCU Control Unit.

The inaccuracy of the analogue inputs is 1% of the full scale range and the resolution is 11 bits (+ sign). The hardware filter time constant is approximately 0.25 ms.

Analogue inputs can be used as the source for speed and torque reference.

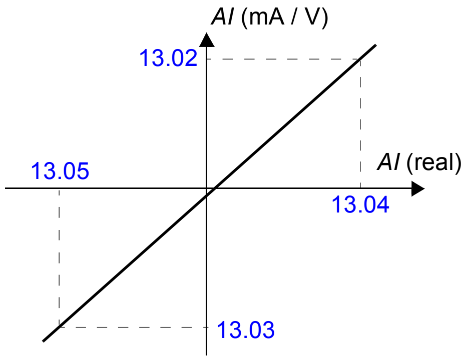
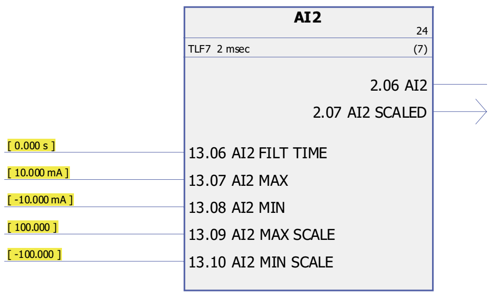
Analogue input supervision can be added with standard function blocks. See chapter Standard function blocks.

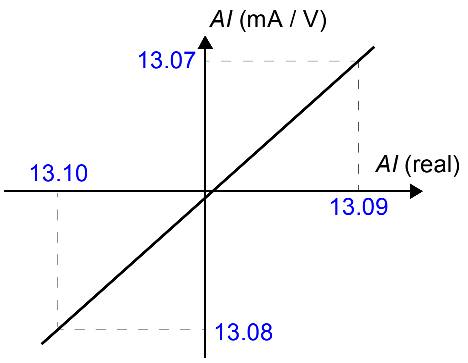
<table>
<tr><td colspan="3">13 ANALOGUE INPUTS</td></tr>
<tr><td colspan="2">Firmware block:
AI1
(12)

Filters and scales the analogue input AI1 signal and selects the AI1 supervision. Also shows the value of the input.</td><td>
</td></tr>
<tr><td colspan="2">Block outputs located in other parameter groups</td><td>2.04 AI1 (page 80)
2.05 AI1 SCALED (page 80)</td></tr>
<tr><td>13.01</td><td>AI1 FILT TIME</td><td>FW block: AI1 (see above)</td></tr>
<tr><td></td><td colspan="2">Defines the filter time constant for analogue input AI1.


$O = I \cdot (1 - e^{-t/T})$
I = filter input (step)
O = filter output
t = time
T = filter time constant

Note: The signal is also filtered due to the signal interface hardware (approximately 0.25 ms time constant). This cannot be changed by any parameter.</td></tr>
<tr><td></td><td>0…30 s</td><td>Filter time constant for AI1.</td></tr>
<tr><td>13.02</td><td>AI1 MAX</td><td>FW block: AI1 (see above)</td></tr>
<tr><td></td><td colspan="2">Defines the maximum value for analogue input AI1. The type is selected with jumper J1 on the JCU Control Unit.</td></tr>
<tr><td></td><td>-11…11 V / -22…22 mA</td><td>Maximum AI1 input value.</td></tr>
</table>

13.03	AI1 MIN	FW block: AI1 (see above)
	Defines the minimum value for analogue input AI1. The type is selected with jumper J1 on the JCU Control Unit.	
	-11…11 V / -22…22 mA	Minimum AI1 input value.
13.04	AI1 MAX SCALE	FW block: AI1 (see above)
	Defines the real value that corresponds to the maximum analogue input value defined by parameter 13.02 AI1 MAX. 	
	-32768…32768	Real value corresponding to value of parameter 13.02.
13.05	AI1 MIN SCALE	FW block: AI1 (see above)
	Defines the real value that corresponds to the minimum analogue input value defined by parameter 13.03 AI1 MIN. See parameter 13.04 AI1 MAX SCALE.	
	-32768…32768	Real value corresponding to value of parameter 13.03.
Firmware block: **AI2** (13) Filters and scales the analogue input AI2 signal and selects the AI2 supervision. Also shows the value of the input.		
Block outputs located in other parameter groups		2.06 AI2 (page 80) 2.07 AI2 SCALED (page 80)
13.06	AI2 FILT TIME	FW block: AI2 (see above)
	Defines the filter time constant for analogue input AI2. See parameter 13.01 AI1 FILT TIME.	
	0…30 s	Filter time constant for AI2.
13.07	AI2 MAX	FW block: AI2 (see above)
	Defines the maximum value for analogue input AI2. The type is selected with jumper J2 on the JCU Control Unit.	

	-11…11 V / -22…22 mA	Maximum AI2 input value.
13.08	AI2 MIN	FW block: AI2 (see above)
	Defines the minimum value for analogue input AI2. The type is selected with jumper J2 on the JCU Control Unit.	
	-11…11 V / -22…22 mA	Minimum AI2 input value.
13.09	AI2 MAX SCALE	FW block: AI2 (see above)
	Defines the real value that corresponds to the maximum analogue input value defined by parameter 13.07 AI2 MAX. 	
	-32768…32768	Real value corresponding to value of parameter 13.07.
13.10	AI2 MIN SCALE	FW block: AI2 (see above)
	Defines the real value that corresponds to the minimum analogue input value defined by parameter 13.08 AI2 MIN. See parameter 13.09 AI2 MAX SCALE.	
	-32768…32768	Real value corresponding to value of parameter 13.08.
13.11	AITUNE	FW block: None
	Triggers the AI tuning function. Connect the signal to the input and select the appropriate tuning function.	
	(0) NO ACTION	AI tune is not activated.
	(1) AI1 MIN TUNE	Current analogue input AI1 signal value is set as minimum value for AI1, parameter 13.03 AI1 MIN. The value reverts back to (0) NO ACTION automatically.
	(2) AI1 MAX TUNE	Current analogue input AI1 signal value is set as maximum value for AI1, parameter 13.02 AI1 MAX. The value reverts back to (0) NO ACTION automatically.
	(3) AI2 MIN TUNE	Current analogue input AI2 signal value is set as minimum value for AI2, parameter 13.08 AI2 MIN. The value reverts back to (0) NO ACTION automatically.
	(4) AI2 MAX TUNE	Current analogue input AI2 signal value is set as maximum value for AI2, parameter 13.07 AI2 MAX. The value reverts back to (0) NO ACTION automatically.

13.12	AI SUPERVISION	FW block: None
	Selects how the drive reacts when analogue input signal limit is reached. The limit is selected by parameter 13.13 AI SUPERVIS ACT.	
	(0) NO	No action taken.
	(1) FAULT	The drive trips on fault AI SUPERVISION.
	(2) SPD REF SAFE	The drive generates alarm AI SUPERVISION and sets the speed to the speed defined by parameter 46.02 SPEED REF SAFE. **WARNING!** Make sure that it is safe to continue operation in case of a communication break.
	(3) LAST SPEED	The drive generates alarm AI SUPERVISION and freezes the speed to the level the drive was operating at. The speed is determined by the average speed over the previous 10 seconds. **WARNING!** Make sure that it is safe to continue operation in case of a communication break.
13.13	AI SUPERVIS ACT	FW block: None
	Selects the analogue input signal supervision limit.	

Bit		Supervision selected by parameter 13.12 AI SUPERVISION is activated if
0	AI1<min	AI1 signal value falls below the value defined by equation: par. 13.03 AI1 MIN - 0.5 mA or V
1	AI1>max	AI1 signal value exceeds the value defined by equation: par. 13.02 AI1 MAX + 0.5 mA or V
2	AI2<min	AI2 signal value falls below the value defined by equation: par. 13.08 AI2 MIN - 0.5 mA or V
3	AI2>min	AI2 signal value exceeds the value defined by equation: par. 13.07 AI2 MAX + 0.5 mA or V

Example: If parameter value is set to 0010 (bin), bit 1 AI1>max is selected.

	0b0000…0b1111	AI1/AI2 signal supervision selection.

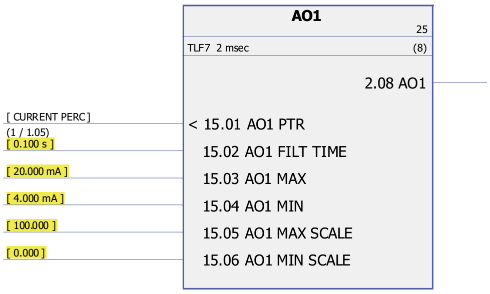
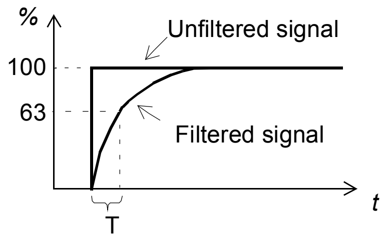
Group 15 ANALOGUE OUTPUTS

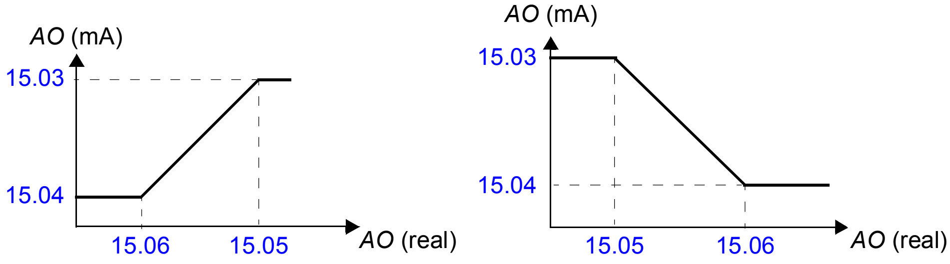
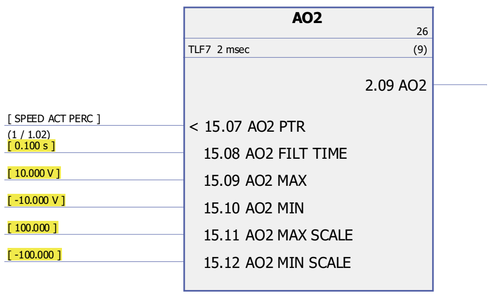
Settings for the analogue outputs.

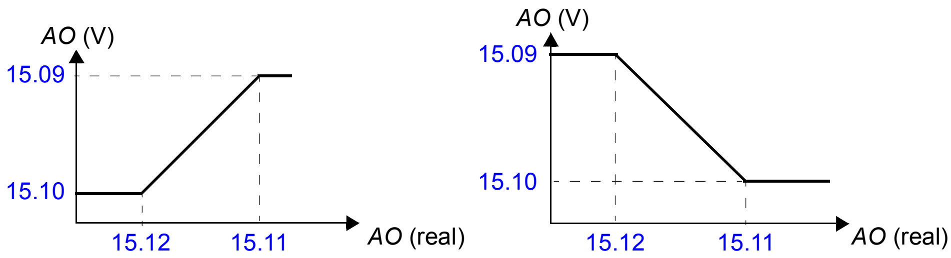
The drive offers two programmable analogue outputs: one current output AO1 (0…20 mA) and one voltage output AO2 (-10…10 V).

The resolution of the analogue outputs is 11 bits (+ sign) and the inaccuracy is 2% of the full scale range.

The analogue output signals can be proportional to motor speed, process speed (scaled motor speed), output frequency, output current, motor torque, motor power, etc. It is possible to write a value to an analogue output through a serial communication link (e.g. fieldbus link).

15 ANALOGUE OUTPUTS		
Firmware block: **AO1** (14) Connects an actual signal to analogue output AO1, and filters and scales the output signal. Also shows the value of the output.		
Block outputs located in other parameter groups		2.08 AO1 (page 80)
15.01	AO1 PTR	FW block: AO1 (see above)
	Selects a drive signal to be connected to analogue output AO1.	
	Value pointer: Group and index	
15.02	AO1 FILT TIME	FW block: AO1 (see above)
	Defines the filtering time constant for analogue output AO1.  $O = I \cdot (1 - e^{-t/T})$ I = filter input (step) O = filter output t = time T = filter time constant **Note:** The signal is also filtered due to the signal interface hardware (approximately 0.5 ms time constant). This cannot be changed by any parameter.	
	0…30 s	Filter time constant for AO1.
15.03	AO1 MAX	FW block: AO1 (see above)
	Defines the maximum value for analogue output AO1.	

	0…22.7 mA	Maximum AO1 output value.
15.04	AO1 MIN	FW block: AO1 (see above)
	Defines the minimum value for analogue output AO1.	
	0…22.7 mA	Minimum AO1 output value.
15.05	AO1 MAX SCALE	FW block: AO1 (see above)
	Defines the real value that corresponds to the maximum analogue output value defined by parameter 15.03 AO1 MAX. 	
	-32768…32767	Real value corresponding to value of parameter 15.03.
15.06	AO1 MIN SCALE	FW block: AO1 (see above)
	Defines the real value that corresponds to the minimum analogue output value defined by parameter 15.04 AO1 MIN. See parameter 15.05 AO1 MAX SCALE.	
	-32768…32767	Real value corresponding to value of parameter 15.04.
Firmware block: **AO2** (15) Connects an actual signal to analogue output AO2, and filters and scales the output signal. Also shows the value of the output.		
Block outputs located in other parameter groups		2.09 AO2 (page 80)
15.07	AO2 PTR	FW block: AO2 (see above)
	Selects a drive signal to be connected to analogue output AO2.	
	Value pointer: Group and index	
15.08	AO2 FILT TIME	FW block: AO2 (see above)
	Defines the filtering time constant for analogue output AO2. See parameter 15.02 AO1 FILT TIME.	
	0…30 s	Filter time constant for AO2.

15.09	AO2 MAX	FW block: AO2 (see above)
	Defines the maximum value for analogue output AO2.	
	-10…10 V	Maximum AO2 output value.
15.10	AO2 MIN	FW block: AO2 (see above)
	Defines the minimum value for analogue output AO2.	
	-10…10 V	Minimum AO2 output value.
15.11	AO2 MAX SCALE	FW block: AO2 (see above)
	Defines the real value that corresponds to the maximum analogue output value defined by parameter 15.09 AO2 MAX. 	
	-32768…32767	Real value corresponding to value of parameter 15.09.
15.12	AO2 MIN SCALE	FW block: AO2 (see above)
	Defines the real value that corresponds to the minimum analogue output value defined by parameter 15.10 AO2 MIN. See parameter 15.11 AO2 MAX SCALE.	
	-32768…32767	Real value corresponding to value of parameter 15.10.

Group 16 SYSTEM

Local control and parameter access settings, restoration of default parameter values, save of parameters into permanent memory.

16 SYSTEM		
16.01	LOCAL LOCK	FW block: None
	Selects the source for disabling local control (Take/Release button on the PC tool, LOC/REM key of the panel). 1 = Local control disabled. 0 = Local control enabled. **WARNING!** Before activating, ensure that the control panel is not needed for stopping the drive!	
	Bit pointer: Group, index and bit	
16.02	PARAMETER LOCK	FW block: None
	Selects the state of the parameter lock. The lock prevents parameter changing. **Note:** This parameter can only be adjusted after the correct pass code has been entered at parameter 16.03 PASS CODE.	
	(0) LOCKED	Locked. Parameter values cannot be changed from the control panel.
	(1) OPEN	The lock is open. Parameter values can be changed.
	(2) NOT SAVED	The lock is open. Parameter values can be changed, but the changes will not be stored at power switch off.
16.03	PASS CODE	FW block: None
	After entering 358 at this parameter, parameter 16.02 PARAMETER LOCK can be adjusted. The value reverts back to 0 automatically.	
16.04	PARAM RESTORE	FW block: None
	Restores the original settings of the application, i.e. parameter factory default values. **Note:** This parameter cannot be changed while the drive is running.	
	(0) DONE	Restoration is completed.
	(1) RESTORE DEFS	All parameter values are restored to default values, except motor data, ID run results, and fieldbus, drive-to-drive link and encoder configuration data.
	(2) CLEAR ALL	All parameter values are restored to default values, including motor data, ID run results and fieldbus and encoder configuration data. PC tool communication is interrupted during the restoration. Drive CPU is re-booted after the restoration is completed.
16.07	PARAM SAVE	FW block: None
	Saves the valid parameter values to the permanent memory. **Note:** A new parameter value is saved automatically when changed from the PC tool or panel but not when altered through a fieldbus connection.	

	(0) DONE	Save completed.
	(1) SAVE	Save in progress.
16.09	USER SET SEL	FW block: None
	Enables the save and restoration of up to four custom sets of parameter settings. The set that was in use before powering down the drive is in use after the next power-up. **Note:** Any parameter changes made after loading a set are not automatically stored – they must be saved using this parameter.	
	(1) NO REQUEST	Load or save operation complete; normal operation.
	(2) LOAD SET 1	Load user parameter set 1.
	(3) LOAD SET 2	Load user parameter set 2.
	(4) LOAD SET 3	Load user parameter set 3.
	(5) LOAD SET 4	Load user parameter set 4.
	(6) SAVE SET 1	Save user parameter set 1.
	(7) SAVE SET 2	Save user parameter set 2.
	(8) SAVE SET 3	Save user parameter set 3.
	(9) SAVE SET 4	Save user parameter set 4.
	(10) IO MODE	Load user parameter set using parameters 16.11 and 16.12.
16.10	USER SET LOG	FW block: None
	Shows the status of the user parameter sets (see parameter 16.09 USER SET SEL). Read-only.	
	N/A	No user sets have been saved.
	(1) LOADING	A user set is being loaded.
	(2) SAVING	A user set is being saved.
	(4) FAULTED	Invalid or empty parameter set.
	(8) SET1 IO ACT	User parameter set 1 has been selected by parameters 16.11 and 16.12.
	(16) SET2 IO ACT	User parameter set 2 has been selected by parameters 16.11 and 16.12.
	(32) SET3 IO ACT	User parameter set 3 has been selected by parameters 16.11 and 16.12.
	(64) SET4 IO ACT	User parameter set 4 has been selected by parameters 16.11 and 16.12.
	(128) SET1 PAR ACT	User parameter set 1 has been loaded using parameter 16.09.
	(256) SET2 PAR ACT	User parameter set 2 has been loaded using parameter 16.09.

	(512) SET3 PAR ACT	User parameter set 3 has been loaded using parameter 16.09.
	(1024) SET4 PAR ACT	User parameter set 4 has been loaded using parameter 16.09.
16.11	USER IO SET LO	FW block: None
	Together with parameter 16.12 USER IO SET HI, selects the user parameter set when parameter 16.09 USER SET SEL is set to (10) IO MODE. The status of the source defined by this parameter and parameter 16.12 select the user parameter set as follows:	
	Bit pointer: Group, index and bit	
16.12	USER IO SET HI	FW block: None
	See parameter 16.11 USER IO SET LO.	
	Bit pointer: Group, index and bit	
16.13	TIME SOURCE PRIO	FW block: None
	Selects which real-time clock source is adopted by the drive as the master real-time clock. Some selections specify multiple sources that are in order of priority.	
	(0) FB_D2D_MMI	Fieldbus (highest priority); drive-to-drive link; man-machine interface (control panel or PC).
	(1) D2D_FB_MMI	Drive-to-drive link (highest priority); fieldbus; man-machine interface (control panel or PC).
	(2) FB_D2D	Fieldbus (highest priority); drive-to-drive link.
	(3) D2D_FB	Drive-to-drive link (highest priority); fieldbus.
	(4) FB ONLY	Fieldbus only.
	(5) D2D ONLY	Drive-to-drive link only.
	(6) MMI_FB_D2D	Man-machine interface (control panel or PC) (highest priority); fieldbus; drive-to-drive link.
	(7) MMI ONLY	Man-machine interface (control panel or PC) only.
	(8) INTERNAL	No external sources are used as master real-time clock.

Table for parameter 16.11:

Status of source defined by par. 16.11	Status of source defined by par. 16.12	User parameter set selected
FALSE	FALSE	Set 1
TRUE	FALSE	Set 2
FALSE	TRUE	Set 3
TRUE	TRUE	Set 4

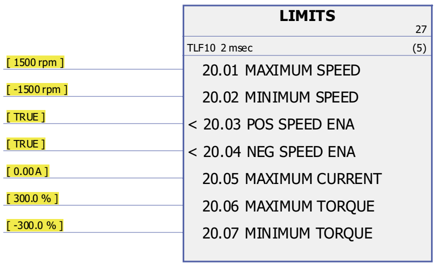
Group 17 PANEL DISPLAY

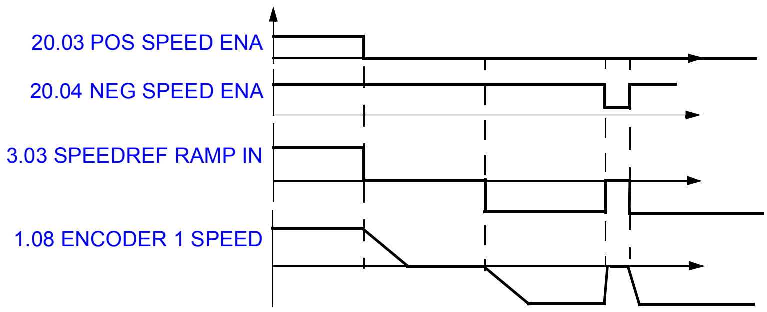
Selection of signals for panel display.

17 PANEL DISPLAY		
17.01	SIGNAL1 PARAM	FW block: None
	Selects the first signal to be displayed on the control panel. The default signal is 1.03 FREQUENCY.	
	Value pointer: Group and index	
17.02	SIGNAL2 PARAM	FW block: None
	Selects the second signal to be displayed on the control panel. The default signal is 1.04 CURRENT.	
	Value pointer: Group and index	
17.03	SIGNAL3 PARAM	FW block: None
	Selects the third signal to be displayed on the control panel. The default signal is 1.06 TORQUE.	
	Value pointer: Group and index	

Group 20 LIMITS

Definition of drive operation limits.

20 LIMITS		
Firmware block: **LIMITS** (20) Adjusts the drive speed, current and torque limits, selects the source for the positive/negative speed reference enable command and enables the thermal current limitation.		
20.01	MAXIMUM SPEED	FW block: LIMITS (see above).
	Defines the allowed maximum speed. See also parameter 22.08 SPEED TRIPMARGIN.	
	0…30000 rpm	Allowed maximum speed.
20.02	MINIMUM SPEED	FW block: LIMITS (see above).
	Defines the allowed minimum speed. See also parameter 22.08 SPEED TRIPMARGIN.	
	-30000…0 rpm	Allowed minimum speed.

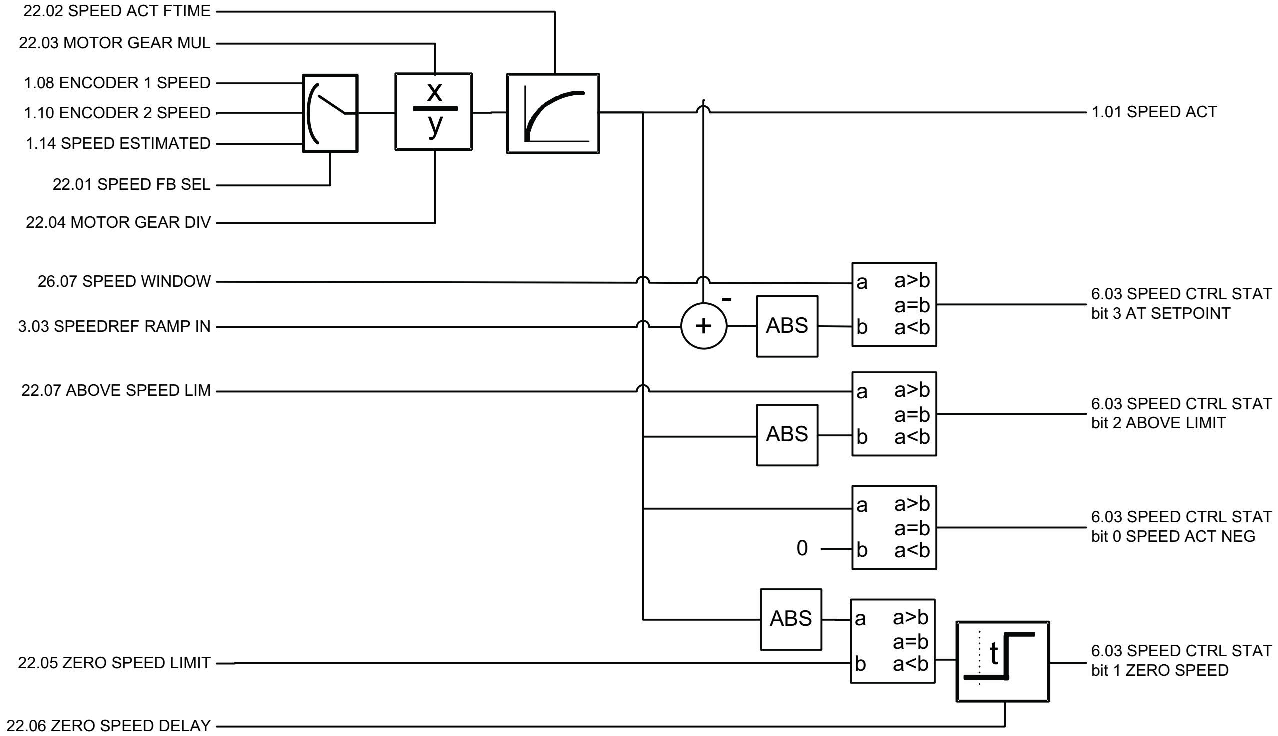
20.03	POS SPEED ENA	FW block: LIMITS (see above).
	Selects the source of the positive speed reference enable command. 1 = Positive speed reference is enabled. 0 = Positive speed reference is interpreted as zero speed reference (In the figure below 3.03 SPEEDREF RAMP IN is set to zero after the positive speed enable signal has cleared). Actions in different control modes: Speed control: Speed reference is set to zero and the motor is stopped along the currently active deceleration ramp. Torque control: Torque limit is set to zero and the rush controller stops the motor. Position and synchron control: Dynamic limiter sets the positioning speed reference to zero and the motor is stopped according to 70.06 POS DECEL LIM. Homing and profile velocity mode control: Dynamic limiter sets the speed reference to zero and the motor is stopped according to 70.06 POS DECEL LIM.  **Example:** The motor is rotating in the forward direction. To stop the motor, the positive speed enable signal is deactivated by a hardware limit switch (e.g. via digital input). If the positive speed enable signal remains deactivated and the negative speed enable signal is active, only reverse rotation of the motor is allowed.	
	Bit pointer: Group, index and bit	
20.04	NEG SPEED ENA	FW block: LIMITS (see above).
	Selects the source of the negative speed reference enable command. See parameter 20.03 POS SPEED ENA.	
	Bit pointer: Group, index and bit	
20.05	MAXIMUM CURRENT	FW block: LIMITS (see above).
	Defines the allowed maximum motor current.	
	0…30000 A	Maximum allowed motor current.
20.06	MAXIMUM TORQUE	FW block: LIMITS (see above).
	Defines the maximum torque limit for the drive (in percent of the motor nominal torque).	
	0…1600%	Maximum torque limit.
20.07	MINIMUM TORQUE	FW block: LIMITS (see above).
	Defines the minimum torque limit for the drive (in percent of the motor nominal torque).	
	-1600…0%	Minimum torque limit.

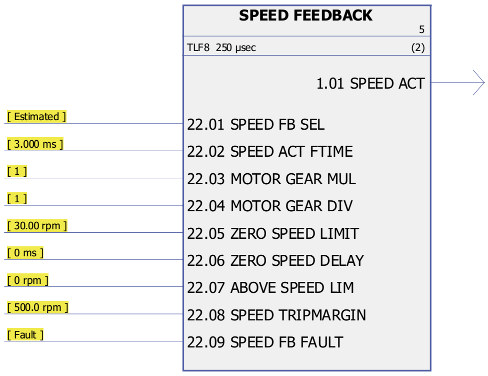
20.08	THERM CURR LIM	FW block: None
	Enables the thermal current limitation. Thermal current limit is calculated by the inverter thermal protection function.	
	(0) ENABLE	The calculated thermal current value limits the inverter output current (i.e. motor current).
	(1) DISABLE	The calculated thermal limit is not used. If the inverter output current is excessive, alarm IGBT OVERTEMP is generated and eventually the drive trips on fault IGBT OVERTEMP.

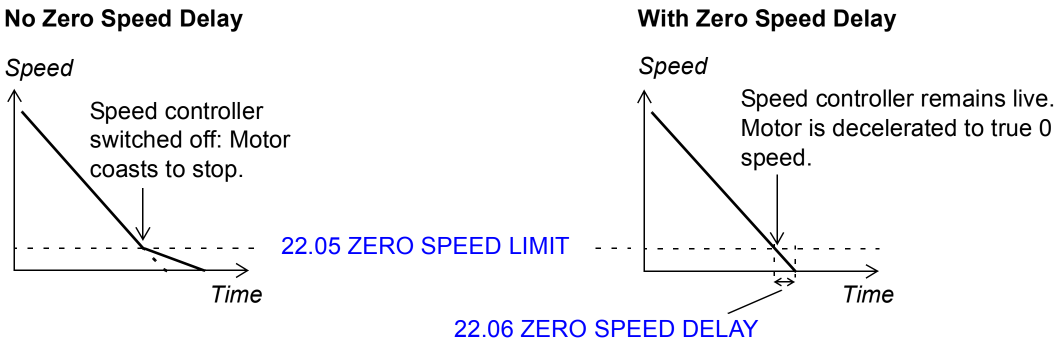
Group 22 SPEED FEEDBACK

Settings for

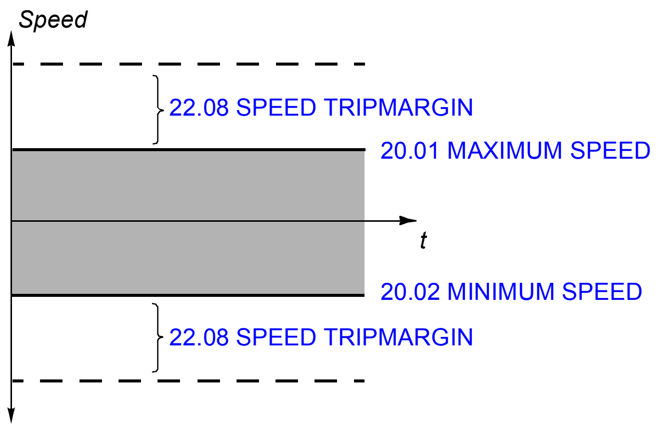
- selection of speed feedback used in drive control
- filtering disturbances in measured speed signal
- motor encoder gear function
- zero speed limit for stop function
- delay for Zero Speed Delay function
- definition of limits for actual speed supervision
- loss of speed feedback signal protection.



22 SPEED FEEDBACK		
Firmware block: **SPEED FEEDBACK** (22)		
Block outputs located in other parameter groups		1.01 SPEED ACT (page 77)
22.01	SPEED FB SEL	FW block: SPEED FEEDBACK (see above)
	Selects the speed feedback value used in control.	
	(0) ESTIMATED	Calculated speed estimate.
	(1) ENC1 SPEED	Actual speed measured with encoder 1. The encoder is selected by parameter 90.01 ENCODER 1 SEL.
	(2) ENC2 SPEED	Actual speed measured with encoder 2. The encoder is selected by parameter 90.02 ENCODER 2 SEL.
22.02	SPEED ACT FTIME	FW block: SPEED FEEDBACK (see above)
	Defines the time constant of the actual speed filter, i.e. time within the actual speed has reached 63% of the nominal speed (filtered speed = 1.01 SPEED ACT). If the used speed reference remains constant, the possible interferences in the speed measurement can be filtered with the actual speed filter. Reducing the ripple with filter may cause speed controller tuning problems. A long filter time constant and fast acceleration time contradict one another. A very long filter time results in unstable control. If there are substantial interferences in the speed measurement, the filter time constant should be proportional to the total inertia of the load and motor, in this case 10…30% of the mechanical time constant $t_{mech} = (n_{nom} / T_{nom}) \times J_{tot} \times 2\pi / 60$, where J_{tot} = total inertia of the load and motor (the gear ratio between the load and motor must be taken into account) n_{nom} = motor nominal speed T_{nom} = motor nominal torque See also parameter 26.06 SPD ERR FTIME.	
	0…10000 ms	Time constant for actual speed filter.

<table>
<tr><td>22.03</td><td>MOTOR GEAR MUL</td><td>FW block: SPEED FEEDBACK (see above)</td></tr>
<tr><td></td><td colspan="2">Defines the motor gear numerator for the motor encoder gear function.
$$\frac{\text{22.03 MOTOR GEAR MUL}}{\text{22.04 MOTOR GEAR DIV}} = \frac{\text{Actual speed}}{\text{Input speed}}$$
where input speed is encoder 1/2 speed (1.08 ENCODER 1 SPEED / 1.10 ENCODER 2 SPEED) or speed estimate (1.14 SPEED ESTIMATED).
See section Motor encoder gear function on page 48.</td></tr>
<tr><td></td><td>$-2^{31} \ldots 2^{31} -1$</td><td>Numerator for motor encoder gear. Note: A setting of 0 is changed internally to 1.</td></tr>
<tr><td>22.04</td><td>MOTOR GEAR DIV</td><td>FW block: SPEED FEEDBACK (see above)</td></tr>
<tr><td></td><td colspan="2">Defines the motor gear denominator for the motor encoder gear function. See parameter 22.03 MOTOR GEAR MUL.</td></tr>
<tr><td></td><td>$1 \ldots 2^{31} -1$</td><td>Denominator for motor encoder gear.</td></tr>
<tr><td>22.05</td><td>ZERO SPEED LIMIT</td><td>FW block: SPEED FEEDBACK (see above)</td></tr>
<tr><td></td><td colspan="2">Defines the zero speed limit. The motor is stopped along a speed ramp until the defined zero speed limit is reached. After the limit, the motor coasts to stop.
Note: Too low a setting may result in the drive not stopping at all.</td></tr>
<tr><td></td><td>0…30000 rpm</td><td>Zero speed limit.</td></tr>
<tr><td>22.06</td><td>ZERO SPEED DELAY</td><td>FW block: SPEED FEEDBACK (see above)</td></tr>
<tr><td></td><td colspan="2">Defines the delay for the zero speed delay function. The function is useful in applications where a smooth and quick restarting is essential. During the delay the drive knows accurately the rotor position.


No Zero Speed Delay
The drive receives a stop command and decelerates along a ramp. When the motor actual speed falls below an internal limit (called Zero Speed), the speed controller is switched off. The inverter modulation is stopped and the motor coasts to standstill.
With Zero Speed Delay
The drive receives a stop command and decelerates along a ramp. When the actual motor speed falls below an internal limit (called Zero Speed), the zero speed delay function activates. During the delay the function keeps the speed controller live: the inverter modulates, motor is magnetised and the drive is ready for a quick restart. Zero speed delay can be used e.g. with the jogging function.</td></tr>
<tr><td></td><td>0…30000 ms</td><td>Zero speed delay.</td></tr>
</table>

22.07	ABOVE SPEED LIM	FW block: SPEED FEEDBACK (see above)
	Defines the supervision limit for the actual speed.	
	0…30000 rpm	Supervision limit for actual speed.
22.08	SPEED TRIPMARGIN	FW block: SPEED FEEDBACK (see above)
	Defines, together with 20.01 MAXIMUM SPEED and 20.02 MINIMUM SPEED, the maximum allowed speed of the motor (overspeed protection). If the actual speed (1.01 SPEED ACT) exceeds the speed limit defined by parameter 20.01 or 20.02 by more than 22.08 SPEED TRIPMARGIN, the drive trips on fault OVERSPEED. Example: If the maximum speed is 1420 rpm and speed trip margin is 300 rpm, the drive trips at 1720 rpm. 	
	0…10000 rpm	Speed trip margin.
22.09	SPEED FB FAULT	FW block: SPEED FEEDBACK (see above)
	Selects the action in case of speed feedback data loss.	
	(0) FAULT	Drive trips on a fault (OPTION COMM LOSS, ENCODER 1/2 FAILURE or SPEED FEEDBACK depending on the type of problem).
	(1) WARNING	Drive continues operation with open loop control and generates an alarm (OPTION COMM LOSS, ENCODER 1/2 FAILURE or SPEED FEEDBACK depending on the type of problem).
	(2) NO	Drive continues operation with open loop control. No faults or alarms are generated.

Group 24 SPEED REF MOD

Settings for

- speed reference selection
- speed reference modification (scaling and inversion)
- constant speed and jogging references
- definition of absolute minimum speed reference.

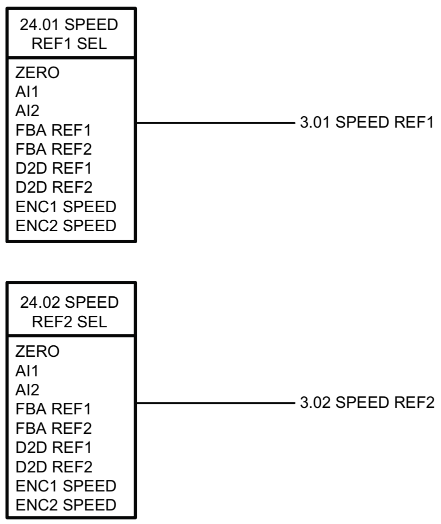
Depending on user selection, either speed reference 1 or speed reference 2 is active at a time.

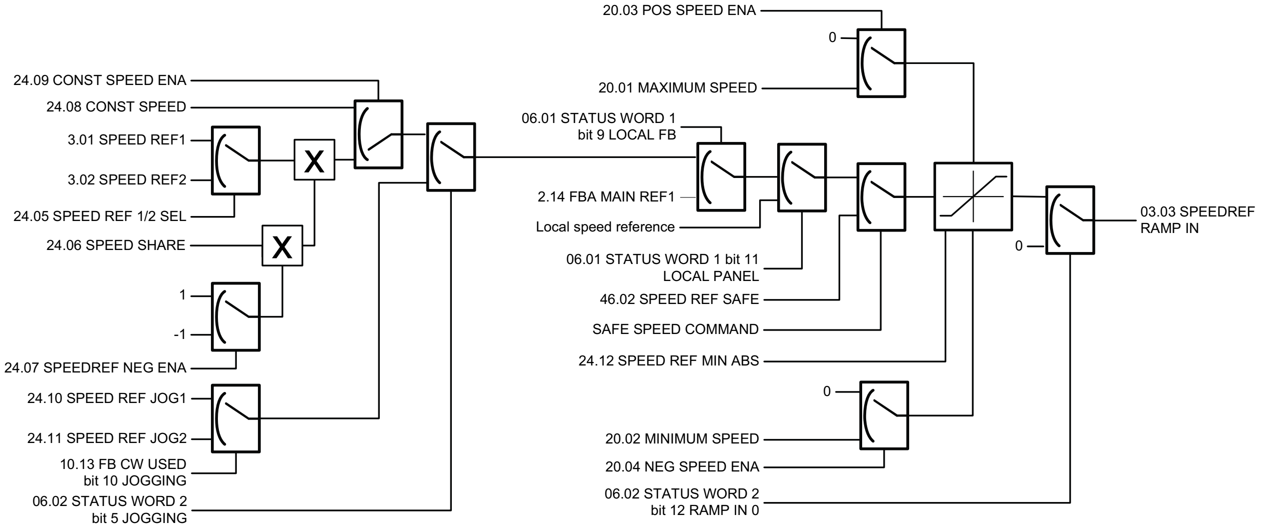
Speed reference can be any of the following (in priority order):

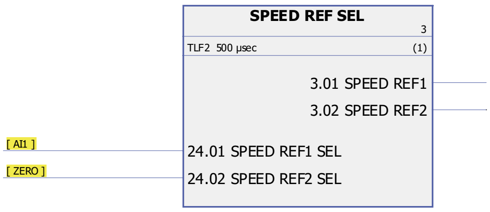
- fault speed reference (in a control panel or PC tool communication break)
- local speed reference (from panel)
- fieldbus local reference
- jogging reference 1/2
- constant speed reference 1/2
- external speed reference.

Note: Constant speed overrides external speed reference.

Speed reference is limited according to the set minimum and maximum speed values and ramped and shaped according to the defined acceleration and deceleration values. See parameter group 25 SPEED REF RAMP (page 139).





24 SPEED REF MOD		
Firmware block: **SPEED REF SEL** (23) Selects the sources for two speed references, REF1 or REF2, from a selection list. Also shows the values of both speed references. The sources can alternatively be selected with value pointer parameters. See firmware block SPEED REF MOD on page 136.		
Block outputs located in other parameter groups		3.01 SPEED REF1 (page 87) 3.02 SPEED REF2 (page 87)
24.01	SPEED REF1 SEL	FW block: SPEED REF SEL (see above)
	Selects the source for speed reference 1 (3.01 SPEED REF1). Source for speed reference 1/2 can also be selected by value pointer parameter 24.03 SPEED REF1 IN / 24.04 SPEED REF2 IN.	
	(0) ZERO	Zero reference.
	(1) AI1	Analogue input AI1.
	(2) AI2	Analogue input AI2.
	(3) FBA REF1	Fieldbus reference 1.
	(4) FBA REF2	Fieldbus reference 2.

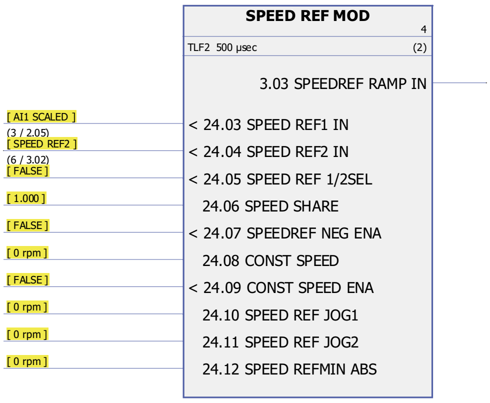
	(5) D2D REF1	Drive to drive reference 1.
	(6) D2D REF2	Drive to drive reference 2.
	(7) ENC1 SPEED	Encoder 1 (1.08 ENCODER 1 SPEED).
	(8) ENC2 SPEED	Encoder 2 (1.10 ENCODER 2 SPEED).
24.02	SPEED REF2 SEL	FW block: SPEED REF SEL (see above)
	Selects the source for speed reference 2 (3.02 SPEED REF2). See parameter 24.01 SPEED REF1 SEL.	

Firmware block:

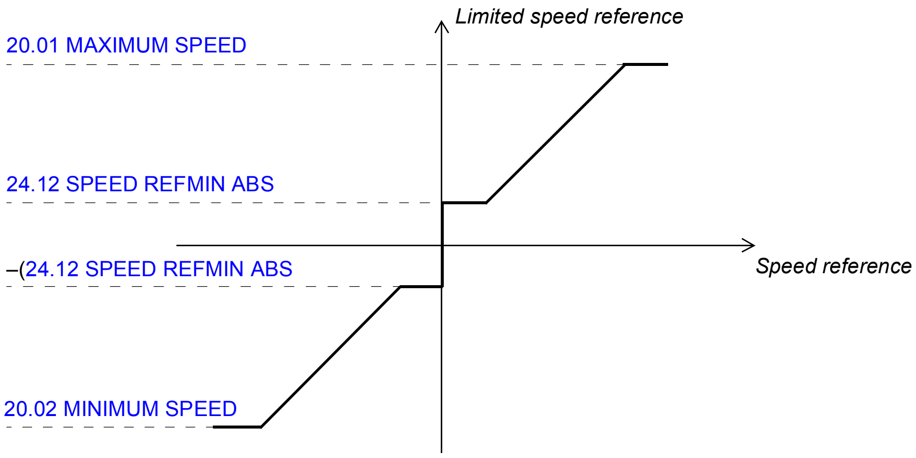
SPEED REF MOD
(24)

This block

- selects the sources for two speed references, REF1 or REF2
- scales and inverts the speed reference
- defines the constant speed reference
- defines the speed reference for jogging functions 1 and 2
- defines the speed reference absolute minimum limit.



Block outputs located in other parameter groups		3.03 SPEEDREF RAMP IN (page 87)
24.03	SPEED REF1 IN	FW block: SPEED REF MOD (see above)
	Selects the source for speed reference 1 (overrides the setting of parameter 24.01 SPEED REF1 SEL). The default value is P.3.1, i.e. 3.01 SPEED REF1, which is the output of the SPEED REF RAMP block.	
	Value pointer: Group and index	
24.04	SPEED REF2 IN	FW block: SPEED REF MOD (see above)
	Selects the source for speed reference 2 (overrides the setting of parameter 24.02 SPEED REF2 SEL). The default value is P.3.2, i.e. 3.02 SPEED REF2, which is the output of the SPEED REF RAMP block.	
	Value pointer: Group and index	
24.05	SPEED REF 1/2SEL	FW block: SPEED REF MOD (see above)
	Selects between speed reference 1 or 2. Reference 1/2 source is defined by parameter 24.03 SPEED REF1 IN / 24.04 SPEED REF2 IN. 0 = Speed reference 1.	
	Bit pointer: Group, index and bit	
24.06	SPEED SHARE	FW block: SPEED REF MOD (see above)
	Defines the scaling factor for speed reference 1/2 (speed reference 1 or 2 is multiplied by the defined value). Speed reference 1 or 2 is selected by parameter 24.05 SPEED REF 1/2SEL.	

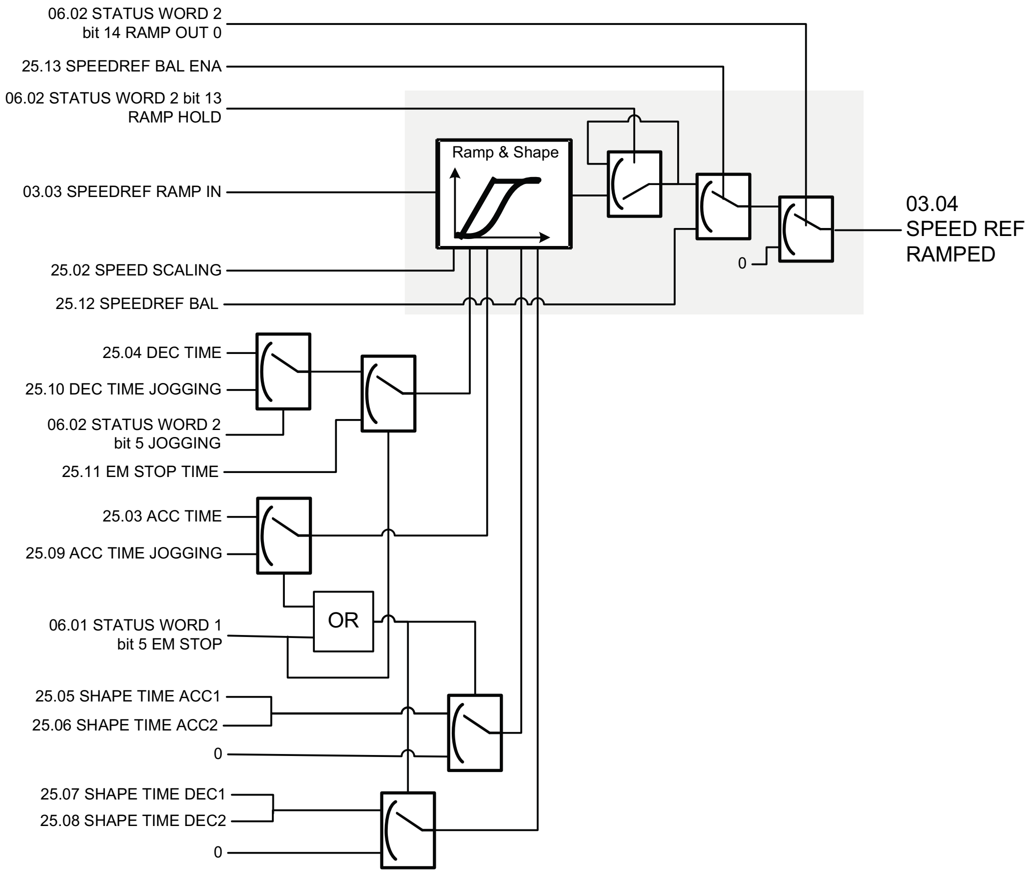
	-8…8	Scaling factor for speed reference 1/2.
24.07	SPEEDREF NEG ENA	FW block: SPEED REF MOD (see above)
	Selects the source for the speed reference inversion. 1 = Sign of the speed reference is changed (inversion active).	
	Bit pointer: Group, index and bit	
24.08	CONST SPEED	FW block: SPEED REF MOD (see above)
	Defines the constant speed.	
	-30000…30000 rpm	Constant speed.
24.09	CONST SPEED ENA	FW block: SPEED REF MOD (see above)
	Selects the source for enabling the use of the constant speed reference define by parameter 24.08 CONST SPEED. 1 = Enable.	
	Bit pointer: Group, index and bit	
24.10	SPEED REF JOG1	FW block: SPEED REF MOD (see above)
	Defines the speed reference for jogging function 1. See section *Jogging* on page 46.	
	-30000…30000 rpm	Speed reference for jogging 1.
24.11	SPEED REF JOG2	FW block: SPEED REF MOD (see above)
	Defines the speed reference for jogging function 2. See section *Jogging* on page 46.	
	-30000…30000 rpm	Speed reference for jogging 2.
24.12	SPEED REFMIN ABS	FW block: SPEED REF MOD (see above)
	Defines the absolute minimum limit for the speed reference. 	
	0…30000 rpm	Absolute minimum limit for speed reference.

Group 25 SPEED REF RAMP

Speed reference ramp settings such as

- selection of source for speed ramp input
- acceleration and deceleration times (also for jogging)
- acceleration and deceleration ramp shapes
- emergency stop OFF3 ramp time
- the speed reference balancing function (forcing the output of the ramp generator to a predefined value).

Note: Emergency stop OFF1 uses the currently active ramp time.



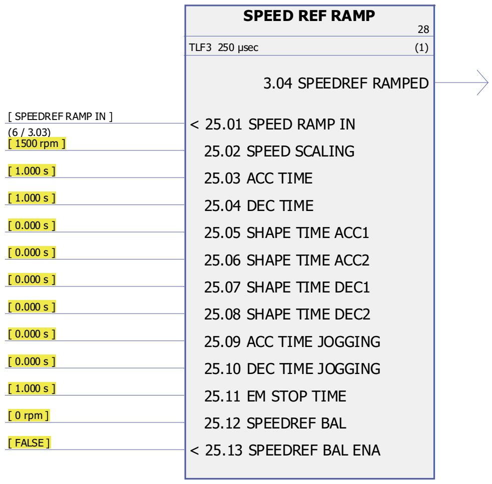
25 SPEED REF RAMP

Firmware block:

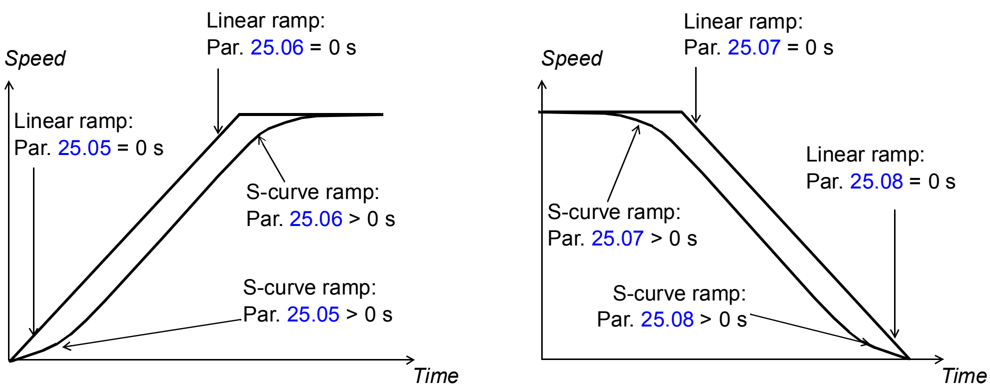
SPEED REF RAMP
(25)

This block

- selects the source for the speed ramp input
- adjusts acceleration and deceleration times (also for jogging)
- adjusts acceleration/deceleration ramp shapes
- adjusts ramp time for emergency stop OFF3
- forces the output of the ramp generator to a defined value
- shows the ramped and shaped speed reference value.



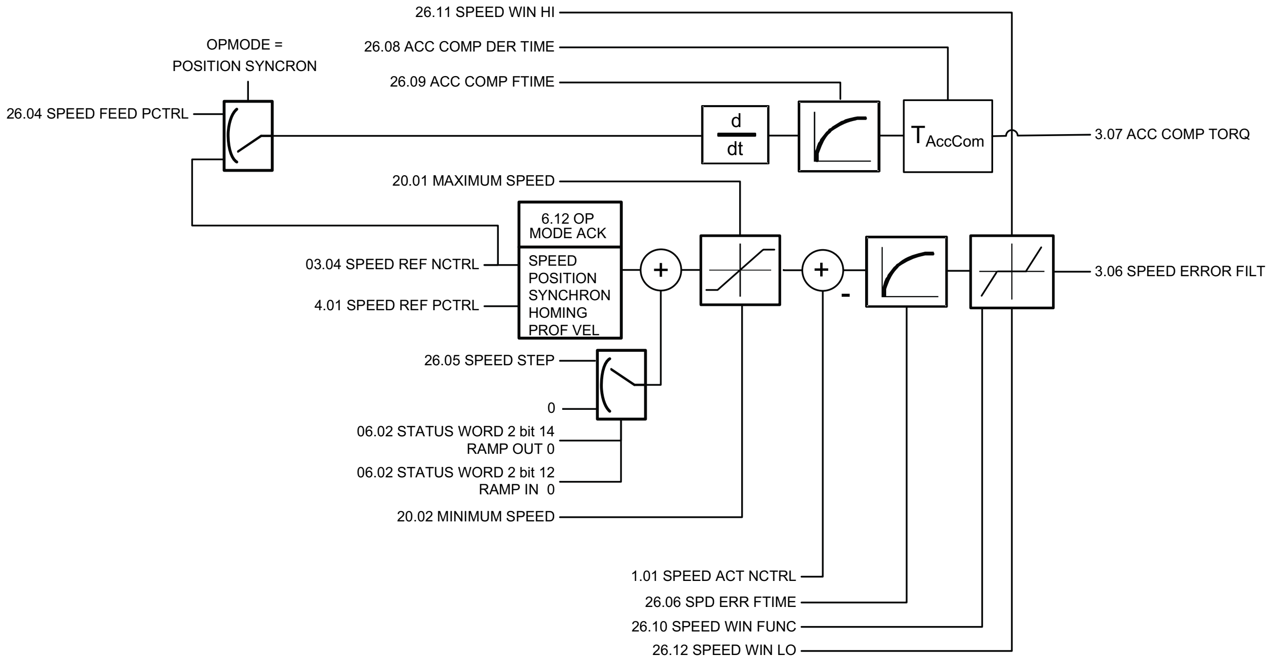
Block outputs located in other parameter groups		3.04 SPEEDREF RAMPED (page 87)
25.01	SPEED RAMP IN	FW block: SPEED REF RAMP (see above)
	Shows the source of the speed ramp input. The default value is P.3.3 i.e. signal 3.03 SPEEDREF RAMP IN, which is the output of the SPEED REF MOD firmware block. **Note:** This parameter cannot be set by the user.	
	Value pointer: Group and index	
25.02	SPEED SCALING	FW block: SPEED REF RAMP (see above)
	Defines the speed value used in acceleration and deceleration (parameters 25.03/25.09 and 25.04/ 25.10/25.11). Also affects fieldbus reference scaling (see *Appendix A – Fieldbus control*, section *Fieldbus references* on page 389).	
	0…30000 rpm	Speed value for acceleration/deceleration.
25.03	ACC TIME	FW block: SPEED REF RAMP (see above)
	Defines the acceleration time i.e. the time required for the speed to change from zero to the speed value defined by parameter 25.02 SPEED SCALING. If the speed reference increases faster than the set acceleration rate, the motor speed will follow the acceleration rate. If the speed reference increases slower than the set acceleration rate, the motor speed will follow the reference signal. If the acceleration time is set too short, the drive will automatically prolong the acceleration in order not to exceed the drive torque limits.	
	0…1800 s	Acceleration time.

<table>
<tr><td>25.04</td><td>DEC TIME</td><td>FW block: SPEED REF RAMP (see above)</td></tr>
<tr><td></td><td colspan="2">Defines the deceleration time i.e. the time required for the speed to change from the speed value defined by parameter 25.02 SPEED SCALING to zero.
If the speed reference decreases slower than the set deceleration rate, the motor speed will follow the reference signal.
If the reference changes faster than the set deceleration rate, the motor speed will follow the deceleration rate.
If the deceleration time is set too short, the drive will automatically prolong the deceleration in order not to exceed drive torque limits. If there is any doubt about the deceleration time being too short, ensure that the DC overvoltage control is on (parameter 47.01 OVERVOLTAGE CTRL).
Note: If a short deceleration time is needed for a high inertia application, the drive should be equipped with an electric braking option e.g. with a brake chopper (built-in) and a brake resistor.</td></tr>
<tr><td></td><td>0…1800 s</td><td>Deceleration time.</td></tr>
<tr><td>25.05</td><td>SHAPE TIME ACC1</td><td>FW block: SPEED REF RAMP (see above)</td></tr>
<tr><td></td><td colspan="2">Selects the shape of the acceleration ramp at the beginning of the acceleration.
0.00 s: Linear ramp. Suitable for steady acceleration or deceleration and for slow ramps.
0.01…1000.00 s: S-curve ramp. S-curve ramps are ideal for conveyor and lifting applications. The S-curve consists of symmetrical curves at both ends of the ramp and a linear part in between.
Note: When jogging or emergency ramp stop is active, acceleration and deceleration shape times are forced to zero.

</td></tr>
<tr><td></td><td>0…1000 s</td><td>Ramp shape at beginning of acceleration.</td></tr>
<tr><td>25.06</td><td>SHAPE TIME ACC2</td><td>FW block: SPEED REF RAMP (see above)</td></tr>
<tr><td></td><td colspan="2">Selects the shape of the acceleration ramp at the end of the acceleration. See parameter 25.05 SHAPE TIME ACC1.</td></tr>
<tr><td></td><td>0…1000 s</td><td>Ramp shape at end of acceleration.</td></tr>
<tr><td>25.07</td><td>SHAPE TIME DEC1</td><td>FW block: SPEED REF RAMP (see above)</td></tr>
<tr><td></td><td colspan="2">Selects the shape of the deceleration ramp at the beginning of the deceleration. See parameter 25.05 SHAPE TIME ACC1.</td></tr>
<tr><td></td><td>0…1000 s</td><td>Ramp shape at beginning of deceleration.</td></tr>
</table>

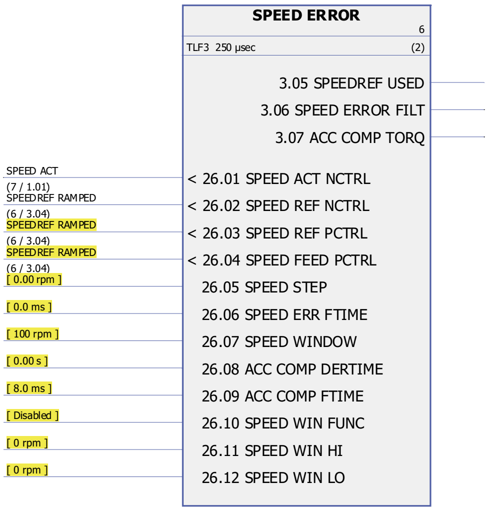
25.08	SHAPE TIME DEC2	FW block: SPEED REF RAMP (see above)
	Selects the shape of the deceleration ramp at the end of the deceleration. See parameter 25.05 SHAPE TIME ACC1.	
	0…1000 s	Ramp shape at end of deceleration.
25.09	ACC TIME JOGGING	FW block: SPEED REF RAMP (see above)
	Defines the acceleration time for the jogging function i.e. the time required for the speed to change from zero to the speed value defined by parameter 25.02 SPEED SCALING.	
	0…1800 s	Acceleration time for jogging.
25.10	DEC TIME JOGGING	FW block: SPEED REF RAMP (see above)
	Defines the deceleration time for the jogging function i.e. the time required for the speed to change from the speed value defined by parameter 25.02 SPEED SCALING to zero.	
	0…1800 s	Deceleration time for jogging.
25.11	EM STOP TIME	FW block: SPEED REF RAMP (see above)
	Defines the time inside which the drive is stopped if an emergency stop OFF3 is activated (i.e. the time required for the speed to change from the speed value defined by parameter 25.02 SPEED SCALING to zero). Emergency stop activation source is selected by parameter 10.10 EM STOP OFF3. Emergency stop can also be activated through fieldbus (2.12 FBA MAIN CW). Emergency stop OFF1 uses the active ramp time.	
	0…1800 s	Emergency stop OFF3 deceleration time.
25.12	SPEEDREF BAL	FW block: SPEED REF RAMP (see above)
	Defines the reference for the speed ramp balancing, i.e. the output of the speed reference ramp firmware block is forced to a defined value. The source for the balancing enable signal is selected by parameter 25.13 SPEEDREF BAL.	
	-30000…30000 rpm	Speed ramp balancing reference.
25.13	SPEEDREF BAL ENA	FW block: SPEED REF RAMP (see above)
	Selects the source for enabling the speed ramp balancing. See parameter 25.12 SPEEDREF BAL. 1 = Speed ramp balancing enabled.	
	Bit pointer: Group, index and bit	

Group 26 SPEED ERROR

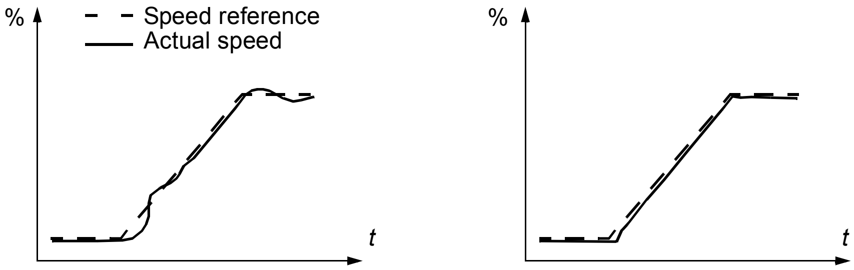
Speed error is determined by comparing the speed reference and speed feedback. The error can be filtered using a first-order low-pass filter if the feedback and reference have disturbances. In addition, a torque boost can be applied to compensate acceleration; the torque is relative to the rate of change (derivative) in the speed reference and inertia of the load.



<table>
<tr><td colspan="3">26 SPEED ERROR</td></tr>
<tr><td colspan="2">Firmware block:
SPEED ERROR
(26)

This block
• selects the source for speed error calculation (speed reference - actual speed) in different control modes
• selects the sources for speed reference and speed reference feedforward
• defines the speed error filtering time
• defines an additional speed step to the speed error
• defines the supervision of speed error with the speed error window function
• defines inertia compensation during acceleration
• shows the used speed reference, filtered speed error and the output of the acceleration compensation.</td><td>
</td></tr>
<tr><td colspan="2">Block outputs located in other parameter groups</td><td>3.05 SPEEDREF USED (page 87)
3.06 SPEED ERROR FILT (page 87)
3.07 ACC COMP TORQ (page 87)</td></tr>
<tr><td>26.01</td><td>SPEED ACT NCTRL</td><td>FW block: SPEED ERROR (see above)</td></tr>
<tr><td></td><td colspan="2">Selects the source for the actual speed in the speed control mode.
Note: This parameter has been locked, i.e. no user setting is possible.</td></tr>
<tr><td></td><td colspan="2">Value pointer: Group and index</td></tr>
<tr><td>26.02</td><td>SPEED REF NCTRL</td><td>FW block: SPEED ERROR (see above)</td></tr>
<tr><td></td><td colspan="2">Selects the source for the speed reference in the speed control mode.
Note: This parameter has been locked, i.e. no user setting is possible.</td></tr>
<tr><td></td><td colspan="2">Value pointer: Group and index</td></tr>
<tr><td>26.03</td><td>SPEED REF PCTRL</td><td>FW block: SPEED ERROR (see above)</td></tr>
<tr><td></td><td colspan="2">Selects the source for the speed reference in position and synchron control modes.
Note: This parameter is only for positioning applications.</td></tr>
<tr><td></td><td colspan="2">Value pointer: Group and index</td></tr>
</table>

26.04	SPEED FEED PCTRL	FW block: SPEED ERROR (see above)
	Selects the source for the speed reference feedforward in position and synchron control modes. Selects the source for the speed reference in homing and profile velocity modes. **Note:** This parameter is only for positioning applications.	
	Value pointer: Group and index	
26.05	SPEED STEP	FW block: SPEED ERROR (see above)
	Defines an additional speed step given to the input of the speed controller (added to the speed error value).	
	-30000…30000 rpm	Speed step.
26.06	SPD ERR FTIME	FW block: SPEED ERROR (see above)
	Defines the time constant of the speed error low pass filter. If the used speed reference changes rapidly (servo application), the possible interferences in the speed measurement can be filtered with the speed error filter. Reducing the ripple with filter may cause speed controller tuning problems. A long filter time constant and fast acceleration time contradict one another. A very long filter time results in unstable control. See also parameter 22.02 SPEED ACT FTIME.	
	0…1000 ms	Time constant for speed error low pass filter. 0 ms = filtering disabled.
26.07	SPEED WINDOW	FW block: SPEED ERROR (see above)
	Defines the absolute value for the motor speed window supervision, i.e. the absolute value for the difference between the actual speed and the unramped speed reference (1.01 SPEED ACT - 3.03 SPEEDREF RAMP IN). When the motor speed is within the limits defined by this parameter, signal 2.13 bit 8 (AT_SETPOINT) value is 1. If the motor speed is not within the defined limits, bit 8 value is 0.	
	0…30000 rpm	Absolute value for motor speed window supervision.

<table>
<tr><td>26.08</td><td>ACC COMP DERTIME</td><td>FW block: SPEED ERROR (see above)</td></tr>
<tr><td></td><td colspan="2">Defines the derivation time for acceleration (deceleration) compensation. Used to improve the speed control dynamic reference change.
In order to compensate inertia during acceleration, a derivative of the speed reference is added to the output of the speed controller. The principle of a derivative action is described for parameter 28.04 DERIVATION TIME.
Note: The parameter value should be proportional to the total inertia of the load and motor, i.e. approximately 50…100% of the mechanical time constant (t_{mech}). See the mechanical time constant equation in parameter 22.02 SPEED ACT FTIME.
If parameter value is set to zero, the function is deactivated.
The figure below shows the speed responses when a high inertia load is accelerated along a ramp.
No acceleration compensation No acceleration compensation


See also parameter 26.09 ACC COMP FTIME.
The source for the acceleration compensation torque can also be selected by parameter 28.06 ACC COMPENSATION. See firmware group 28 SPEED CONTROL.</td></tr>
<tr><td></td><td>0…600 s</td><td>Derivation time for acceleration/deceleration compensation.</td></tr>
<tr><td>26.09</td><td>ACC COMP FTIME</td><td>FW block: SPEED ERROR (see above)</td></tr>
<tr><td></td><td colspan="2">Defines the filter time for the acceleration compensation.</td></tr>
<tr><td></td><td>0…1000 ms</td><td>Filter time for acceleration compensation. 0 ms = filtering disabled.</td></tr>
<tr><td>26.10</td><td>SPEED WIN FUNC</td><td>FW block: SPEED ERROR (see above)</td></tr>
<tr><td></td><td colspan="2">Enables or disables speed error window control.
Speed error window control forms a speed supervision function for a torque-controlled drive. It supervises the speed error value (speed reference – actual speed). In the normal operating range, window control keeps the speed controller input at zero. When the speed error moves outside the window, the exceeding part of the error value is connected to the speed controller. The speed controller produces a reference term relative to the input and gain of the speed controller (parameter 28.02 PROPORT GAIN) which the torque selector adds to the torque reference. The result is used as the internal torque reference for the drive.
Example: In a load loss condition, the internal torque reference of the drive is decreased to prevent an excessive rise of the motor speed. If window control were inactive, the motor speed would rise until a speed limit of the drive were reached.</td></tr>
<tr><td></td><td>(0) DISABLED</td><td>Speed error window control inactive.</td></tr>
<tr><td></td><td>(1) ABSOLUTE</td><td>Speed error window control active. The window boundaries set by parameters 28.02 and 28.02 are effective in both directions of rotation (the boundaries have a negative value when actual speed is negative).</td></tr>
</table>

	(2) RELATIVE	Speed error window control active. The window boundaries set by parameters 28.02 and 28.02 are only effective in the forward direction (i.e. when actual speed is positive).
26.11	SPEED WIN HI	FW block: SPEED ERROR (see above)
	High limit for speed window control. See parameter 26.10 SPEED WIN FUNC.	
	0…3000 rpm	High limit for speed error window control.
26.12	SPEED WIN LO	FW block: SPEED ERROR (see above)
	Low limit for speed window control. See parameter 26.10 SPEED WIN FUNC.	
	0…3000 rpm	Low limit for speed error window control.

Group 28 SPEED CONTROL

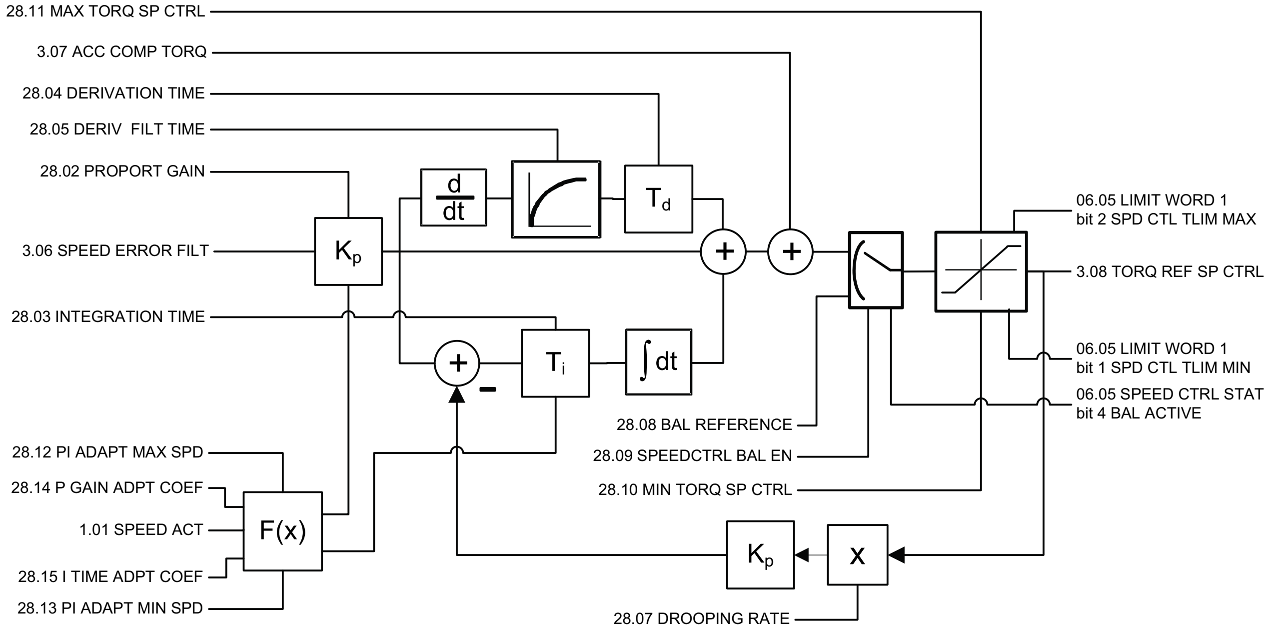
Speed controller settings such as

- selection of source for speed error
- adjustment of PID-type speed controller variables
- limitation of speed controller output torque
- selection of source for acceleration compensation torque
- forcing an external value to the output of the speed controller (with the balancing function).
- adjustment of the load sharing in a Master/Follower application run by several drives (the drooping function).

The speed controller includes an anti-windup function (i.e. I-term is frozen during torque reference limitation).

In torque control mode, the speed controller output is frozen.

For manual speed controller tuning, see section *Manual speed controller tuning* on page *25*.



28 SPEED CONTROL

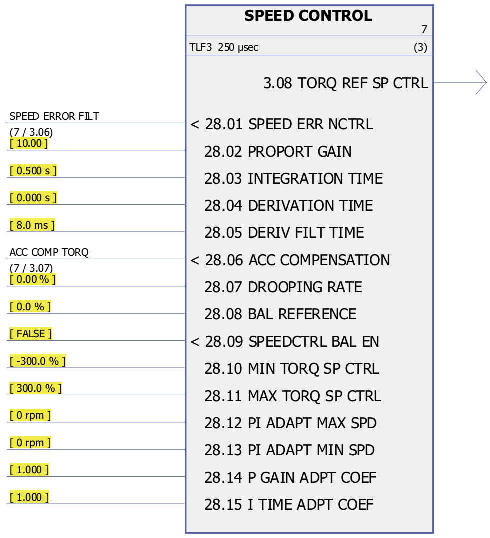
Firmware block:

SPEED CONTROL

(28)

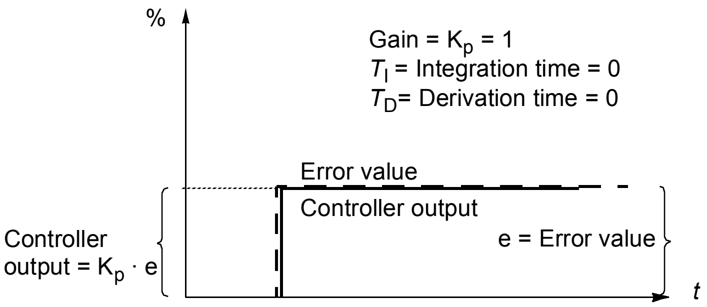
This block

- selects the source for speed error
- adjusts PID-type speed controller variables
- defines limits for speed controller output torque
- selects the source for acceleration compensation torque
- configures the balancing function which forces the output of the speed controller to an external value
- configures the drooping function (adjustment of load sharing in a Master/Follower application)
- shows the limited speed controller output torque value.



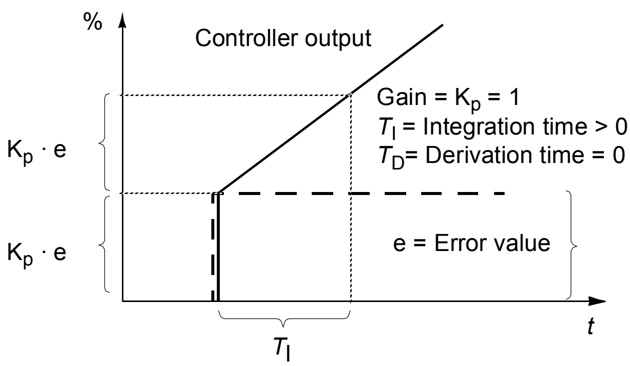
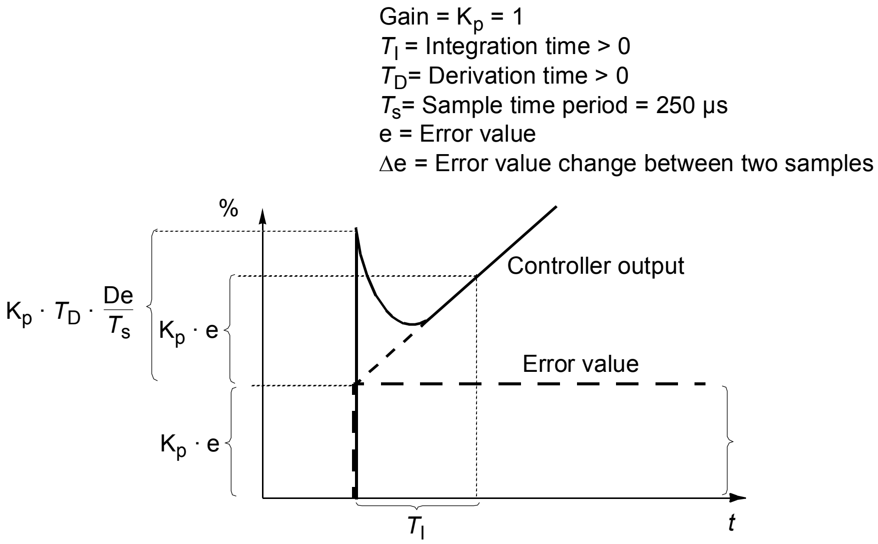
Block outputs located in other parameter groups	3.08 TORQ REF SP CTRL (page 87)

28.01	SPEED ERR NCTRL	FW block: SPEED CONTROL (see above)
	Selects the source for the speed error (reference - actual). The default value is P.3.6 i.e. signal 3.06 SPEED ERROR FILT, which is the output of the SPEED ERROR firmware block. **Note:** This parameter has been locked, i.e. no user setting is possible.	
	Value pointer: Group and index	
28.02	PROPORT GAIN	FW block: SPEED CONTROL (see above)
	Defines the proportional gain (K_p) of the speed controller. Too great a gain may cause speed oscillation. The figure below shows the speed controller output after an error step when the error remains constant.	



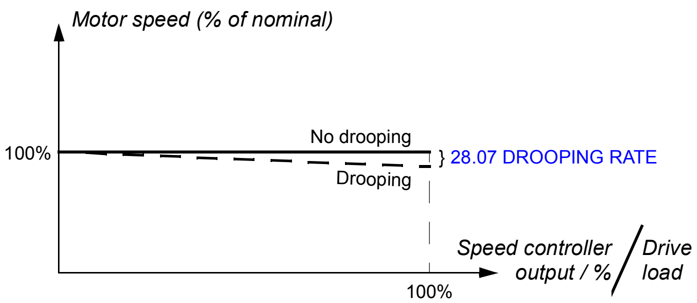
If gain is set to 1, a 10% change in error value (reference - actual value) causes the speed controller output to change by 10%.

	0…200	Proportional gain for speed controller.

28.03	INTEGRATION TIME	FW block: SPEED CONTROL (see above)
	Defines the integration time of the speed controller. The integration time defines the rate at which the controller output changes when the error value is constant and the proportional gain of the speed controller is 1. The shorter the integration time, the faster the continuous error value is corrected. Too short integration time makes the control unstable. If parameter value is set to zero, the I-part of the controller is disabled. Anti-windup stops the integrator if the controller output is limited. See 6.05 LIMIT WORD 1. The figure below shows the speed controller output after an error step when the error remains constant. 	
	0…600 s	Integration time for speed controller.
28.04	DERIVATION TIME	FW block: SPEED CONTROL (see above)
	Defines the derivation time of the speed controller. Derivative action boosts the controller output if the error value changes. The longer the derivation time, the more the speed controller output is boosted during the change. If the derivation time is set to zero, the controller works as a PI controller, otherwise as a PID controller. The derivation makes the control more responsive for disturbances. The speed error derivative must be filtered with a low pass filter to eliminate disturbances. The figure below shows the speed controller output after an error step when the error remains constant.  **Note:** Changing this parameter value is recommended only if a pulse encoder is used.	

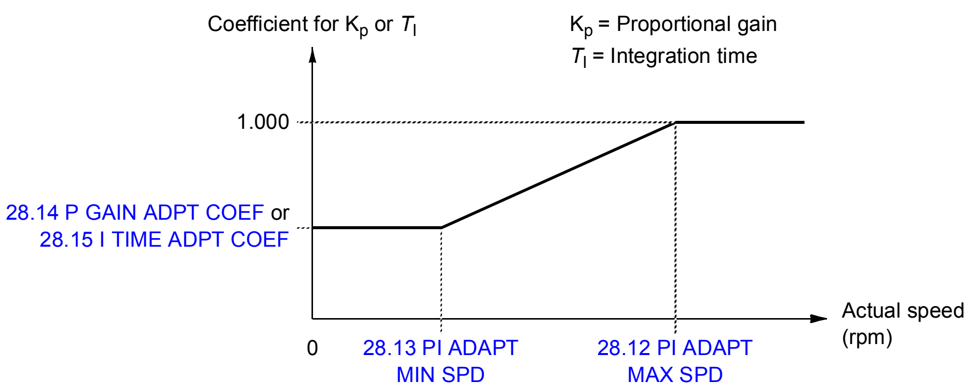
	0…10 s	Derivation time for speed controller.
28.05	DERIV FILT TIME	FW block: SPEED CONTROL (see above)
	Defines the derivation filter time constant.	
	0…1000 ms	Derivation filter time constant.
28.06	ACC COMPENSATION	FW block: SPEED CONTROL (see above)
	Selects the source for the acceleration compensation torque. The default value is P.3.7, i.e signal 3.07 ACC COMP TORQ, which is the output of the SPEED ERROR firmware block. **Note:** This parameter has been locked, i.e. no user setting is possible.	
	Value pointer: Group and index	
28.07	DROOPING RATE	FW block: SPEED CONTROL (see above)
	Defines the droop rate (in percent of the motor nominal speed). The drooping slightly decreases the drive speed as the drive load increases. The actual speed decrease at a certain operating point depends on the droop rate setting and the drive load (= torque reference / speed controller output). At 100% speed controller output, drooping is at its nominal level, i.e. equal to the value of this parameter. The drooping effect decreases linearly to zero along with the decreasing load. Drooping rate can be used e.g. to adjust the load sharing in a Master/Follower application run by several drives. In a Master/Follower application the motor shafts are coupled to each other. The correct droop rate for a process must be found out case by case in practice.	

Speed decrease = Speed controller output · Drooping · Max. speed
Example: Speed controller output is 50%, drooping rate is 1%, maximum speed of the drive is 1500 rpm.
Speed decrease = 0.50 · 0.01 · 1500 rpm = 7.5 rpm.



	0…100%	Droop rate.
28.08	BAL REFERENCE	FW block: SPEED CONTROL (see above)
	Defines the reference used in the speed control output balancing, i.e. an external value to be forced to the output of the speed controller. In order to guarantee smooth operation during output balancing, the speed controller D-part is disabled and the acceleration compensation term is set to zero. The source for the balancing enable signal is selected by parameter 28.09 SPEEDCTRL BAL EN.	
	-1600…1600%	Speed control output balancing reference.
28.09	SPEEDCTRL BAL EN	FW block: SPEED CONTROL (see above)
	Selects the source for the speed control output balancing enable signal. See parameter 28.08 BAL REFERENCE. 1 = Enabled. 0 = Disabled.	

	Bit pointer: Group, index and bit	
28.10	MIN TORQ SP CTRL	FW block: SPEED CONTROL (see above)
	Defines the minimum speed controller output torque.	
	-1600…1600%	Minimum speed controller output torque.
28.11	MAX TORQ SP CTRL	FW block: SPEED CONTROL (see above)
	Defines the maximum speed controller output torque.	
	-1600…1600%	Maximum speed controller output torque.
28.12	PI ADAPT MAX SPD	FW block: SPEED CONTROL (see above)
	Maximum actual speed for speed controller adaptation. Speed controller gain and integration time can be adapted according to actual speed. This is done by multiplying the gain (28.02 PROPORT GAIN) and integration time (28.03 INTEGRATION TIME) by coefficients at certain speeds. The coefficients are defined individually for both gain and integration time. When the actual speed is below or equal to 28.13 PI ADAPT MIN SPD, 28.02 PROPORT GAIN and 28.03 INTEGRATION TIME are multiplied by 28.14 P GAIN ADPT COEF and 28.15 I TIME ADPT COEF respectively. When the actual speed is equal to or exceeds 28.12 PI ADAPT MAX SPD, no adaptation takes place; in other words, 28.02 PROPORT GAIN and 28.03 INTEGRATION TIME are used as such. Between 28.13 PI ADAPT MIN SPD and 28.12 PI ADAPT MAX SPD, the coefficients are calculated linearly on the basis of the breakpoints.	



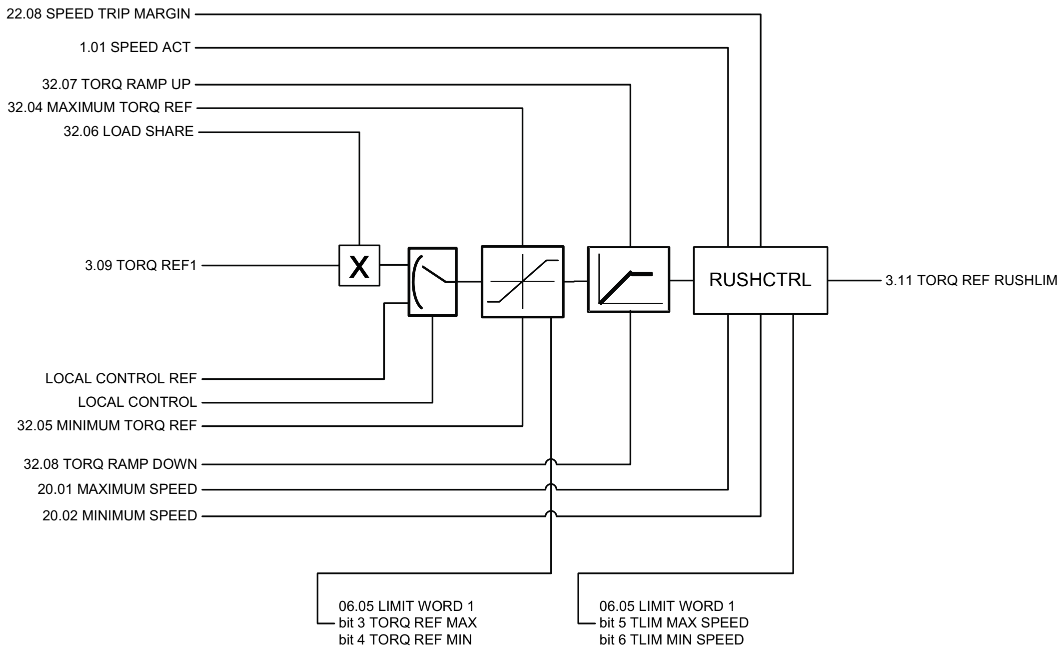
	0…30000 rpm	Maximum actual speed for speed controller adaptation.
28.13	PI ADAPT MIN SPD	FW block: SPEED CONTROL (see above)
	Minimum actual speed for speed controller adaptation. See parameter 28.12 PI ADAPT MAX SPD.	
	0…30000 rpm	Minimum actual speed for speed controller adaptation.
28.14	P GAIN ADPT COEF	FW block: SPEED CONTROL (see above)
	Proportional gain coefficient. See parameter 28.12 PI ADAPT MAX SPD.	
	0.000 … 10.000	Proportional gain coefficient.

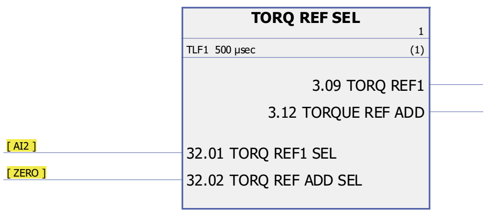
28.15	I TIME ADPT COEF	FW block: SPEED CONTROL (see above)
	Integration time coefficient. See parameter 28.12 PI ADAPT MAX SPD.	
	0.000 … 10.000	Integration time coefficient.

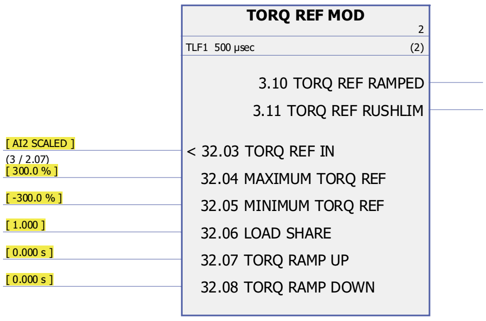
Group 32 TORQUE REFERENCE

Reference settings for torque control.

In torque control, the drive speed is limited between the defined minimum and maximum limits. Speed-related torque limits are calculated and the input torque reference is limited according to these limits. An OVERSPEED fault is generated if the maximum allowed speed is exceeded.



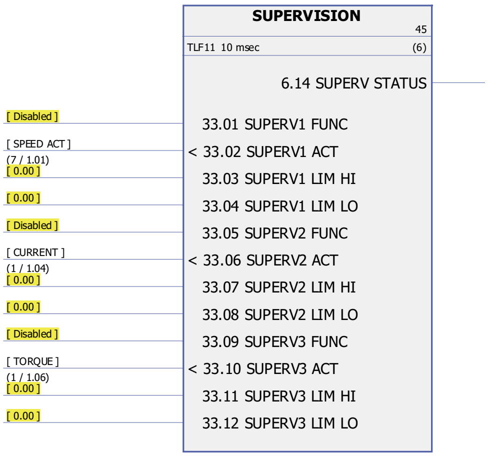
32 TORQUE REFERENCE		
Firmware block: **TORQ REF SEL** (32) Selects the source for torque reference 1 (from a parameter selection list) and the source for torque reference addition (used e.g. for compensating mechanical interferences). Also shows the torque reference and reference addition values.		
Block outputs located in other parameter groups		3.09 TORQ REF1 (page 87) 3.12 TORQUE REF ADD (page 87)
32.01	TORQ REF1 SEL	FW block: TORQ REF SEL (see above)
	Selects the source for torque reference 1. See also parameter 32.03 TORQ REF IN.	
	(0) ZERO	Zero reference.

	(1) AI1	Analogue input AI1.
	(2) AI2	Analogue input AI2.
	(3) FBA REF1	Fieldbus reference 1.
	(4) FBA REF2	Fieldbus reference 2.
	(5) D2D REF1	Drive to drive reference 1.
	(6) D2D REF2	Drive to drive reference 2.
32.02	TORQ REF ADD SEL	FW block: TORQ REF SEL (see above)
	Selects the source for the torque reference addition, 3.12 TORQUE REF ADD. Parameter 34.10 TORQ REF ADD SRC is connected to signal 3.12 TORQUE REF ADD by default. Because the reference is added after the torque reference selection, this parameter can be used in speed and torque control modes. See block diagram at parameter group 34 REFERENCE CTRL (page 160).	
	(0) ZERO	Zero reference addition.
	(1) AI1	Analogue input AI1.
	(2) AI2	Analogue input AI2.
	(3) FBA REF1	Fieldbus reference 1.
	(4) FBA REF2	Fieldbus reference 2.
	(5) D2D REF1	Drive to drive reference 1.
	(6) D2D REF2	Drive to drive reference 2.
Firmware block: **TORQ REF MOD** (33) This block • selects the source for the torque reference • scales the input torque reference according to the defined load share factor • defines limits for the torque reference • defines ramp-up and ramp-down times for the torque reference • shows the ramped torque reference value and the torque reference value limited by the rush control.		
Block outputs located in other parameter groups		3.10 TORQ REF RAMPED (page 87) 3.11 TORQ REF RUSHLIM (page 87)
32.03	TORQ REF IN	FW block: TORQ REF MOD (see above)
	Selects the source for the torque reference input for the torque ramp function. The default value is P.3.9, i.e. signal 3.09 TORQ REF1, which is the output of the TORQ REF SEL firmware block.	

	Value pointer: Group and index	
32.04	MAXIMUM TORQ REF	FW block: TORQ REF MOD (see above)
	Defines the maximum torque reference.	
	0…1000%	Maximum torque reference.
32.05	MINIMUM TORQ REF	FW block: TORQ REF MOD (see above)
	Defines the minimum torque reference.	
	-1000…0%	Minimum torque reference.
32.06	LOAD SHARE	FW block: TORQ REF MOD (see above)
	Scales the external torque reference to a required level (external torque reference is multiplied by the selected value). **Note:** If local torque reference is used, no load share scaling is applied.	
	-8…8	External torque reference multiplier.
32.07	TORQ RAMP UP	FW block: TORQ REF MOD (see above)
	Defines the torque reference ramp-up time, i.e. the time for the reference to increase from zero to the nominal motor torque.	
	0…60 s	Torque reference ramp-up time.
32.08	TORQ RAMP DOWN	FW block: TORQ REF MOD (see above)
	Defines the torque reference ramp-down time, i.e. the time for the reference to decrease from the nominal motor torque to zero.	
	0…60 s	Torque reference ramp-down time.

Group 33 SUPERVISION

Configuration of signal supervision.

33 SUPERVISION		
Firmware block: **SUPERVISION** (17)		
Block outputs located in other parameter groups		6.14 SUPERV STATUS (page 98)
33.01	SUPERV1 FUNC	FW block: SUPERVISION (see above)
	Selects the mode of supervision 1.	
	(0) DISABLED	Supervision 1 not in use.
	(1) LOW	When the signal selected by parameter 33.02 SUPERV1 ACT falls below the value of parameter 33.04 SUPERV1 LIM LO, bit 0 of 6.14 SUPERV STATUS is activated.
	(2) HIGH	When the signal selected by parameter 33.02 SUPERV1 ACT exceeds the value of parameter 33.03 SUPERV1 LIM HI, bit 0 of 6.14 SUPERV STATUS is activated.
	(3) ABS LOW	When the absolute value of the signal selected by parameter 33.02 SUPERV1 ACT falls below the value of parameter 33.04 SUPERV1 LIM LO, bit 0 of 6.14 SUPERV STATUS is activated.
	(4) ABS HIGH	When the absolute value of the signal selected by parameter 33.02 SUPERV1 ACT exceeds the value of parameter 33.03 SUPERV1 LIM HI, bit 0 of 6.14 SUPERV STATUS is activated.
33.02	SUPERV1 ACT	FW block: SUPERVISION (see above)
	Selects the signal to be monitored by supervision 1. See parameter 33.01 SUPERV1 FUNC.	
	Value pointer: Group and index	
33.03	SUPERV1 LIM HI	FW block: SUPERVISION (see above)
	Sets the upper limit for supervision 1. See parameter 33.01 SUPERV1 FUNC.	

	-32768…32768	Upper limit for supervision 1.
33.04	SUPERV1 LIM LO	FW block: SUPERVISION (see above)
	Sets the lower limit for supervision 1. See parameter 33.01 SUPERV1 FUNC.	
	-32768…32768	Lower limit for supervision 1.
33.05	SUPERV2 FUNC	FW block: SUPERVISION (see above)
	Selects the mode of supervision 2.	
	(0) DISABLED	Supervision 2 not in use.
	(1) LOW	When the signal selected by parameter 33.06 SUPERV2 ACT falls below the value of parameter 33.08 SUPERV2 LIM LO, bit 1 of 6.14 SUPERV STATUS is activated.
	(2) HIGH	When the signal selected by parameter 33.06 SUPERV2 ACT exceeds the value of parameter 33.07 SUPERV2 LIM HI, bit 1 of 6.14 SUPERV STATUS is activated.
	(3) ABS LOW	When the absolute value of the signal selected by parameter 33.06 SUPERV2 ACT falls below the value of parameter 33.08 SUPERV2 LIM LO, bit 1 of 6.14 SUPERV STATUS is activated.
	(4) ABS HIGH	When the absolute value of the signal selected by parameter 33.06 SUPERV2 ACT exceeds the value of parameter 33.07 SUPERV2 LIM HI, bit 1 of 6.14 SUPERV STATUS is activated.
33.06	SUPERV2 ACT	FW block: SUPERVISION (see above)
	Selects the signal to be monitored by supervision 2. See parameter 33.05 SUPERV2 FUNC.	
	Value pointer: Group and index	
33.07	SUPERV2 LIM HI	FW block: SUPERVISION (see above)
	Sets the upper limit for supervision 2. See parameter 33.05 SUPERV2 FUNC.	
	-32768…32768	Upper limit for supervision 2.
33.08	SUPERV2 LIM LO	FW block: SUPERVISION (see above)
	Sets the lower limit for supervision 2. See parameter 33.05 SUPERV2 FUNC.	
	-32768…32768	Lower limit for supervision 2.
33.09	SUPERV3 FUNC	FW block: SUPERVISION (see above)
	Selects the mode of supervision 3.	
	(0) DISABLED	Supervision 3 not in use.
	(1) LOW	When the signal selected by parameter 33.10 SUPERV3 ACT falls below the value of parameter 33.12 SUPERV3 LIM LO, bit 2 of 6.14 SUPERV STATUS is activated.

	(2) HIGH	When the signal selected by parameter 33.10 SUPERV3 ACT exceeds the value of parameter 33.11 SUPERV3 LIM HI, bit 2 of 6.14 SUPERV STATUS is activated.
	(3) ABS LOW	When the absolute value of the signal selected by parameter 33.10 SUPERV3 ACT falls below the value of parameter 33.12 SUPERV3 LIM LO, bit 2 of 6.14 SUPERV STATUS is activated.
	(4) ABS HIGH	When the absolute value of the signal selected by parameter 33.10 SUPERV3 ACT exceeds the value of parameter 33.11 SUPERV3 LIM HI, bit 2 of 6.14 SUPERV STATUS is activated.
33.10	SUPERV3 ACT	FW block: SUPERVISION (see above)
	Selects the signal to be monitored by supervision 3. See parameter 33.09 SUPERV3 FUNC.	
	Value pointer: Group and index	
33.11	SUPERV3 LIM HI	FW block: SUPERVISION (see above)
	Sets the upper limit for supervision 3. See parameter 33.09 SUPERV3 FUNC.	
	-32768…32768	Upper limit for supervision 3.
33.12	SUPERV3 LIM LO	FW block: SUPERVISION (see above)
	Sets the lower limit for supervision 3. See parameter 33.09 SUPERV3 FUNC.	
	-32768…32768	Lower limit for supervision 3.

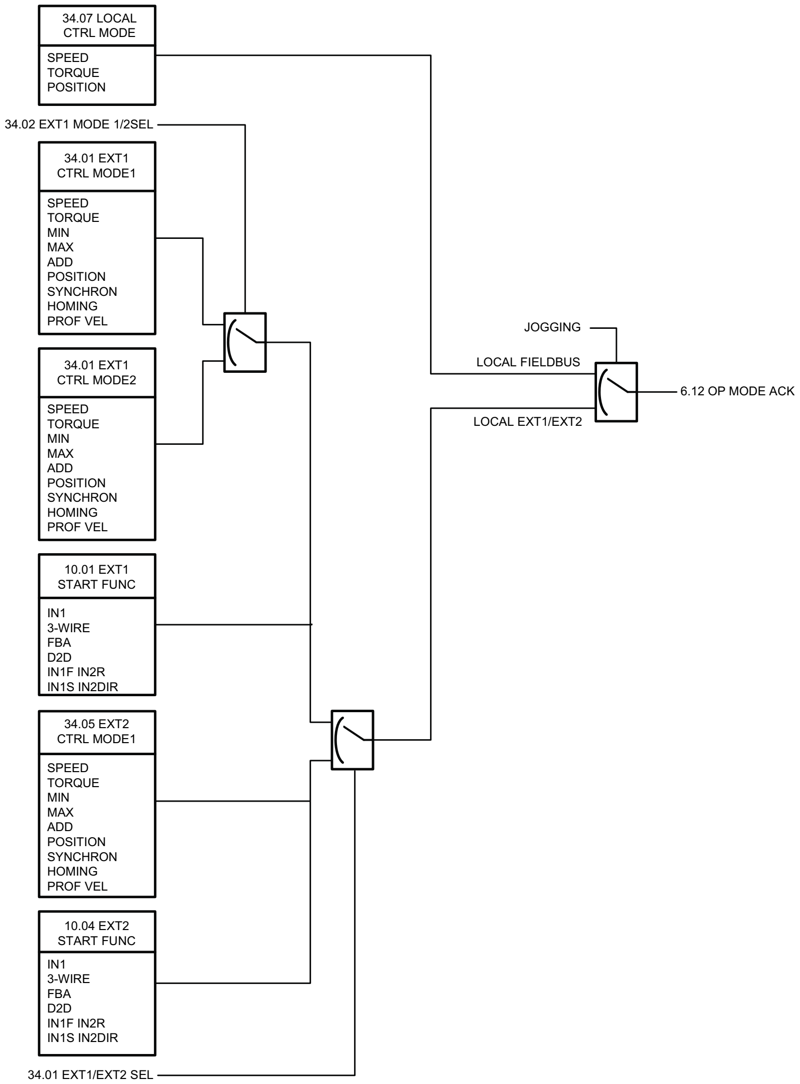
Group 34 REFERENCE CTRL

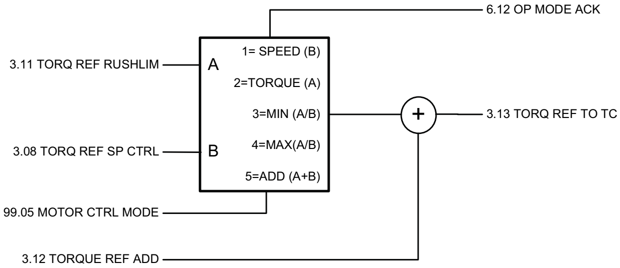
Reference source and type selection.

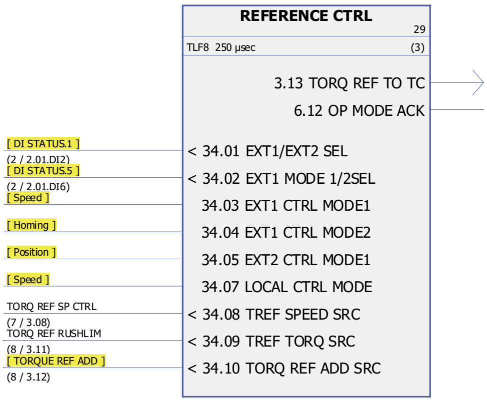
Using the parameters in this group, it is possible to select whether external control location EXT1 or EXT2 is used (either one is active at a time). These parameters also select the control mode (SPEED/TORQUE/MIN/MAX/ADD) and the used torque reference in local and external control.

For more information on control locations and control modes, see chapter *Drive control and features*.

For start/stop control in different control locations, see parameter group 10 START/ STOP (page 103).





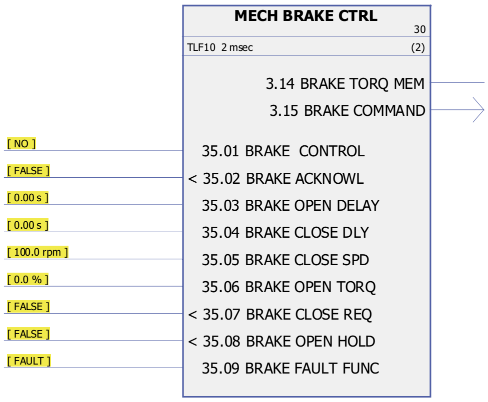
34 REFERENCE CTRL		
Firmware block: **REFERENCE CTRL** (34) This block • defines the selection method between external control locations EXT1 and EXT2 • configures control mode (SPEED/ TORQUE/MIN/MAX/ADD) selection • selects the torque reference used in local and external control • shows the torque reference (for torque control) and operating mode.		
Block outputs located in other parameter groups		3.13 TORQ REF TO TC (page 88) 6.12 OP MODE ACK (page 97)
34.01	EXT1/EXT2 SEL	FW block: REFERENCE CTRL (see above)
	Selects the source for external control location EXT1/EXT2 selection. 0 = EXT1. 1 = EXT2.	
	Bit pointer: Group, index and bit	
34.02	EXT1 MODE 1/2SEL	FW block: REFERENCE CTRL (see above)
	Selects the source for EXT1 control mode 1/2 selection. 1 = mode 2. 0 = mode 1. Control mode 1/2 is selected by parameter 34.03 EXT1 CTRL MODE1 / 34.04 EXT1 CTRL MODE2.	
	Bit pointer: Group, index and bit	
34.03	EXT1 CTRL MODE1	FW block: REFERENCE CTRL (see above)
	Selects control mode 1 for external control location EXT1.	
	(1) SPEED	Speed control. Torque reference is 3.08 TORQ REF SP CTRL, which is the output of the SPEED CONTROL firmware block. Torque reference source can be changed by parameter 34.08 TREF SPEED SRC.

	(2) TORQUE	Torque control. Torque reference is 3.11 TORQ REF RUSHLIM, which is the output of the TORQ REF MOD firmware block. Torque reference source can be changed by parameter 34.09 TREF TORQ SRC.
	(3) MIN	Combination of selections (1) SPEED and (2) TORQUE: Torque selector compares the torque reference and the speed controller output and the smaller of them is used.
	(4) MAX	Combination of selections (1) SPEED and (2) TORQUE: Torque selector compares the torque reference and the speed controller output and the greater of them is used.
	(5) ADD	Combination of selections (1) SPEED and (2) TORQUE: Torque selector adds the speed controller output to the torque reference.
	(6) POSITION	Position control. Torque reference is 3.08 TORQ REF SP CTRL, which is the output of the SPEED CONTROL firmware block. Speed reference is 4.01 SPEED REF POS, which is an output of the POS CONTROL firmware block. Speed reference source can be changed by parameter 26.03 SPEED REF PCTRL.
	(7) SYNCHRON	Synchron control. Torque reference is 3.08 TORQ REF SP CTRL, which is the output of the SPEED CONTROL firmware block. Speed reference is 4.01 SPEED REF POS, which is an output of the POS CONTROL firmware block. Speed reference source can be changed by parameter 26.03 SPEED REF PCTRL.
	(8) HOMING	Homing control. Torque reference is 3.08 TORQ REF SP CTRL, which is the output of the SPEED CONTROL firmware block. Speed reference is 4.20 SPEED FEED FWD, which is an output of the POS CONTROL firmware block. Speed reference source can be changed by parameter 26.04 SPEED FEED PCTRL.
	(9) PROF VEL	Profile velocity control. Used e.g. with CANOpen profile. Torque reference is 3.08 TORQ REF SP CTRL, which is the output of the SPEED CONTROL firmware block. Speed reference is 4.20 SPEED FEED FWD, which is an output of the POS CONTROL firmware block. Speed reference source can be changed by parameter 26.04 SPEED FEED PCTRL.
34.04	EXT1 CTRL MODE2	FW block: REFERENCE CTRL (see above)
	Selects control mode 2 for external control location EXT1. For selections, see parameter 34.03 EXT1 CTRL MODE1.	
34.05	EXT2 CTRL MODE1	FW block: REFERENCE CTRL (see above)
	Selects control mode for external control location EXT2. For selections, see parameter 34.03 EXT1 CTRL MODE1.	
34.07	LOCAL CTRL MODE	FW block: REFERENCE CTRL (see above)
	Selects the control mode for local control. **Note:** This parameter cannot be changed while the drive is running.	

	(1) SPEED	Speed control. Torque reference is 3.08 TORQ REF SP CTRL, which is the output of the SPEED CONTROL firmware block. Torque reference source can be changed by parameter 34.08 TREF SPEED SRC.
	(2) TORQUE	Torque control. Torque reference is 3.11 TORQ REF RUSHLIM, which is an output of the TORQ REF MOD firmware block. Torque reference source can be changed by parameter 34.09 TREF TORQ SRC.
	(6) POSITION	Position control. Torque reference is 3.08 TORQ REF SP CTRL, which is the output of the SPEED CONTROL firmware block. Speed reference is 4.01 SPEED REF POS, which is an output of the POS CONTROL firmware block. Speed reference source can be changed by parameter 26.03 SPEED REF PCTRL.
34.08	TREF SPEED SRC	FW block: REFERENCE CTRL (see above)
	Selects the source for the torque reference (from the speed controller). Default value is P.3.8, i.e. 3.08 TORQ REF SP CTRL, which is the output of the SPEED CONTROL firmware block. **Note:** This parameter has been locked, i.e. no user setting is possible.	
	Value pointer: Group and index	
34.09	TREF TORQ SRC	FW block: REFERENCE CTRL (see above)
	Selects the source for the torque reference (from the torque reference chain). Default value is P.3.11, i.e. 3.11 TORQ REF RUSHLIM, which is an output of the TORQ REF MOD firmware block. **Note:** This parameter has been locked, i.e. no user setting is possible.	
	Value pointer: Group and index	
34.10	TORQ REF ADD SRC	FW block: REFERENCE CTRL (see above)
	Selects the source for the torque reference added to the torque value after the torque selection. Default value is P.3.12, i.e. 3.12 TORQUE REF ADD, which is an output of the TORQ REF SEL firmware block. **Note:** This parameter has been locked, i.e. no user setting is possible.	
	Value pointer: Group and index	

Group 35 MECH BRAKE CTRL

Settings for the control of a mechanical brake. See also section *Mechanical brake* on page 49.

<table>
<tr><th colspan="3">35 MECH BRAKE CTRL</th></tr>
<tr><td colspan="2">Firmware block:
MECH BRAKE CTRL
(35)</td><td>
</td></tr>
<tr><td colspan="2">Block outputs located in other parameter groups</td><td>3.14 BRAKE TORQ MEM (page 88)
3.15 BRAKE COMMAND (page 88)</td></tr>
<tr><td>35.01</td><td>BRAKE CONTROL</td><td>FW block: MECH BRAKE CTRL (see above)</td></tr>
<tr><td></td><td colspan="2">Activates the brake control function with or without supervision.
Note: This parameter cannot be changed while the drive is running.</td></tr>
<tr><td></td><td>(0) NO</td><td>Inactive.</td></tr>
<tr><td></td><td>(1) WITH ACK</td><td>Brake control with supervision (supervision is activated by parameter 35.02 BRAKE ACKNOWL).</td></tr>
<tr><td></td><td>(2) NO ACK</td><td>Brake control without supervision.</td></tr>
<tr><td>35.02</td><td>BRAKE ACKNOWL</td><td>FW block: MECH BRAKE CTRL (see above)</td></tr>
<tr><td></td><td colspan="2">Selects the source for the external brake on/off supervision activation (when par. 35.01 BRAKE CONTROL = (1) WITH ACK). The use of the external on/off supervision signal is optional. 1 = The brake is open. 0 = The brake is closed.
Brake supervision is usually controlled with a digital input. It can also be controlled with an external control system, e.g. fieldbus.
When brake control error is detected the drive reacts as defined by parameter 35.09 BRAKE FAULT FUNC.
Note: This parameter cannot be changed while the drive is running.</td></tr>
<tr><td></td><td colspan="2">Bit pointer: Group, index and bit</td></tr>
</table>

35.03	BRAKE OPEN DELAY	FW block: MECH BRAKE CTRL (see above)
	Defines the brake open delay (= the delay between the internal open brake command and the release of the motor speed control). The delay counter starts when the drive has magnetised the motor and risen the motor torque to the level required at the brake release (parameter 35.06 BRAKE OPEN TORQ). Simultaneously with the counter start, the brake function energises the relay output controlling the brake and the brake starts opening. Set the delay the same as the mechanical opening delay of the brake specified by the brake manufacturer.	
	0…5 s	Brake open delay.
35.04	BRAKE CLOSE DLY	FW block: MECH BRAKE CTRL (see above)
	Defines the brake close delay. The delay counter starts when the motor actual speed has fallen below the set level (parameter 35.05 BRAKE CLOSE SPD) after the drive has received the stop command. Simultaneously with the counter start, the brake control function de-energises the relay output controlling the brake and the brake starts closing. During the delay, the brake function keeps the motor live preventing the motor speed from falling below zero. Set the delay time to the same value as the mechanical make-up time of the brake (= operating delay when closing) specified by the brake manufacturer.	
	0…60 s	Brake close delay.
35.05	BRAKE CLOSE SPD	FW block: MECH BRAKE CTRL (see above)
	Defines the brake close speed (an absolute value). See parameter 35.04 BRAKE CLOSE DLY.	
	0…1000 rpm	Brake close speed.
35.06	BRAKE OPEN TORQ	FW block: MECH BRAKE CTRL (see above)
	Defines the motor starting torque at brake release (in percent of the motor nominal torque).	
	0…1000%	Motor starting torque at brake release.
35.07	BRAKE CLOSE REQ	FW block: MECH BRAKE CTRL (see above)
	Selects the source for the brake close (open) request. 1 = Brake close request. 0 = Brake open request. **Note:** This parameter cannot be changed while the drive is running.	
	Bit pointer: Group, index and bit	
35.08	BRAKE OPEN HOLD	FW block: MECH BRAKE CTRL (see above)
	Selects the source for the activation of the brake open command hold. 1 = Hold active. 0 = Normal operation. **Note:** This parameter cannot be changed while the drive is running.	
	Bit pointer: Group, index and bit	
35.09	BRAKE FAULT FUNC	FW block: MECH BRAKE CTRL (see above)
	Defines how the drive reacts in case of mechanical brake control error. If brake control supervision has not been activated by parameter 35.01 BRAKE CONTROL, this parameter is disabled.	

	(0) FAULT	The drive trips on fault BRAKE NOT CLOSED / BRAKE NOT OPEN if the status of the optional external brake acknowledgement signal does not meet the status presumed by the brake control function. The drive trips on fault BRAKE START TORQUE if the required motor starting torque at brake release is not achieved.
	(1) ALARM	The drive generates alarm BRAKE NOT CLOSED / BRAKE NOT OPEN if the status of the optional external brake acknowledgement signal does not meet the status presumed by the brake control function. The drive generates alarm BRAKE START TORQUE if the required motor starting torque at brake release is not achieved.
	(2) OPEN FLT	The drive trips on fault BRAKE NOT CLOSED / BRAKE NOT OPEN if the status of the optional external brake acknowledgement signal does not meet the status presumed by the brake control function during the opening of the brake. Other brake function errors generate alarm BRAKE NOT CLOSED / BRAKE NOT OPEN.

Group 40 MOTOR CONTROL

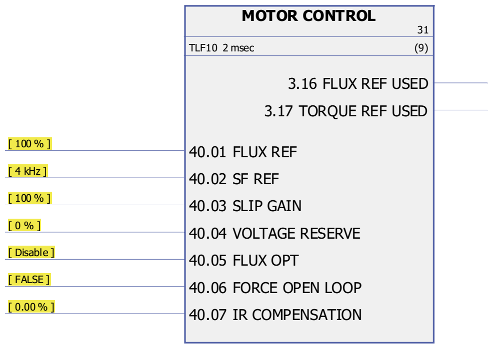
Motor control settings, such as

- flux reference
- drive switching frequency
- motor slip compensation
- voltage reserve
- flux optimisation
- IR compensation for scalar control mode.

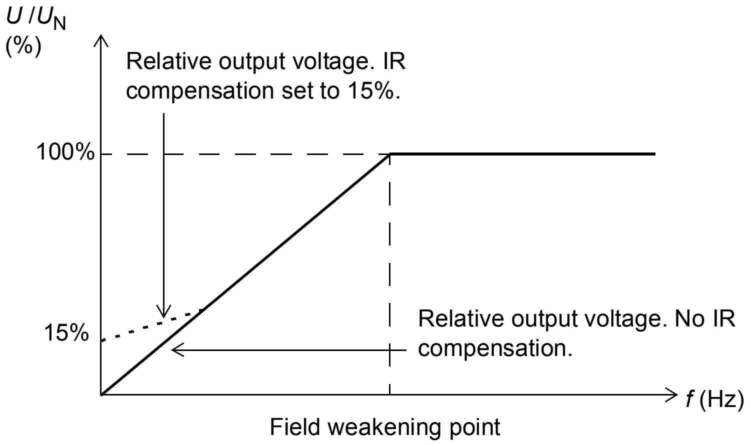
Flux optimisation

Flux optimisation reduces the total energy consumption and motor noise level when the drive operates below the nominal load. The total efficiency (motor and drive) can be improved by 1% to 10%, depending on the load torque and speed.

Note: Flux optimisation limits the dynamic control performance of the drive because with a small flux reference the drive torque cannot be increased fast.

40 MOTOR CONTROL		
Firmware block: **MOTOR CONTROL** (40) This block defines motor control settings such as • flux reference • drive switching frequency • motor slip compensation • voltage reserve • flux optimisation • IR compensation for scalar control mode. The block also shows the flux and torque reference used.		
Block outputs located in other parameter groups		3.16 FLUX REF USED (page 88) 3.17 TORQUE REF USED (page 88)
40.01	FLUX REF	FW block: MOTOR CONTROL (see above)
	Defines the flux reference.	
	0…200%	Flux reference.
40.02	SF REF	FW block: MOTOR CONTROL (see above)
	Defines the switching frequency of the drive. When switching frequency exceeds 4 kHz, the allowed drive output current is limited. See switching frequency derating in the appropriate *Hardware Manual*.	

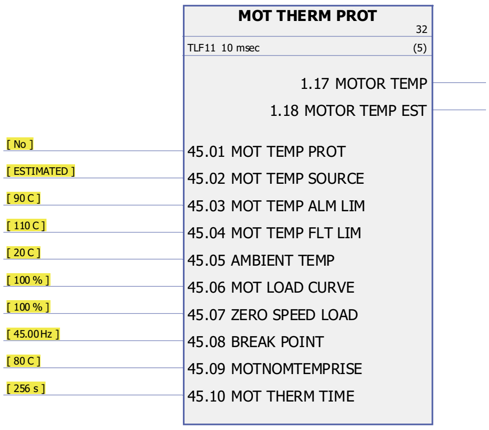
	1/2/3/4/5/8/16 kHz	Switching frequency.
40.03	SLIP GAIN	FW block: MOTOR CONTROL (see above)
	Defines the slip gain which is used to improve the estimated motor slip. 100% means full slip gain; 0% means no slip gain. The default value is 100%. Other values can be used if a static speed error is detected despite of the full slip gain. Example (with nominal load and nominal slip of 40 rpm): A 1000 rpm constant speed reference is given to the drive. Despite of the full slip gain (= 100%), a manual tachometer measurement from the motor axis gives a speed value of 998 rpm. The static speed error is 1000 rpm - 998 rpm = 2 rpm. To compensate the error, the slip gain should be increased. At the 105% gain value, no static speed error exists (2 rpm / 40 rpm = 5%).	
	0…200%	Slip gain.
40.04	VOLTAGE RESERVE	FW block: MOTOR CONTROL (see above)
	Defines the minimum allowed voltage reserve. When the voltage reserve has decreased to the set value, the drive enters the field weakening area. If the intermediate circuit DC voltage U_{dc} = 550 V and the voltage reserve is 5%, the RMS value of the maximum output voltage in steady-state operation is 0.95 × 550 V / sqrt(2) = 369 V The dynamic performance of the motor control in the field weakening area can be improved by increasing the voltage reserve value, but the drive enters the field weakening area earlier.	
	-4…50 %	Minimum allowed voltage reserve.
40.05	FLUX OPT	FW block: MOTOR CONTROL (see above)
	Enables the flux optimisation function. Flux optimisation improves motor efficiency and reduces noise. Flux optimisation is used in drives that usually operate below nominal load.	
	(0) DISABLE	Flux optimisation disabled.
	(1) ENABLE	Flux optimisation enabled.
40.06	FORCE OPEN LOOP	FW block: MOTOR CONTROL (see above)
	Defines the speed/position information used by the motor model.	
	(0) FALSE	Motor model uses the speed feedback selected by parameter 22.01 SPEED FB SEL.
	(1) TRUE	Motor model uses the internal speed estimate (even when parameter 22.01 SPEED FB SEL setting is (1) ENC1 SPEED / (2) ENC2 SPEED).

<table>
<tr><td>40.07</td><td>IR COMPENSATION</td><td>FW block: MOTOR CONTROL (see above)</td></tr>
<tr><td></td><td colspan="2">Defines the relative output voltage boost at zero speed (IR compensation). The function is useful in applications with high break-away torque when no DTC motor can be applied.
This parameter is only effective when parameter 99.05 MOTOR CTRL MODE is set to (1) SCALAR.


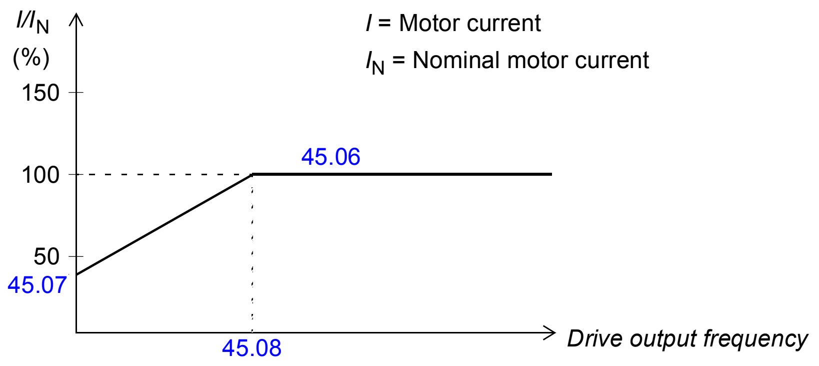
</td></tr>
<tr><td></td><td>0…50%</td><td>IR compensation.</td></tr>
</table>

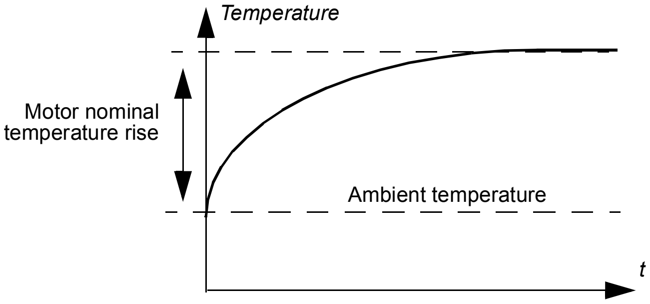
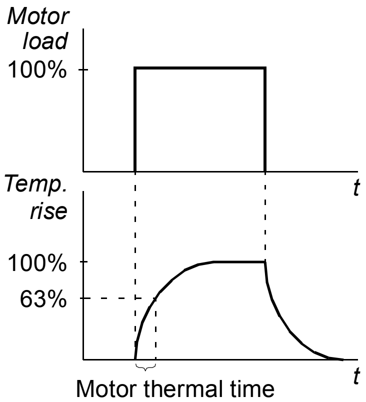
Group 45 MOT THERM PROT

Settings for thermal protection of the motor. See also section *Thermal motor protection* on page 41.

45 MOT THERM PROT		
Firmware block: **MOT THERM PROT** (45) Configures motor overtemperature protection and temperature measurement. Also shows the estimated and measured motor temperatures.		
Block outputs located in other parameter groups		1.17 MOTOR TEMP (page 78) 1.18 MOTOR TEMP EST (page 78)
45.01	MOT TEMP PROT	FW block: MOT THERM PROT (see above)
	Selects how the drive reacts when motor overtemperature is detected.	
	(0) NO	Inactive.
	(1) ALARM	The drive generates alarm MOTOR TEMPERATURE when the temperature exceeds the alarm level defined by parameter 45.03 MOT TEMP ALM LIM.
	(2) FAULT	The drive generates alarm MOTOR TEMPERATURE or trips on fault MOTOR OVERTEMP when the temperature exceeds the alarm/fault level defined by parameter 45.03 MOT TEMP ALM LIM / 45.04 MOT TEMP FLT LIM.
45.02	MOT TEMP SOURCE	FW block: MOT THERM PROT (see above)
	Selects the motor temperature protection. When overtemperature is detected the drive reacts as defined by parameter 45.01 MOT TEMP PROT.	

	(0) ESTIMATED	The temperature is supervised based on the motor thermal protection model, which uses the motor thermal time constant (parameter 45.10 MOT THERM TIME) and the motor load curve (parameters 45.06…45.08). User tuning is typically needed only if the ambient temperature differs from the normal operating temperature specified for the motor. The motor temperature increases if it operates in the region above the motor load curve. The motor temperature decreases if it operates in the region below the motor load curve (if the motor is overheated). **WARNING!** The model does not protect the motor if it does not cool properly due to dust and dirt.
	(1) KTY JCU	The temperature is supervised using a KTY84 sensor connected to drive thermistor input TH.
	(2) KTY 1st FEN	The temperature is supervised using a KTY84 sensor connected to encoder interface module FEN-xx installed in drive Slot 1/2. If two encoder interface modules are used, encoder module connected to Slot 1 is used for the temperature supervision. **Note:** This selection does not apply for FEN-01. *
	(3) KTY 2nd FEN	The temperature is supervised using a KTY84 sensor connected to encoder interface module FEN-xx installed in drive Slot 1/2. If two encoder interface modules are used, encoder module connected to Slot 2 is used for the temperature supervision. **Note:** This selection does not apply for FEN-01. *
	(4) PTC JCU	The temperature is supervised using 1…3 PTC sensors connected to drive thermistor input TH.
	(5) PTC 1st FEN	The temperature is supervised using a PTC sensor connected to encoder interface module FEN-xx installed in drive Slot 1/2. If two encoder interface modules are used, encoder module connected to Slot 1 is used for the temperature supervision. *
	(6) PTC 2nd FEN	The temperature is supervised using a PTC sensor connected to encoder interface module FEN-xx installed in drive Slot 1/2. If two encoder interface modules are used, encoder module connected to Slot 2 is used for the temperature supervision. *
	***Note:** If one FEN-xx module is used, parameter setting must be either (2) KTY 1st FEN or (5) PTC 1st FEN. The FEN-xx module can be in either Slot 1 or Slot 2.	
45.03	MOT TEMP ALM LIM	FW block: MOT THERM PROT (see above)
	Defines the alarm limit for the motor overtemperature protection (when par. 45.01 MOT TEMP PROT = (1) ALARM/(2) FAULT).	
	0…200 °C	Motor overtemperature alarm limit.
45.04	MOT TEMP FLT LIM	FW block: MOT THERM PROT (see above)
	Defines the fault limit for the motor overtemperature protection (when par. 45.01 MOT TEMP PROT = (2) FAULT).	
	0…200 °C	Motor overtemperature fault limit.

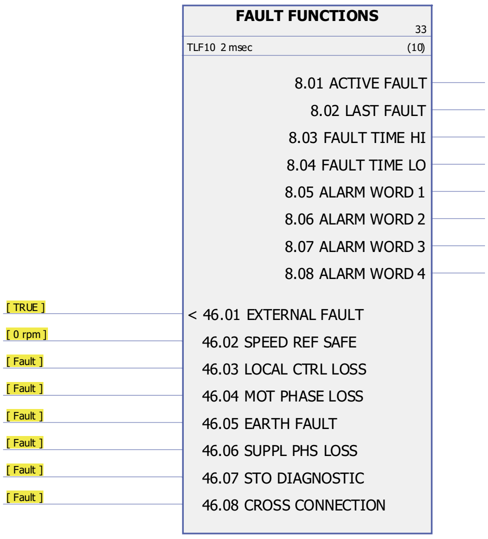
45.05	AMBIENT TEMP	FW block: MOT THERM PROT (see above)
	Defines the ambient temperature for the thermal protection mode.	
	-60…100 °C	Ambient temperature.
45.06	MOT LOAD CURVE	FW block: MOT THERM PROT (see above)
	Defines the load curve together with parameters 45.07 ZERO SPEED LOAD and 45.08 BREAK POINT. The value is given in percent of nominal motor current. When the parameter is set to 100%, the maximum load is equal to the value of the parameter 99.06 MOT NOM CURRENT (higher loads heat up the motor). The load curve level should be adjusted if the ambient temperature differs from the nominal value.  The load curve is used by the motor thermal protection model when parameter 45.02 MOT TEMP SOURCE is set to (0) ESTIMATED.	
	50…150%	Motor current above breakpoint.
45.07	ZERO SPEED LOAD	FW block: MOT THERM PROT (see above)
	Defines the load curve together with parameters 45.06 MOT LOAD CURVE and 45.08 BREAK POINT. Defines the maximum motor load at zero speed of the load curve. A higher value can be used if the motor has an external motor fan to boost the cooling. See the motor manufacturer's recommendations. The value is given in percent of nominal motor current. The load curve is used by the motor thermal protection model when parameter 45.02 MOT TEMP SOURCE is set to (0) ESTIMATED.	
	50…150%	Motor current at zero speed.
45.08	BREAK POINT	FW block: MOT THERM PROT (see above)
	Defines the load curve together with parameters 45.06 MOT LOAD CURVE and 45.07 ZERO SPEED LOAD. Defines the break point frequency of the load curve i.e. the point at which the motor load curve begins to decrease from the value of parameter 45.06 MOT LOAD CURVE to the value of parameter 45.07 ZERO SPEED LOAD. The load curve is used by the motor thermal protection model when parameter 45.02 MOT TEMP SOURCE is set to (0) ESTIMATED.	
	0.01…500 Hz	Load curve breakpoint.

45.09	MOTNOMTEMPRISE	FW block: MOT THERM PROT (see above)
	Defines the temperature rise of the motor when the motor is loaded with nominal current. See the motor manufacturer's recommendations. The temperature rise value is used by the motor thermal protection model when parameter 45.02 MOT TEMP SOURCE is set to (0) ESTIMATED.  	
	0…300 °C	Motor temperature rise.
45.10	MOT THERM TIME	FW block: MOT THERM PROT (see above)
	Defines the thermal time constant for the motor thermal protection model (i.e. time inside which the temperature has reached 63% of the nominal temperature). See the motor manufacturer's recommendations. The motor thermal protection model is used when parameter 45.02 MOT TEMP SOURCE is set to (0) ESTIMATED.  	
	100…10000 s	Motor thermal time.

Group 46 FAULT FUNCTIONS

Definition of drive behaviour upon a fault situation.

An alarm or a fault message indicates abnormal drive status. For the possible causes and remedies, see chapter *Fault tracing*.

46 FAULT FUNCTIONS		
Firmware block: **FAULT FUNCTIONS** (46) This block • configures supervision of external faults by defining the source (for example, a digital input) for external fault indication signal • selects the reaction of the drive (alarm; fault; continuation at safe speed in some cases) upon situations like local control communication break, motor/supply phase loss, earth fault, or Safe Torque Off function activation • shows the codes of the latest faults, the time at which the active fault occurred, and the alarm words.		
Block outputs located in other parameter groups		8.01 ACTIVE FAULT (page 99) 8.02 LAST FAULT (page 99) 8.03 FAULT TIME HI (page 99) 8.04 FAULT TIME LO (page 99) 8.05 ALARM WORD 1 (page 99) 8.06 ALARM WORD 2 (page 100) 8.07 ALARM WORD 3 (page 100) 8.08 ALARM WORD 4 (page 100)
46.01	EXTERNAL FAULT	FW block: FAULT FUNCTIONS (see above)
	Selects an interface for an external fault signal. 0 = External fault trip. 1 = No external fault.	
	Bit pointer: Group, index and bit	
46.02	SPEED REF SAFE	FW block: FAULT FUNCTIONS (see above)
	Defines the fault speed. Used as a speed reference when an alarm occurs when parameter 13.12 AI SUPERVISION / 46.03 LOCAL CTRL LOSS / 50.02 COMM LOSS FUNC setting is (2) SPD REF SAFE.	
	-30000…30000 rpm	Fault speed.
46.03	LOCAL CTRL LOSS	FW block: FAULT FUNCTIONS (see above)
	Selects how the drive reacts to a control panel or PC tool communication break.	
	(0) NO	No action.

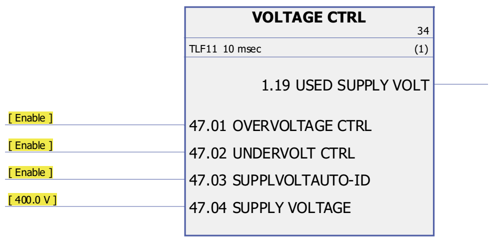
	(1) FAULT	Drive trips on LOCAL CTRL LOSS fault.
	(2) SPD REF SAFE	The drive generates alarm LOCAL CTRL LOSS and sets the speed to the speed defined by parameter 46.02 SPEED REF SAFE. **WARNING!** Make sure that it is safe to continue operation in case of a communication break.
	(3) LAST SPEED	The drive generates alarm LOCAL CTRL LOSS and freezes the speed to the level the drive was operating at. The speed is determined by the average speed over the previous 10 seconds. **WARNING!** Make sure that it is safe to continue operation in case of a communication break.
46.04	MOT PHASE LOSS	FW block: FAULT FUNCTIONS (see above)
	Selects how the drive reacts when a motor phase loss is detected.	
	(0) NO	No action.
	(1) FAULT	Drive trips on MOTOR PHASE fault.
46.05	EARTH FAULT	FW block: FAULT FUNCTIONS (see above)
	Selects how the drive reacts when an earth fault or current unbalance is detected in the motor or the motor cable.	
	(0) NO	No action.
	(1) WARNING	Drive generates alarm EARTH FAULT.
	(2) FAULT	Drive trips on EARTH FAULT.
46.06	SUPPL PHS LOSS	FW block: FAULT FUNCTIONS (see above)
	Selects how the drive reacts when a supply phase loss is detected.	
	(0) NO	No reaction.
	(1) FAULT	Drive trips on SUPPLY PHASE fault.
46.07	STO DIAGNOSTIC	FW block: FAULT FUNCTIONS (see above)
	Selects how the drive reacts when the drive detects that the Safe Torque Off function is active while the drive is stopped. The Safe Torque Off function disables the control voltage of the power semiconductors of the drive output stage, thus preventing the inverter from generating the voltage required to rotate the motor. For Safe Torque Off wiring, see the appropriate hardware manual. **Note:** This parameter is only for supervision. The Safe Torque Off function can activate, even when this parameter selection is NO. **Note**: Fault STO 1 LOST / STO 2 LOST is activated if safety circuit signal 1/2 is lost when the drive is at stopped state and this parameter is set to (2) ALARM or (3) NO.	
	(1) FAULT	Drive trips on SAFE TORQUE OFF fault.
	(2) ALARM	Drive generates SAFE TORQUE OFF alarm.
	(3) NO	No reaction.

46.08	CROSS CONNECTION	FW block: FAULT FUNCTIONS (see above)
	Selects how the drive reacts to incorrect input power and motor cable connection (i.e. input power cable is connected to drive motor connection).	
	(0) NO	No reaction.
	(1) FAULT	Drive trips on CABLE CROSS CON fault.

Group 47 VOLTAGE CTRL

Settings for overvoltage and undervoltage control, and supply voltage.

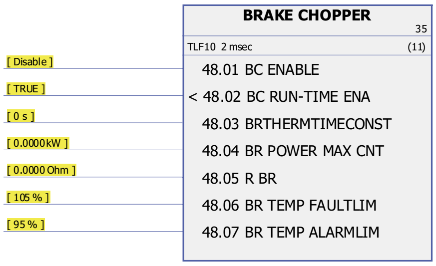
<table>
<tr><td colspan="3">47 VOLTAGE CTRL</td></tr>
<tr><td colspan="2">Firmware block:
VOLTAGE CTRL
(47)

This block
• enables/disables overvoltage and undervoltage control
• enables/disables automatic identification of supply voltage
• provides a parameter for manual definition of supply voltage
• shows the supply voltage value used by the control program.</td><td>
</td></tr>
<tr><td colspan="2">Block outputs located in other parameter groups</td><td>1.19 USED SUPPLY VOLT (page 78)</td></tr>
<tr><td>47.01</td><td>OVERVOLTAGE CTRL</td><td>FW block: VOLTAGE CTRL (see above)</td></tr>
<tr><td></td><td colspan="2">Enables the overvoltage control of the intermediate DC link. Fast braking of a high inertia load causes the voltage to rise to the overvoltage control limit. To prevent the DC voltage from exceeding the limit, the overvoltage controller automatically decreases the braking torque.
Note: If a brake chopper and resistor or a regenerative supply section are included in the drive, the controller must be disabled.</td></tr>
<tr><td></td><td>(0) DISABLE</td><td>Overvoltage control disabled.</td></tr>
<tr><td></td><td>(1) ENABLE</td><td>Overvoltage control enabled.</td></tr>
<tr><td>47.02</td><td>UNDERVOLT CTRL</td><td>FW block: VOLTAGE CTRL (see above)</td></tr>
<tr><td></td><td colspan="2">Enables the undervoltage control of the intermediate DC link. If the DC voltage drops due to input power cut off, the undervoltage controller will automatically decrease the motor torque in order to keep the voltage above the lower limit. By decreasing the motor torque, the inertia of the load will cause regeneration back to the drive, keeping the DC link charged and preventing an undervoltage trip until the motor coasts to stop. This will act as a power-loss ride-through functionality in systems with high inertia, such as a centrifuge or a fan.</td></tr>
<tr><td></td><td>(0) DISABLE</td><td>Undervoltage control disabled.</td></tr>
<tr><td></td><td>(1) ENABLE</td><td>Undervoltage control enabled.</td></tr>
<tr><td>47.03</td><td>SUPPLVOLTAUTO-ID</td><td>FW block: VOLTAGE CTRL (see above)</td></tr>
<tr><td></td><td colspan="2">Enables the auto-identification of the supply voltage.</td></tr>
<tr><td></td><td>(0) DISABLE</td><td>Auto-identification of supply voltage disabled.</td></tr>
<tr><td></td><td>(1) ENABLE</td><td>Auto-identification of supply voltage enabled.</td></tr>
</table>

47.04	SUPPLY VOLTAGE	FW block: VOLTAGE CTRL (see above)
	Defines the nominal supply voltage. Used if auto-identification of the supply voltage is not enabled by parameter 47.03 SUPPLVOLTAUTO-ID.	
	0…1000 V	Nominal supply voltage.

Group 48 BRAKE CHOPPER

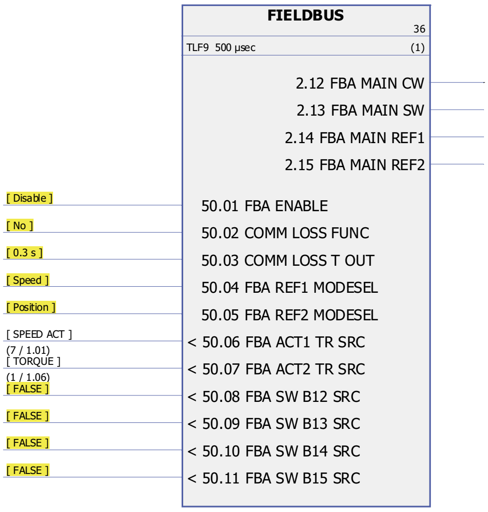
Configuration of internal brake chopper.

48 BRAKE CHOPPER		
Firmware block: **BRAKE CHOPPER** (48) This block configures the brake chopper control and supervision.		
48.01	BC ENABLE	FW block: BRAKE CHOPPER (see above)
	Enables the brake chopper control. **Note:** Before enabling brake chopper control, ensure the brake resistor is installed and the overvoltage control is switched off (parameter 47.01 OVERVOLTAGE CTRL). The drive has a built-in brake chopper.	
	(0) DISABLE	Brake chopper control disabled.
	(1) ENABLETHERM	Enable brake chopper control with resistor overload protection.
	(2) ENABLE	Enable brake chopper control without resistor overload protection. This setting can be used, for example, if the resistor is equipped with a thermal circuit breaker that is wired to stop the drive if the resistor overheats.
48.02	BC RUN-TIME ENA	FW block: BRAKE CHOPPER (see above)
	Selects the source for quick run-time brake chopper control. 0 = Brake chopper IGBT pulses are cut off. 1 = Normal brake chopper IGBT modulation. The overvoltage control is automatically switched off. This parameter can be used to program the chopper control to function only when the drive is operating in generating mode.	
	Bit pointer: Group, index and bit	
48.03	BRTHERMTIMECONST	FW block: BRAKE CHOPPER (see above)
	Defines the thermal time constant of the brake resistor for overload protection.	
	0…10000 s	Brake resistor thermal time constant.
48.04	BR POWER MAX CNT	FW block: BRAKE CHOPPER (see above)
	Defines the maximum continuous braking power which will raise the resistor temperature to the maximum allowed value. The value is used in the overload protection.	
	0…10000 kW	Maximum continuous braking power.

48.05	R BR	FW block: BRAKE CHOPPER (see above)
	Defines the resistance value of the brake resistor. The value is used for brake chopper protection.	
	0.1…1000 ohm	Resistance.
48.06	BR TEMP FAULTLIM	FW block: BRAKE CHOPPER (see above)
	Selects the fault limit for the brake resistor temperature supervision. The value is given in percent of the temperature the resistor reaches when loaded with the power defined by parameter 48.04 BR POWER MAX CNT. When the limit is exceeded the drive trips on fault BR OVERHEAT.	
	0…150%	Resistor temperature fault limit.
48.07	BR TEMP ALARMLIM	FW block: BRAKE CHOPPER (see above)
	Selects the alarm limit for the brake resistor temperature supervision. The value is given in percent of the temperature the resistor reaches when loaded with the power defined by parameter 48.04 BR POWER MAX CNT. When limit is exceeded the drive generates alarm BR OVERHEAT.	
	0…150%	Resistor temperature alarm limit.

Group 50 FIELDBUS

Basic settings for fieldbus communication. See also chapter *Appendix A – Fieldbus control* on page 385.

50 FIELDBUS		
Firmware block: **FIELDBUS** (50) This block • initialises the fieldbus communication • selects communication supervision method • defines scaling of the fieldbus references and actual values • selects sources for programmable status word bits • shows the fieldbus control and status words, and references.		
Block outputs located in other parameter groups		2.12 FBA MAIN CW (page 81) 2.13 FBA MAIN SW (page 84) 2.14 FBA MAIN REF1 (page 85) 2.15 FBA MAIN REF2 (page 85)
50.01	FBA ENABLE	FW block: FIELDBUS (see above)
	Enables communication between the drive and fieldbus adapter.	
	(0) DISABLE	No communication.
	(1) ENABLE	Communication between drive and fieldbus adapter enabled.
50.02	COMM LOSS FUNC	FW block: FIELDBUS (see above)
	Selects how the drive reacts in a fieldbus communication break. The time delay is defined by parameter 50.03 COMM LOSS T OUT.	
	(0) NO	Protection inactive.
	(1) FAULT	Protection active. The drive generates alarm FIELDBUS COMM and sets the speed to the value defined by parameter 46.02 SPEED REF SAFE. ⚠ **WARNING!** Make sure that it is safe to continue operation in case of a communication break.
	(2) SPD REF SAFE	Protection is active.

	(3) LAST SPEED	Protection is active. The drive generates alarm FIELDBUS COMM and freezes the speed to the level the drive was operating at. The speed is determined by the average speed over the previous 10 seconds. **WARNING**! Make sure that it is safe to continue operation in case of a communication break.
50.03	COMM LOSS T OUT	FW block: FIELDBUS (see above)
	Defines the time delay before the action defined by parameter 50.02 COMM LOSS FUNC is taken. Time count starts when the link fails to update the message.	
	0.3…6553.5 s	Delay for fieldbus communication loss function.
50.04	FBA REF1 MODESEL	FW block: FIELDBUS (see above)
	Selects the fieldbus reference FBA REF1 scaling and the actual value, which is sent to the fieldbus (FBA ACT1).	
	(0) RAW DATA	No scaling (i.e. data is transmitted without scaling). Source for the actual value, which is sent to the fieldbus, is selected by parameter 50.06 FBA ACT1 TR SRC.
	(1) TORQUE	Fieldbus adapter module uses torque reference scaling. Torque reference scaling is defined by the used fieldbus profile (e.g. with ABB Drives Profile integer value 10000 corresponds to 100% torque value). Signal 1.06 TORQUE is sent to the fieldbus as an actual value. See the *User's Manual* of the appropriate fieldbus adapter module.
	(2) SPEED	Fieldbus adapter module uses speed reference scaling. Speed reference scaling is defined by the used fieldbus profile (e.g. with ABB Drives Profile integer value 20000 corresponds to the value of parameter 25.02 SPEED SCALING). Signal 1.01 SPEED ACT is sent to the fieldbus as an actual value. See the *User's Manual* of the appropriate fieldbus adapter module.
	(3) POSITION	Fieldbus adapter module uses position reference scaling. Position reference scaling is defined by parameters 60.05 POS UNIT and 60.08 POS2INT SCALE. Signal 1.12 POS ACT is sent to the fieldbus as an actual value.
	(4) VELOCITY	Fieldbus adapter module uses position speed scaling. Position speed scaling is defined by parameters 60.10 POS SPEED UNIT and 60.11 POS SPEED2INT. Signal 4.02 SPEED ACT LOAD is sent to the fieldbus as an actual value.
	(5) AUTO	One of the above selections is chosen automatically according to the currently active control mode. See parameter group 34 REFERENCE CTRL.
50.05	FBA REF2 MODESEL	FW block: FIELDBUS (see above)
	Selects the fieldbus reference FBA REF2 scaling. See parameter 50.04 FBA REF1 MODESEL.	

50.06	FBA ACT1 TR SRC	FW block: FIELDBUS (see above)
	Selects the source for fieldbus actual value 1 when parameter 50.04 FBA REF1 MODESEL / 50.05 FBA REF2 MODESEL is set to (0) RAW DATA.	
	Value pointer: Group and index	
50.07	FBA ACT2 TR SRC	FW block: FIELDBUS (see above)
	Selects the source for fieldbus actual value 2 when parameter 50.04 FBA REF1 MODESEL / 50.05 FBA REF2 MODESEL is set to (0) RAW DATA.	
	Value pointer: Group and index	
50.08	FBA SW B12 SRC	FW block: FIELDBUS (see above)
	Selects the source for freely programmable fieldbus status word bit 28 (2.13 FBA MAIN SW bit 28 SW B12).	
	Bit pointer: Group, index and bit	
50.09	FBA SW B13 SRC	FW block: FIELDBUS (see above)
	Selects the source for freely programmable fieldbus status word bit 29 (2.13 FBA MAIN SW bit 29 SW B13).	
	Bit pointer: Group, index and bit	
50.10	FBA SW B14 SRC	FW block: FIELDBUS (see above)
	Selects the source for freely programmable fieldbus status word bit 30 (2.13 FBA MAIN SW bit 30 SW B14).	
	Bit pointer: Group, index and bit	
50.11	FBA SW B15 SRC	FW block: FIELDBUS (see above)
	Selects the source for freely programmable fieldbus status word bit 31 (2.13 FBA MAIN SW bit 31 SW B15).	
	Bit pointer: Group, index and bit	

Group 51 FBA SETTINGS

Further fieldbus communication configuration. These parameters need to be set only if a fieldbus adapter module is installed. See also *Appendix A – Fieldbus control* on page 385.

Notes:

- This parameter group is presented in the *User's Manual* of the fieldbus adapter as parameter group 1 or A.
- The new settings will take effect when the drive is powered up the next time (before powering off the drive, wait at least 1 minute), or when parameter 51.27 FBA PAR REFRESH is activated.

51 FBA SETTINGS		
51.01	FBA TYPE	FW block: None
	Displays the type of the connected fieldbus adapter module.	
	NOT DEFINED	Fieldbus adapter module not found (not properly connected, or disabled by parameter 50.01 FBA ENABLE).
	(1)	FPBA-xx PROFIBUS-DP adapter module.
	(32)	FCAN-xx CANopen adapter module.
	(37)	FDNA-xx DeviceNet adapter module.
51.02	FBA PAR2	FW block: None
…	….	….
51.26	FBA PAR26	FW block: None
	Parameters 51.02…51.26 are adapter module-specific. For more information, see the *User's Manual* of the fieldbus adapter module. Note that not all of these parameters are necessarily visible.	
51.27	FBA PAR REFRESH	FW block: None
	Validates any changed adapter module configuration parameter settings. After refreshing, the value reverts automatically to (0) DONE. **Note:** This parameter cannot be changed while the drive is running.	
	(0) DONE	Refreshing done.
	(1) REFRESH	Refreshing.
51.28	PAR TABLE VER	FW block: None
	Displays the parameter table revision of the fieldbus adapter module mapping file stored in the memory of the drive. In format xyz, where x = major revision number; y = minor revision number; z = correction number.	

51.29	DRIVE TYPE CODE	FW block: None
	Displays the drive type code of the fieldbus adapter module mapping file stored in the memory of the drive. Example: 520 = ACSM1 Speed and Torque Control Program.	
51.30	MAPPING FILE VER	FW block: None
	Displays the fieldbus adapter module mapping file revision stored in the memory of the drive. In decimal format. Example: 1 = revision 1.	
51.31	D2FBA COMM STA	FW block: None
	Displays the status of the fieldbus adapter module communication.	
	(0) IDLE	Adapter not configured.
	(1) EXEC. INIT	Adapter initializing.
	(2) TIME OUT	A timeout has occurred in the communication between the adapter and the drive.
	(3) CONFIG ERROR	Adapter configuration error – the major or minor revision code of the common program revision in the fieldbus adapter module is not the revision required by the module (see par. 51.32 FBA COMM SW VER), or mapping file upload has failed more than three times.
	(4) OFF-LINE	Adapter is off-line.
	(5) ON-LINE	Adapter is on-line.
	(6) RESET	Adapter is performing a hardware reset.
51.32	FBA COMM SW VER	FW block: None
	Displays the common program revision of the adapter module. In format axyz, where a = major revision number, xy = minor revision numbers. z = correction letter. Example: 190A = revision 1.90A.	
51.33	FBA APPL SW VER	FW block: None
	Displays the application program revision of the adapter module. In format axyz, where: a = major revision number, xy = minor revision numbers, z = correction letter. Example: 190A = revision 1.90A.	

Group 52 FBA DATA IN

These parameters select the data to be sent by the drive to the fieldbus controller, and need to be set only if a fieldbus adapter module is installed. See also *Appendix A – Fieldbus control* on page 385.

Notes:

- This parameter group is presented in the *User's Manual* of the fieldbus adapter as parameter group 3 or C.
- The new settings will take effect when the drive is powered up the next time (before powering off the drive, wait at least 1 minute), or when parameter 51.27 FBA PAR REFRESH is activated.
- The maximum number of data words is protocol-dependent.

52 FBA DATA IN		
52.01	FBA DATA IN1	FW block: None
	Selects data to be transferred from the drive to the fieldbus controller.	
	0	Not in use.
	4	Status Word (16 bits).
	5	Actual value 1 (16 bits).
	6	Actual value 2 (16 bits).
	14	Status Word (32 bits).
	15	Actual value 1 (32 bits).
	16	Actual value 2 (32 bits).
	101…9999	Parameter index.
52.02	FBA DATA IN2	FW block: None
…	…	
52.12	FBA DATA IN12	FW block: None
	See 52.01 FBA DATA IN1.	

Group 53 FBA DATA OUT

These parameters select the data to be sent by the fieldbus controller to the drive, and need to be set only if a fieldbus adapter module is installed. See also *Appendix A – Fieldbus control* on page 385.

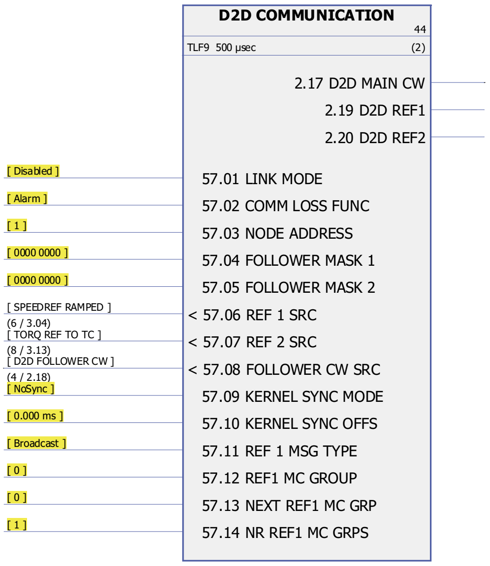
Notes:

- This parameter group is presented in the *User's Manual* of the fieldbus adapter as parameter group 2 or B.
- The new settings will take effect when the drive is powered up the next time (before powering off the drive, wait at least 1 minute), or when parameter 51.27 FBA PAR REFRESH is activated.
- The maximum number of data words is protocol-dependent.

53 FBA DATA OUT		
53.01	FBA DATA OUT1	FW block: None
	Selects data to be transferred from the fieldbus controller to the drive.	
	0	Not in use.
	1	Control Word (16 bits).
	2	Reference REF1 (16 bits).
	3	Reference REF2 (16 bits).
	11	Control Word (32 bits).
	12	Reference REF1 (32 bits).
	13	Reference REF2 (32 bits).
	1001…9999	Parameter index.
53.02	FBA DATA OUT2	FW block: None
…		
53.12	FBA DATA OUT12	FW block: None
	See 53.01 FBA DATA OUT1.	

Group 57 D2D COMMUNICATION

Drive-to-drive communication settings. See *Appendix B – Drive-to-drive link* on page 391.

57 D2D COMMUNICATION		
Firmware block: **D2D COMMUNICATION** (57) This block sets up the drive-to-drive communication. It also shows the main drive-to-drive control word and the two references.		
Block outputs located in other parameter groups		2.17 D2D MAIN CW (page 86) 2.19 D2D REF1 (page 86) 2.20 D2D REF2 (page 86)
57.01	LINK MODE	FW block: D2D COMMUNICATION (see above)
	Activates the drive-to-drive connection.	
	(0) DISABLED	Drive-to-drive connection disabled.
	(1) FOLLOWER	The drive is a follower on the drive-to-drive link.
	(2) MASTER	The drive is the master on the drive-to-drive link. Only one drive can be the master at a time.
57.02	COMM LOSS FUNC	FW block: D2D COMMUNICATION (see above)
	Selects how the drive acts when an erroneous drive-to-drive configuration or a communication break is detected.	
	(0) NO	Protection inactive.
	(1) ALARM	The drive generates an alarm.
	(2) FAULT	The drive trips on a fault.

57.03	NODE ADDRESS	FW block: D2D COMMUNICATION (see above)
	Sets the node address for a follower drive. Each follower must have a dedicated node address. **Note:** If the drive is set to be the master on the drive-to-drive link, this parameter has no effect (the master is automatically assigned node address 0).	
	1…62	Node address.
57.04	FOLLOWER MASK 1	FW block: D2D COMMUNICATION (see above)
	On the master drive, selects the followers to be polled. If no response is received from a polled follower, the action selected by parameter 57.02 COMM LOSS FUNC is taken. The least significant bit represents follower with node address 1, while the most significant bit represents follower 31. When a bit is set to 1, the corresponding node address is polled. For example, followers 1 and 2 are polled when this parameter is set to the value of 0x3.	
	0x00000000…0x7FFFFFFF	Follower mask 1.
57.05	FOLLOWER MASK 2	FW block: D2D COMMUNICATION (see above)
	On the master drive, selects the followers to be polled. If no response is received from a polled follower, the action selected by parameter 57.02 COMM LOSS FUNC is taken. The least significant bit represents follower with node address 32, while the most significant bit represents follower 62. When a bit is set to 1, the corresponding node address is polled. For example, followers 32 and 33 are polled when this parameter is set to the value of 0x3.	
	0x00000000…0x7FFFFFFF	Follower mask 2.
57.06	REF 1 SRC	FW block: D2D COMMUNICATION (see above)
	Selects the source of D2D reference 1 sent to the followers. The parameter is effective on the master drive, as well as submasters (57.03 NODE ADDRESS = 57.12 REF1 MC GROUP) in a multicast message chain (see parameter 57.11 REF 1 MSG TYPE). The default value is P.03.04, i.e. 3.04 SPEEDREF RAMPED.	
	Value pointer: Group and index.	
57.07	REF 2 SRC	FW block: D2D COMMUNICATION (see above)
	On the master drive, selects the source of D2D reference 2 broadcast to all followers. The default value is P.03.13, i.e. 3.13 TORQ REF TO TC.	
	Value pointer: Group and index.	
57.08	FOLLOWER CW SRC	FW block: D2D COMMUNICATION (see above)
	Selects the source of the D2D control word sent to the followers. The parameter is effective on the master drive, as well as submasters in a multicast message chain (see parameter 57.11 REF 1 MSG TYPE). The default value is P.02.18, i.e. 2.18 D2D FOLLOWER CW.	
	Value pointer: Group and index.	
57.09	KERNEL SYNC MODE	FW block: D2D COMMUNICATION (see above)
	Determines which signal the time levels of the drive are synchronised with. An offset can be defined by parameter 57.10 KERNEL SYNC OFFS if desired.	

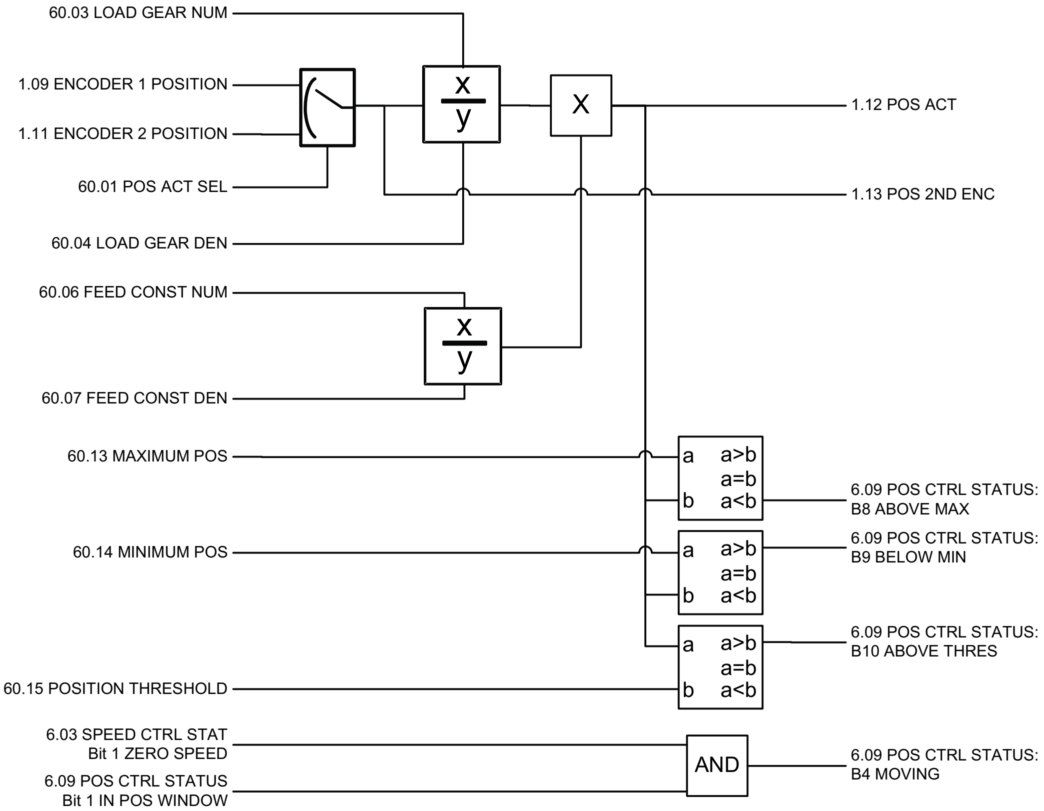
	(0) NO SYNC	No synchronisation.
	(1) D2DSYNC	If the drive is the master on a drive-to-drive link, it broadcasts a synchronisation signal to the follower(s). If the drive is a follower, it synchronises its firmware time levels to the signal received from the master.
	(2) FBSYNC	The drive synchronises its firmware time levels to a synchronisation signal received through a fieldbus adapter.
	(3) FBTOD2DSYNC	If the drive is the master on a drive-to-drive link, it synchronises its firmware time levels to a synchronisation signal received from a fieldbus adapter, and broadcasts the signal on the drive-to-drive link. If the drive is a follower, this setting has no effect.
57.10	KERNEL SYNC OFFS	FW block: D2D COMMUNICATION (see above)
	Defines an offset between the synchronisation signal received and the time levels of the drive. With a positive value, the drive time levels will lag behind the synchronisation signal; with a negative value, the drive time levels will lead.	
	-4999…5000 ms	Synchronisation offset.
57.11	REF 1 MSG TYPE	FW block: D2D COMMUNICATION (see above)
	By default, in drive-to-drive communication, the master broadcasts the drive-to-drive control word and references 1 and 2 to all followers. This parameter enables multicasting, i.e. sending the drive-to-drive control word and reference 1 to a certain drive or group of drives. The message can then be further relayed to another group of drives to form a multicast chain. In the master, as well as any submaster (i.e. follower relaying the message to other followers), the sources for the control word and reference 1 are selected by parameters 57.08 FOLLOWER CW SRC and 57.06 REF 1 SRC respectively. **Note:** Reference 2 is broadcast to all followers. For more information, see *Appendix B – Drive-to-drive link* on page 391.	
	(0) BROADCAST	The control word and reference 1 are sent by the master to all followers. If the master has this setting, the parameter has no effect in the followers.
	(1) REF1 MC GRPS	The drive-to-drive control word and reference 1 are only sent to the drives in the multicast group specified by parameter 57.13 NEXT REF1 MC GRP. This setting can also used in intermediate followers to form a multicast chain.
57.12	REF1 MC GROUP	FW block: D2D COMMUNICATION (see above)
	Selects the multicast group the drive belongs to. See parameter 57.11 REF 1 MSG TYPE.	
	0…62	Multicast group (0 = none).
57.13	NEXT REF1 MC GRP	FW block: D2D COMMUNICATION (see above)
	Specifies the next multicast group of drives the multicast message is relayed to. See parameter 57.11 REF 1 MSG TYPE. This parameter is effective only in the master or intermediate followers (i.e. followers relaying the message to other followers).	
	0…62	Next multicast group in message chain.

57.14	NR REF1 MC GRPS	FW block: D2D COMMUNICATION (see above)
	In the master drive, sets the total number of links (followers or groups of followers) in the multicast message chain. See parameter 57.11 REF 1 MSG TYPE. **Notes:** • This parameter has no effect if the drive is a follower. • The master counts as a member of the chain if acknowledgement from the last drive to the master is desired.	
	1…62	Total number of links in multicast message chain.
57.15	D2D COMM PORT	FW block: None
	Defines the hardware to which the drive-to-drive link is connected. In special cases (such as harsh operating conditions), the galvanic isolation provided by the RS-485 interface of the FMBA module may make for more robust communication than the standard drive-to-drive connection.	
	(0) ON-BOARD	Connector X5 on the JCU Control Unit is used.
	(1) SLOT 1	An FMBA module installed in JCU option slot 1 is used.
	(2) SLOT 2	An FMBA module installed in JCU option slot 2 is used.
	(3) SLOT 3	An FMBA module installed in JCU option slot 3 is used.

Group 60 POS FEEDBACK

Configuration of drive position feedback including

- feedback source
- load gear ratio
- axis type
- positioning unit
- scalings for fieldbus
- scaling between rotational and translational systems
- resolution of internal position calculation
- position limit and threshold values.



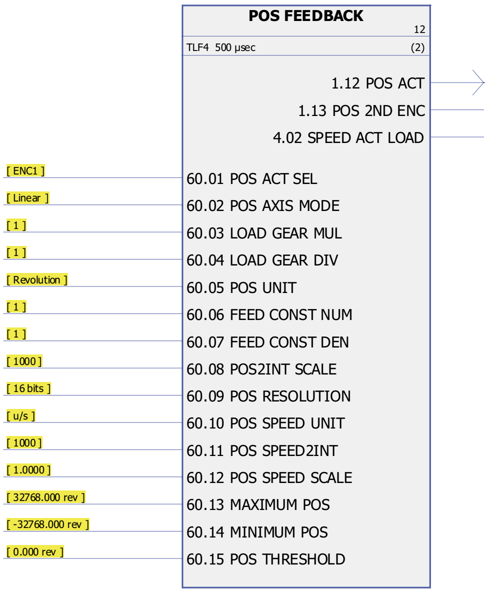
60 POS FEEDBACK

Firmware block:

POS FEEDBACK
(60)

This block

- selects the source for measured actual position value (encoder 1 or 2)
- selects whether positioning is executed along linear or rollover axis
- configures the load encoder gear function
- selects the unit and scaling for the position parameters
- selects the integer scaling of a position value
- defines how many bits are used for position count within one revolution
- defines the minimum and maximum position limits
- defines the position threshold supervision limit
- shows actual position of the encoder, scaled actual position of encoder 2 and filtered actual speed of the load.



Block outputs located in other parameter groups		1.12 POS ACT (page 78) 1.13 POS 2ND ENC (page 78) 4.02 SPEED ACT LOAD (page 89)
60.01	POS ACT SEL	FW block: POS FEEDBACK (see above)
	Selects the source for the actual position value.	
	(0) ENC1	Encoder 1. Inverted gear ratio is considered when the position control output (speed reference) is produced.
	(1) ENC2	Encoder 2. Inverted gear ratio is considered when the position control output (speed reference) is produced.
60.02	POS AXIS MODE	FW block: POS FEEDBACK (see above)
	Selects the positioning axis. **Note:** This parameter cannot be changed while the drive is running.	
	(0) LINEAR	Linear motion. Positioning is between minimum position 60.14 MINIMUM POS and maximum position 60.13 MAXIMUM POS.
	(1) ROLLOVER	Rotating motion. Positioning is between 0 and 1 revolutions, i.e. after 360°, the position calculation starts from 0° again.

60.03	LOAD GEAR MUL	FW block: POS FEEDBACK (see above)
	Defines the numerator for the load encoder gear function. See also section *Load encoder gear function* on page 52. $$\frac{\text{60.03 LOAD GEAR MUL}}{\text{60.04 LOAD GEAR DIV}} = \frac{\text{Load speed}}{\text{Encoder 1/2 speed}}$$ **Note:** When load encoder gear function is set, the gear function defined by parameters 71.07 GEAR RATIO MUL and 71.08 GEAR RATIO DIV must also be set.	
	$-2^{31}...2^{31} - 1$	Numerator for load encoder gear.
60.04	LOAD GEAR DIV	FW block: POS FEEDBACK (see above)
	Defines the denominator for the load encoder gear function. See parameter 60.03 LOAD GEAR MUL.	
	$1...2^{31} - 1$	Denominator for load encoder gear.
60.05	POS UNIT	FW block: POS FEEDBACK (see above)
	Selects the unit and scaling for the position parameters. The scaling factor is equal to one revolution. For positioning speed, acceleration and deceleration units, see parameter 60.10 POS SPEED UNIT. **Note:** If translatory (m, inch) unit is selected, the range also depends on parameter 60.06 FEED CONST NUM and 60.07 FEED CONST DEN settings.	
	(0) REVOLUTION	Unit: revolution. Scaling factor: 1.
	(1) DEGREE	Unit: degree. Scaling factor: 360.
	(2) METER	Unit: meter. Scaling factor: according to parameters 60.06 FEED CONST NUM and 60.07 FEED CONST DEN.
	(3) INCH	Unit: inch. Scaling factor: according to parameters 60.06 FEED CONST NUM and 60.07 FEED CONST DEN.
60.06	FEED CONST NUM	FW block: POS FEEDBACK (see above)
	Defines, together with parameter 60.07 FEED CONST DEN, the feed constant for the position calculation: $$\frac{\text{60.06 FEED CONST NUM}}{\text{60.07 FEED CONST DEN}}$$ The feed constant converts rotational motion into translatory motion. The feed constant is the distance the load moves during one turn of the motor shaft ($2\pi r$), when linear positioning has been selected with 60.05 POS UNIT (i.e. parameter is set to (2) METER or (3) INCH). **Note:** Parameters 60.05 POS UNIT, 60.06 FEED CONST NUM and 60.07 FEED CONST DEN also affect the positioning parameters. If the feed constant is changed, positioning references are re-calculated and the limits are changed. However, the internal motor shaft references remain unchanged.	
	$1... 2^{31} -1$	Feed constant numerator.
60.07	FEED CONST DEN	FW block: POS FEEDBACK (see above)
	Defines, together with parameter 60.06 FEED CONST NUM, the feed constant for the position calculation.	

	1… 2^{31} -1	Feed constant denominator.
60.08	POS2INT SCALE	FW block: POS FEEDBACK (see above)
	Scales position values to integer values. Integer values are used in the control program and fieldbus communication. For positioning speed, acceleration and deceleration value scaling, see parameter 60.11 POS SPEED2INT. Example: If parameter value is set to 100 and 60.05 POS UNIT is set to (2) METER, integer value of 3000 corresponds to position value of 30 m.	
	1/10/100/1000/10000/ 100000/1000000	Scaling factor.
60.09	POS RESOLUTION	FW block: POS FEEDBACK (see above)
	Defines how many bits are used for the position count within one revolution. Example: If parameter is set to a value of 24, 8 bits (32 - 24) are used for the whole revolution count and 24 bits are used for the fractional revolution count. **Note:** This parameter cannot be changed while the drive is running.	
	10…24	Number of bits used for position count.
60.10	POS SPEED UNIT	FW block: POS FEEDBACK (see above)
	Selects, together with parameter 60.05 POS UNIT (position unit), the unit for positioning speed, acceleration and deceleration values.	
	(0) U/S	Position unit/s (s = second). With acceleration/deceleration values: position unit/s^2.
	(1) U/MIN	Position unit/min (min = minute). With acceleration/deceleration values: position unit/min^2.
	(2) U/H	Position unit/h (h = hour). With acceleration/deceleration values: position unit/h^2.
60.11	POS SPEED2INT	FW block: POS FEEDBACK (see above)
	Scales all positioning speed, acceleration and deceleration values to an integer value. Integer values are used in the control program and fieldbus communication. Example: If parameter value is set to 10, an integer value of 10 corresponds to positioning speed value 1 rev/s.	
	1/10/100/1000/10000/ 100000/1000000	Scaling factor.
60.12	POS SPEED SCALE	FW block: POS FEEDBACK (see above)
	Defines an additional scaling for internal positioning speed, acceleration and deceleration values. Can be used e.g. to improve calculation accuracy at low and high speeds. Example: If parameter value is set to 0.1, internal speed value 1 rev/s is changed to value 10 rev/s.	
	0…32768	Additional scaling factor.

60.13	MAXIMUM POS	FW block: POS FEEDBACK (see above)
	Defines the maximum position value. If the actual position value exceeds the maximum position limit, fault message POSERR MAX is generated. The unit depends on parameter 60.05 POS UNIT selection.	
	0…32768	Maximum position value.
60.14	MINIMUM POS	FW block: POS FEEDBACK (see above)
	Defines the minimum position value. If the actual position value falls below the minimum position limit, fault message POSERR MIN is generated. The unit depends on parameter 60.05 POS UNIT selection.	
	-32768…0	Minimum position value.
60.15	POS THRESHOLD	FW block: POS FEEDBACK (see above)
	Defines the position threshold supervision limit. If actual position 1.12 POS ACT exceeds the defined limit, 6.09 POS CTRL STATUS bit 8 ABOVE MAX is activated. The unit depends on parameter 60.05 POS UNIT selection.	
	-32768…32768	Position threshold supervision limit.

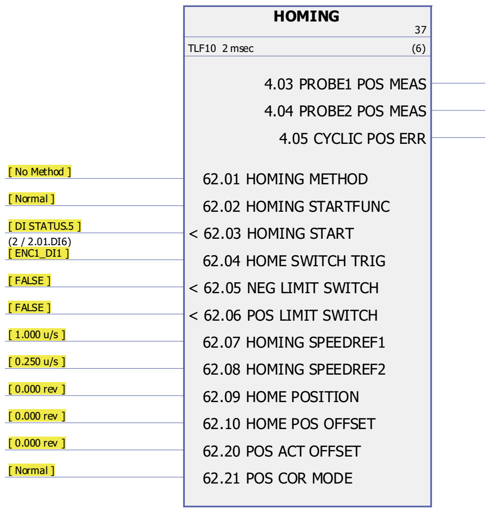
Group 62 POS CORRECTION

Settings for position correction functions (homing, presets, and cyclic corrections). With these functions, the user can define the relationship between the actual position of the drive positioning system and the driven machinery.

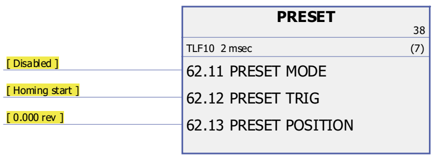
Some of the correction functions need an external probe or limit switch to be connected to the digital inputs of the drive control board or encoder interface module.

See also section *Position correction* on page 56.

Note: Only one position correction function can be active at a time. Homing has the highest priority, cyclic correction the lowest.

62 POS CORRECTION		
Firmware block: **HOMING** (62) This block • selects homing mode (1…35) • selects the homing start function (NORMAL/PULSE) and the source for the homing start command • selects the source for the home switch signal • selects the sources for the negative and positive limit switch signals • defines two homing speed reference values • defines the home position • shows measured position and calculated cyclic position error for the cyclic correction function (see the block CYCLIC CORRECTION on page 199).		
Block outputs located in other parameter groups		4.03 PROBE1 POS MEAS (page 89) 4.04 PROBE2 POS MEAS (page 89) 4.05 CYCLIC POS ERR (page 89)
62.01	HOMING METHOD	FW block: HOMING (see above)
	Selects the homing mode. For more information, see • section *Homing* on page 56 • chapter *Appendix C – Homing modes* on page 403 • *CiA Draft Standard Proposal 402: CANopen Device Profile Drives and Motion Control*.	
	0	None.
	1…35	Homing mode 1…35.

62.02	HOMING STARTFUNC	FW block: HOMING (see above)
	Selects the homing start function.	
	(0) NORMAL	Rising edge of a signal from the source defined by 62.03 HOMING START activates the homing. The input signal has to stay TRUE during the homing task.
	(1) PULSE	Rising edge of a pulse from the source defined by 62.03 HOMING START activates the homing.
62.03	HOMING START	FW block: HOMING (see above)
	Selects the source of the start command used in homing. 0 -> 1: Start. The start function is defined by parameter 62.02 HOMING STARTFUNC.	
	Bit pointer: Group, index and bit.	
62.04	HOME SWITCH TRIG	FW block: HOMING (see above)
	Selects the source for the home switch signal.	
	(0) ENC1_DI1	Encoder 1 digital input DI1.
	(1) ENC1_DI2	Encoder 1 digital input DI2.
	(2) ENC2_DI1	Encoder 2 digital input DI1.
	(3) ENC2_DI2	Encoder 2 digital input DI2.
62.05	NEG LIMIT SWITCH	FW block: HOMING (see above)
	Selects the source for the negative limit switch signal (i.e. external latch signal source for the minimum position). Used with homing modes 1, 11…14, 17 and 27…30. Homing mode is selected by parameter 62.01 HOMING METHOD.	
	Bit pointer: Group, index and bit.	
62.06	POS LIMIT SWITCH	FW block: HOMING (see above)
	Selects the source for the positive limit switch signal (i.e. external latch signal source for the maximum position). Used with homing modes 2, 7…10, 18 and 23…26. Homing mode is selected by parameter 62.01 HOMING METHOD.	
	Bit pointer: Group, index and bit.	
62.07	HOMING SPEEDREF1	FW block: HOMING (see above)
	Defines homing speed reference 1, i.e. the speed reference used when the homing is started (62.03 HOMING START). The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	0…32768	Homing speed reference 1.
62.08	HOMING SPEEDREF2	FW block: HOMING (see above)
	Defines homing speed reference 2. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	

	0…32768	Homing speed reference 2.
62.09	HOME POSITION	FW block: HOMING (see above)
	Defines the home position, which is set as the drive actual position after the home switch latch conditions have been fulfilled. The unit depends on parameter 60.05 POS UNIT selection.	
	-32768…32768	Home position.
62.10	HOME POS OFFSET	FW block: HOMING (see above)
	Defines a home position offset value. After reaching the home switch and latching the defined home position as actual position, the drive will rotate the number of runs specified by this parameter. In practice, the offset is required when the home switch cannot be placed at the physical home position. For example, if this parameter is set to a value of 50 and the home position to 0, the motor will run 50 revolutions in the forward direction after receiving a signal from the home switch. Negative values will make the motor run in the reverse direction.	
	-32768…32768	Home position offset.
Firmware block: **PRESET** (63) This block • selects the preset mode and source for the preset mode start signal • defines the preset position.		
62.11	PRESET MODE	FW block: PRESET (see above)
	Selects the preset mode. Presets are used to set the position system to a parameter value (preset position) or actual position. The physical position of the driven machinery is not changed, but the new position value is used as home position. **Note:** Selections 1…3 can also be activated by the homing start command (source selected by parameter 62.03 HOMING START).	
	(0) DISABLED	Preset mode not in use.
	(1) SYNCH REF	Synchron reference chain (parameter group 68 SYNC REF MOD) is set to the value of the preset position (62.13 PRESET POSITION).
	(2) ACT TO SYNCH	Synchron reference chain (parameter group 68) is set to the value of the actual position (1.12 POS ACT).
	(3) WHOLE SYSTEM	Position system (parameter groups 60, 66, 68, 70 and 71) is set to the value of the preset position (62.13 PRESET POSITION).
62.12	PRESET TRIG	FW block: PRESET (see above)
	Selects the source for the preset mode start signal.	
	(0) HOMING START	The homing start signal (selected by parameter 62.03 HOMING START) also activates the selected preset mode.
	(1) ENC1 DI1 _-	Rising edge of encoder 1 digital input DI1.

	(2) ENC1 DI1 -_	Falling edge of encoder 1 digital input DI1.
	(3) ENC1 DI2 _-	Rising edge of encoder 1 digital input DI2.
	(4) ENC1 DI2 -_	Falling edge of encoder 1 digital input DI2.
	(5)	Reserved.
	(6) ENC1 ZEROP	Rising edge of encoder 1 zero pulse.
	(7) ENC2 DI1 _-	Rising edge of encoder 2 digital input DI1.
	(8) ENC2 DI1 -_	Falling edge of encoder 2 digital input DI1.
	(9) ENC2 DI2 _-	Rising edge of encoder 2 digital input DI2.
	(10) ENC2 DI2 -_	Falling edge of encoder 2 digital input DI2.
	(11)	Reserved.
	(12) ENC2 ZEROP	Rising edge of encoder 2 zero pulse.
62.13	PRESET POSITION	FW block: PRESET (see above)
	Defines the preset position. The unit depends on parameter 60.05 POS UNIT selection.	
	-32768…32768	Preset position.

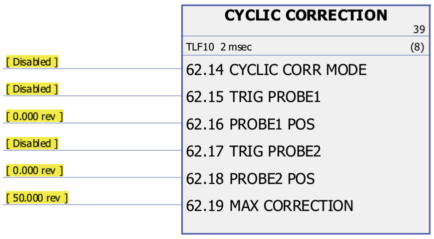
Firmware block:

CYCLIC CORRECTION
(64)

This block

- selects the cyclic correction mode
- defines the source for the latching command for position probe 1/2
- defines the reference position for probe 1/2
- defines the maximum absolute value for cyclic correction.

When the probe latching conditions are fulfilled, the encoder module saves the encoder position (to signal 4.03 PROBE1 POS MEAS or 4.04 PROBE2 POS MEAS).



62.14	CYCLIC CORR MODE	FW block: CYCLIC CORRECTION (see above)
	Selects the cyclic correction mode.	
	(0) DISABLED	No cyclic correction.
	(1) COR ACT POS	Actual position correction.
	(2) COR MAS REF	Master reference correction.
	(3) 1 PROBE DIST	Distance correction with one probe.

	(4) 2 PROBE DIST	Distance correction with two probes.
	(5) COR M/F DIST	Master/Follower distance correction.
62.15	TRIG PROBE1	FW block: CYCLIC CORRECTION (see above)
	Defines the source of the latching command for position probe 1.	
	(0) DISABLED	None.
	(1) ENC1 DI1 _-	Rising edge of encoder 1 digital input DI1.
	(2) ENC1 DI1 -_	Falling edge of encoder 1 digital input DI1.
	(3) ENC1 DI2 _-	Rising edge of encoder 1 digital input DI2.
	(4) ENC1 DI2 -_	Falling edge of encoder 1 digital input DI2.
	(5)	Reserved.
	(6) ENC1 ZEROP	Rising edge of encoder 1 Z-pulse.
	(7) ENC1 DI1_- Z	First rising edge of encoder 1 Z-pulse after the rising edge of encoder 1 digital input DI1.
	(8) ENC1 DI1-_ Z	First rising edge of encoder 1 Z-pulse after the falling edge of encoder 1 digital input DI1.
	(9) ENC1 DI1=1 Z	First rising edge of encoder 1 Z-pulse when encoder 1 digital input DI1 = 1.
	(10) ENC1 DI1=0 Z	First rising edge of encoder 1 Z-pulse when encoder 1 digital input DI1 = 0.
	(11) ENC1 DI2_- Z	First rising edge of encoder 1 Z-pulse after the rising edge of encoder 1 digital input DI2.
	(12) ENC1 DI2-_ Z	First rising edge of encoder 1 Z-pulse after the falling edge of encoder 1 digital input DI2.
	(13) ENC1 DI2=1 Z	First rising edge of encoder 1 Z-pulse when encoder 1 digital input DI2 = 1.
	(14) ENC1 DI2=0 Z	First rising edge of encoder 1 Z-pulse when encoder 1 digital input DI2 = 0.
	(15) ENC2 DI1 _-	Rising edge of encoder 2 digital input DI1.
	(16) ENC2 DI1 -_	Falling edge of encoder 2 digital input DI1.
	(17) ENC2 DI2 _-	Rising edge of encoder 2 digital input DI2.
	(18) ENC2 DI2 -_	Falling edge of encoder 2 digital input DI2.
	(19)	Reserved.
	(20) ENC2 ZEROP	Rising edge of encoder 2 Z-pulse.
	(21) ENC2 DI1_- Z	First rising edge of encoder 2 Z-pulse after the rising edge of encoder 2 digital input DI1.

	(22) ENC2 DI1-_ Z	First rising edge of encoder 2 Z-pulse after the falling edge of encoder 2 digital input DI1.
	(23) ENC2 DI1=1 Z	First rising edge of encoder 2 Z-pulse when encoder 2 digital input DI1 = 1.
	(24) ENC2 DI1=0 Z	First rising edge of encoder 2 Z-pulse when encoder 2 digital input DI1 = 0.
	(25) ENC2 DI2_- Z	First rising edge of encoder 2 Z-pulse after the rising edge of encoder 2 digital input DI2.
	(26) ENC2 DI2-_ Z	First rising edge of encoder 2 Z-pulse after the falling edge of encoder 2 digital input DI2.
	(27) ENC2 DI2=1 Z	First rising edge of encoder 2 Z-pulse when encoder 2 digital input DI2 = 1.
	(28) ENC2 DI2=0 Z	First rising edge of encoder 2 Z-pulse when encoder 2 digital input DI2 = 0.
62.16	PROBE1 POS	FW block: CYCLIC CORRECTION (see above)
	Defines the reference position for position probe 1. The unit depends on parameter 60.05 POS UNIT selection.	
	-32768…32768	Reference position for position probe 1.
62.17	TRIG PROBE2	FW block: CYCLIC CORRECTION (see above)
	Defines the source of the latching command for position probe 2. For selection, see parameter 62.15 TRIG PROBE1.	
62.18	PROBE2 POS	FW block: CYCLIC CORRECTION (see above)
	Defines the reference position for position reference probe 2. The unit depends on parameter 60.05 POS UNIT selection.	
	-32768…32768	Reference position for position probe 2.
62.19	MAX CORRECTION	FW block: CYCLIC CORRECTION (see above)
	Defines the maximum absolute value for cyclic correction. Example: If maximum value is set to 50 revolutions and the requested cyclic correction is 60 revolution, no correction is made. The unit depends on parameter 60.05 POS UNIT selection.	
	0…32768	Maximum absolute value for cyclic correction.

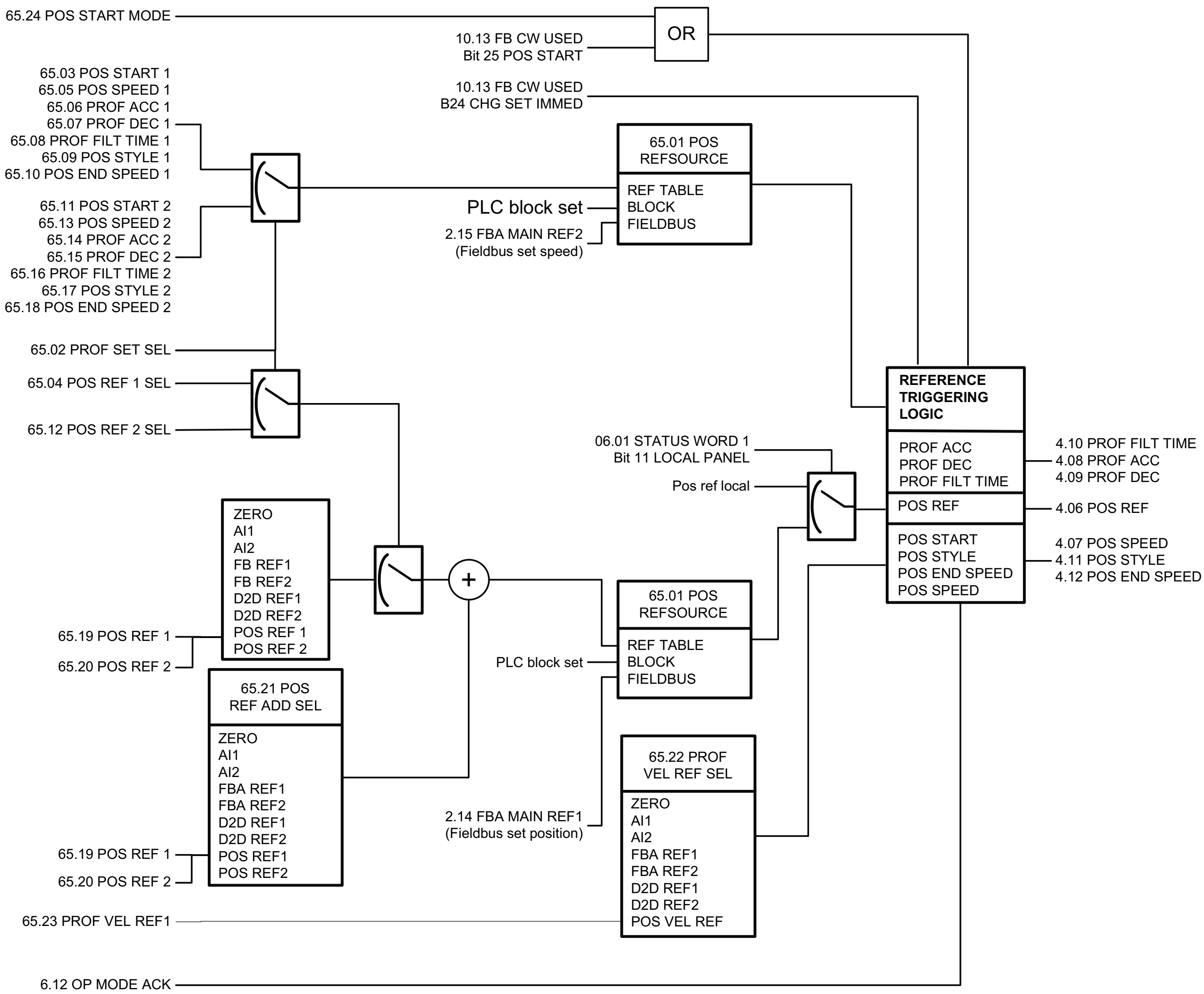
62.20	POS ACT OFFSET	FW block: HOMING (see above)
	Offsets all the position values used by the position system, effectively correcting the position and revolution count signal received from the encoder. For example, this parameter can be used if a non-zero position signal received from the encoder needs to be defined as the zero position for the application. For example, if this parameter is set to a value of -100, the absolute position of 100 revolutions as measured by the encoder is interpreted as the zero position. **Notes:** • The offset takes effect upon the next power-up or when an encoder reconfiguration command is given using parameter 90.10 ENC PAR REFRESH. • The offset will not be visible through any actual signal or other parameter.	
	-32768…32768	Offset for actual position value.
62.21	POS COR MODE	FW block: HOMING (see above)
	Determines if the position change made in homing or in preset mode 2 or 3 is forced permanently into the drive memory by saving it to parameter 62.20, or only until the next power-down.	
	(0) NORMAL	The position change made in homing or in preset mode 2 or 3 is effective only until the next power-down.
	(1) PERMANENT	The position change made in homing or in preset mode 2 or 3 remains permanently effective.

Group 65 PROFILE REFERENCE

Positioning profile and start command settings. The shape of the profile are defined by seven values: position reference, speed, acceleration, deceleration, filtering time, style, and end speed.

The position reference can be taken from an analogue input, fieldbus, drive-to-drive link or the position reference table. The positioning speed is taken from fieldbus or the reference table. The remaining values are taken from the reference table.

See also section *Position reference sets* on page 54.



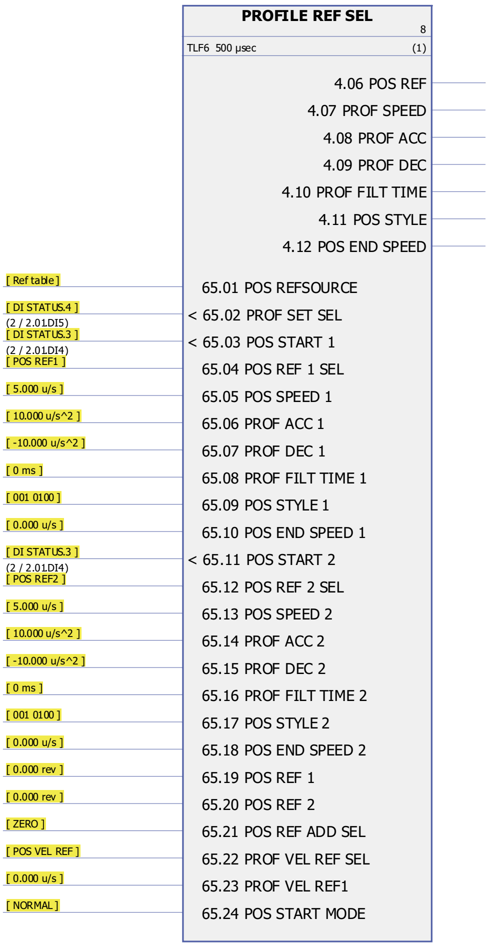
65 PROFILE REFERENCE

Firmware block:

PROFILE REF SEL
(65)

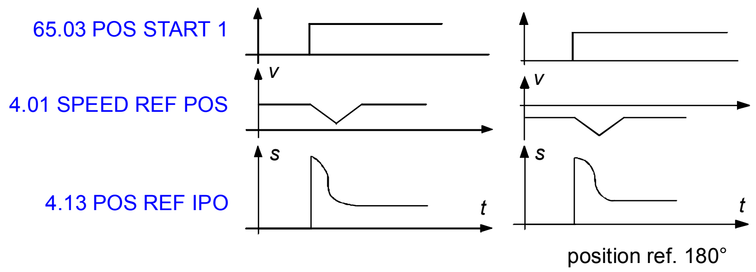
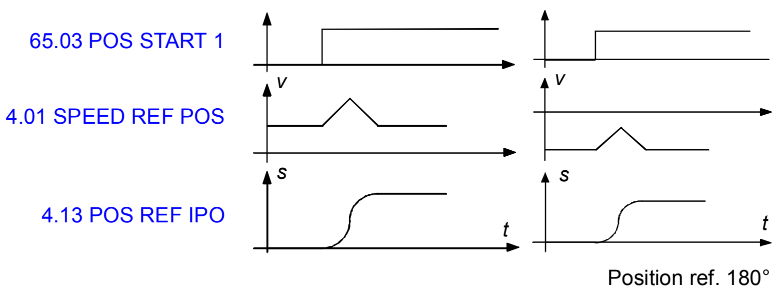
This block

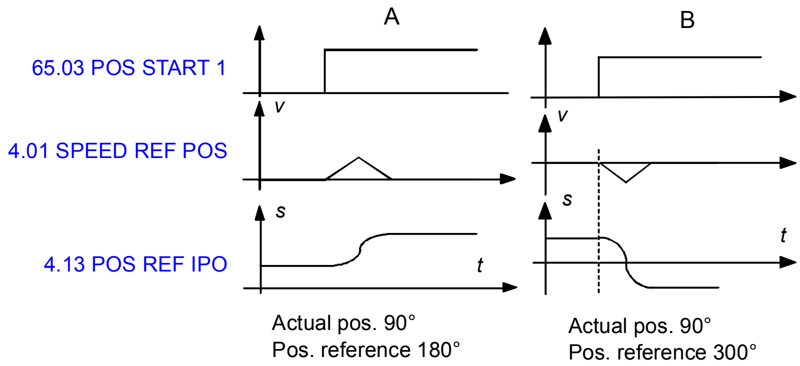
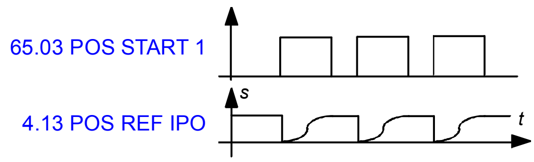
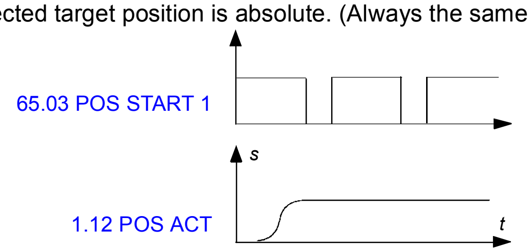
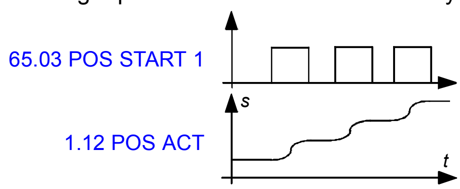
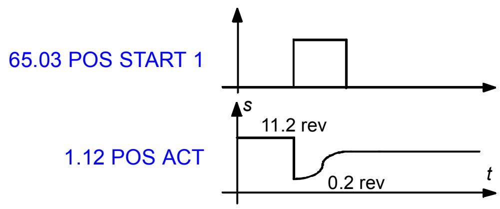
- selects whether position reference is defined with reference set 1/2 or received through fieldbus
- selects the source for position reference set 1/2 selection
- defines the position reference sets 1 and 2
- selects the source for an additional position reference
- selects the source for speed reference in profile velocity mode
- selects the positioning start function
- shows the used positioning values: reference, speed, acceleration, deceleration, filter time and positioning behaviour.



Block outputs located in other parameter groups		4.06 POS REF (page 89) 4.07 PROF SPEED (page 89) 4.08 PROF ACC (page 89) 4.09 PROF DEC (page 89) 4.10 PROF FILT TIME (page 89) 4.11 POS STYLE (page 89) 4.12 POS END SPEED (page 90)
65.01	POS REFSOURCE	FW block: PROFILE REF SEL (see above)
	Selects the source for the used positioning values.	
	(0) REF TABLE	Reference and other positioning parameters are read from reference set 1/2 which is defined by parameters 65.03…65.10 / 65.11…65.18.
	(1) BLOCK	Reserved.
	(2) FIELDBUS	Position reference and speed are read from fieldbus. Other positioning values are read from reference set 1 which is defined by parameters 65.03…65.10.

65.02	PROF SET SEL	FW block: PROFILE REF SEL (see above)
	Selects the source for position reference set 1 or 2 selection. 0 = position reference set 1, 1 = position reference set 2. See parameters 65.04 POS REF 1 SEL and 65.12 POS REF 2 SEL.	
	Bit pointer: Group, index and bit.	
65.03	POS START 1	FW block: PROFILE REF SEL (see above)
	Selects the source for the positioning start command when position reference set 1 used.	
	Bit pointer: Group, index and bit.	
65.04	POS REF 1 SEL	FW block: PROFILE REF SEL (see above)
	Selects the source for the positioning reference when position reference set 1 is used.	
	(0) ZERO	Zero position reference.
	(1) AI1	Analogue input 1.
	(2) AI2	Analogue input 2.
	(3) FBA REF1	Fieldbus reference 1.
	(4) FBA REF2	Fieldbus reference 2.
	(5) D2D REF1	Drive-to-drive reference 1.
	(6) D2D REF2	Drive-to-drive reference 2.
	(7) POS REF1	Position reference 1 defined by parameter 65.19 POS REF 1.
	(8) POS REF2	Position reference 2 defined by parameter 65.20 POS REF 2.
65.05	POS SPEED 1	FW block: PROFILE REF SEL (see above)
	Defines the positioning speed when position reference set 1 is used. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	0…32768	Positioning speed for position reference set 1.
65.06	PROF ACC 1	FW block: PROFILE REF SEL (see above)
	Defines the positioning acceleration when position reference set 1 is used. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	0…32768	Positioning acceleration for position reference set 1.
65.07	PROF DEC 1	FW block: PROFILE REF SEL (see above)
	Defines the positioning deceleration when position reference set 1 is used. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	-32768…0	Positioning deceleration for position reference set 1.
65.08	PROF FILT TIME 1	FW block: PROFILE REF SEL (see above)
	Defines the position reference filter time when position reference set 1 is used.	

	0…1000 ms	Position reference filter time for position reference set 1.
65.09	POS STYLE 1	FW block: PROFILE REF SEL (see above)
	Determines the behaviour of the position profile generator when position reference set 1 is used. The figures below display the behaviour of each bit (different bit combinations are also possible). In synchron applications, bits 0…2 determine in which way the drive moves to an additional position reference or corrects the synchronising. Only one of the bits 0…2 can be active at a time. The positioning priority order is: 1) bit 2 or according to the linear axis positioning selected by par. 60.02 POS AXIS MODE. 2) bit 0 3) bit 1. Conversion from binary to hexadecimal format examples: bit number 4 0 binary value 0001 0000 decimal value $2^4 = 32$ hex value 10h bit number 5 2 binary value 0010 0100 decimal value $2^5 + 2^2 = 32 + 4 = 36$ hex value 20 + 4 = 24h Bits 3…6 determine the path to the target position.	
	0b0000000…0b1111111	Positioning style for position reference set 1.
Bit 0	1 = Positioning direction depends on the direction of the synchronous (master) speed. 0 = Positioning direction is independent of the synchronous (master) speed.	
Bit 1	1 = Counter-clockwise positioning to the target position (bit 0 = 0).  or positioning in the opposite direction to the synchronous (master) speed when bit 0 = 1. 0 = Clockwise positioning to the target position (bit 0 = 0).  or positioning in the direction of the synchronous (master) speed when bit 0 = 1.	

Bit 2	1 = Positioning to the target position along the shortest path, regardless of bit 0 and 1 values.  A = Shortest path from 90° -> 180°: 90° + 90° = 180° B = Shortest path from 90° -> 300°: 90° - 150° = 300° 0 = Positioning to the target position according to bits 0 and 1.
Bit 3	1 = Before the positioning is started, the position system is reset.  0 = The position system is not reset.
Bit 4	1 = Selected target position is absolute. (Always the same position reference).  0 = Selected target position is relative as defined by bit 6. 
Bit 5	1 = Before the positioning is started, the position system is returned to the rollover axis range, i.e. between 0…1 revolutions.  0 = The position system is not returned into the rollover axis range.

Bit 6	Effective only when bit 4 = 0. 1 = Selected target position is relative to the actual position. 0 = Selected target position is relative to the previous target position.	
65.10	POS END SPEED 1	FW block: PROFILE REF SEL (see above)
	Defines the positioning speed when target is reached when position reference set 1 is used. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	-32768…32768	Positioning speed when target is reached for position reference set 1.
65.11	POS START 2	FW block: PROFILE REF SEL (see above)
	Selects the source for the positioning start command when position reference set 2 is used.	
	Bit pointer: Group, index and bit.	
65.12	POS REF 2 SEL	FW block: PROFILE REF SEL (see above)
	Selects the source for the positioning reference when position reference set 2 is used. See 65.04 POS REF 1 SEL.	
65.13	POS SPEED 2	FW block: PROFILE REF SEL (see above)
	Defines the positioning speed when position reference set 2 is used. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	0…32768	Positioning speed for position reference set 2.
65.14	PROF ACC 2	FW block: PROFILE REF SEL (see above)
	Defines the positioning acceleration when position reference set 2 is used. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	0…32768	Positioning acceleration for position reference set 2.
65.15	PROF DEC 2	FW block: PROFILE REF SEL (see above)
	Defines the positioning deceleration when position reference set 2 is used. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	-32768…0	Positioning deceleration for position reference set 2.
65.16	PROF FILT TIME 2	FW block: PROFILE REF SEL (see above)
	Defines the position reference filter time when position reference set 2 is used.	
	0…1000 ms	Position reference filter time for position reference set 2.
65.17	POS STYLE 2	FW block: PROFILE REF SEL (see above)
	Determines the behaviour of the position profile generator when position reference set 2 is used. See parameter 65.09 POS STYLE 1.	
	0b0000000…0b1111111	Positioning style for position reference set 2.

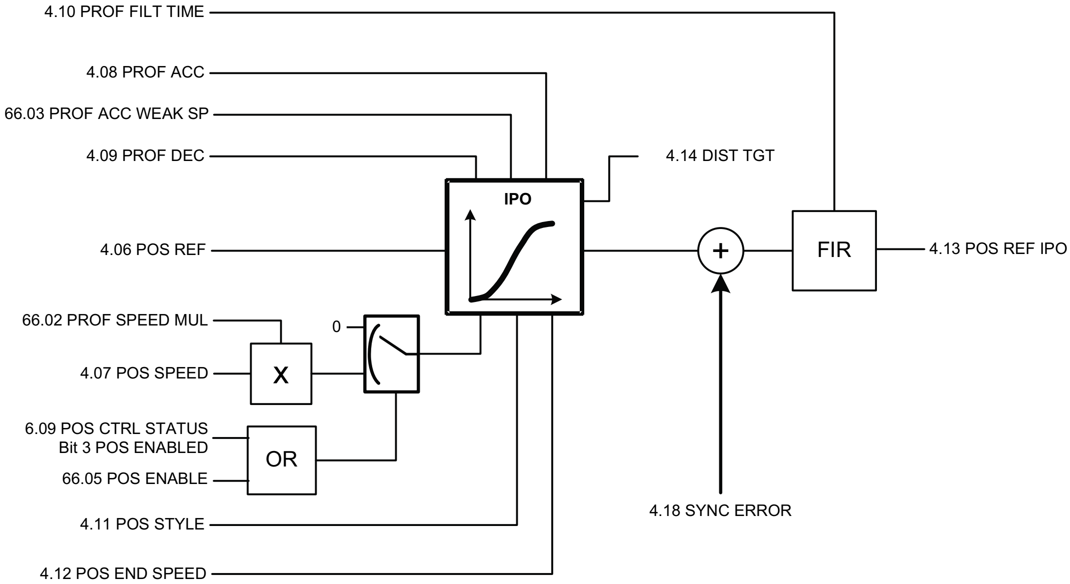
65.18	POS END SPEED 2	FW block: PROFILE REF SEL (see above)
	Defines the positioning speed when target is reached when position reference set 1 is used. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	-32768…32768	Positioning speed when target is reached for position reference set 2.
65.19	POS REF 1	FW block: PROFILE REF SEL (see above)
	Defines positioning reference 1. Used when parameter 65.04 POS REF 1 SEL / 65.12 POS REF 2 SEL / 65.21 POS REF ADD SEL is set to (7) POS REF1. The unit depends on parameter 60.05 POS UNIT selection.	
	-32760…32760	Positioning reference 1.
65.20	POS REF 2	FW block: PROFILE REF SEL (see above)
	Defines positioning reference 2. Used when parameter 65.04 POS REF 1 SEL / 65.12 POS REF 2 SEL / 65.21 POS REF ADD SEL is set to (8) POS REF2. The unit depends on parameter 60.05 POS UNIT selection.	
	-32760…32760	Positioning reference 2.
65.21	POS REF ADD SEL	FW block: PROFILE REF SEL (see above)
	Selects the source for an additional position reference. The value is added to position reference 1 or 2 (source selected by 65.04 POS REF 1 SEL or 65.12 POS REF 2 SEL) when the positioning is started.	
	(0) ZERO	Zero additional position reference.
	(1) AI1	Analogue input 1.
	(2) AI2	Analogue input 2.
	(3) FBA REF1	Fieldbus reference 1.
	(4) FBA REF2	Fieldbus reference 2.
	(5) D2D REF1	Drive-to-drive reference 1.
	(6) D2D REF2	Drive-to-drive reference 2.
	(7) POS REF1	Position reference 1 defined by parameter 65.19 POS REF 1.
	(8) POS REF2	Position reference 2 defined by parameter 65.20 POS REF 2.
65.22	PROF VEL REF SEL	FW block: PROFILE REF SEL (see above)
	Selects the source for the speed reference in profile velocity mode.	
	(0) ZERO	Zero reference.
	(1) AI1	Analogue input 1.
	(2) AI2	Analogue input 2.
	(3) FBA REF1	Fieldbus reference 1.
	(4) FBA REF2	Fieldbus reference 2.

	(5) D2D REF1	Drive-to-drive reference 1.
	(6) D2D REF2	Drive-to-drive reference 2.
	(7) POS VEL REF	Profile velocity reference 1 defined by parameter 65.23 PROF VEL REF1.
65.23	PROF VEL REF1	FW block: PROFILE REF SEL (see above)
	Defines profile velocity reference 1. Used when parameter 65.22 PROF VEL REF SEL is set to (7) POS VEL REF. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	-32768…32768	Profile velocity reference 1.
65.24	POS START MODE	FW block: PROFILE REF SEL (see above)
	Selects the positioning start function.	
	(0) NORMAL	Rising edge of a signal from the source defined by parameter 65.03 POS START 1 / 65.11 POS START 2 activates the positioning. The input signal has to stay TRUE during the homing task.
	(1) PULSE	Rising edge of a pulse from the source defined by parameter 65.03 POS START 1 / 65.11 POS START 2 activates the positioning.

Group 66 PROFILE GENERATOR

Position profile generator settings. With these settings, the user can change the positioning speed during positioning, define positioning speed limits (for example, because of limited power), and set the window for target position.

See also section *Position profile generator* on page 53.



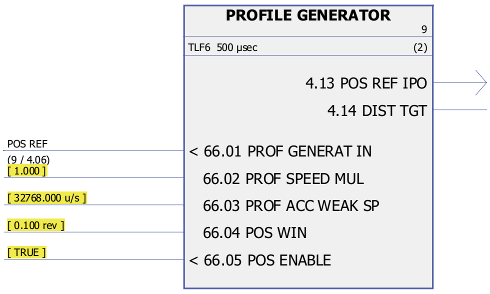
66 PROFILE GENERATOR

Firmware block:

PROFILE GENERATOR
(66)

This block

- selects the source for position profile generator input position reference
- defines the online positioning speed multiplier
- defines a positioning speed value above which the acceleration/ deceleration time is reduced, i.e. defines the power limit used in position reference calculation
- configures positioning window supervision
- selects the source for enabling the position reference generator and calculation of position reference
- shows the position reference from the position profile generator and position profile generator distance to target.

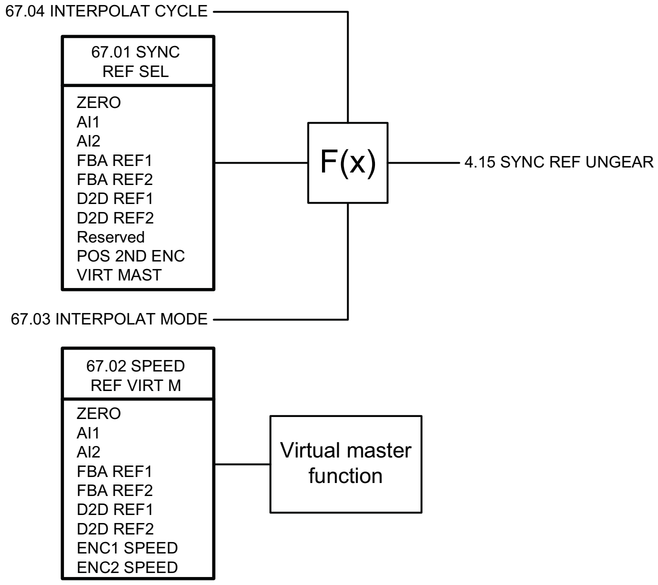


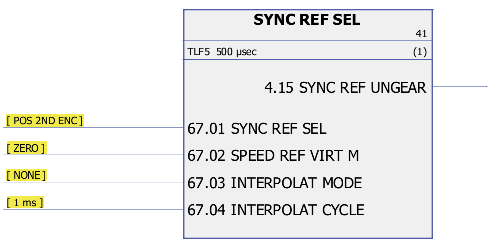
Block outputs located in other parameter groups		4.13 POS REF IPO (page 90) 4.14 DIST TGT (page 90)
66.01	PROF GENERAT IN	FW block: PROFILE GENERATOR (see above)
	Selects the source for the position profile generator input position reference. The default value is P.4.6, i.e. signal 4.06 POS REF (also an output of the PROFILE REF SEL firmware block; see page 204). **Note:** This parameter has been locked, i.e. no user setting is possible.	
	Value pointer: Group and index	
66.02	PROF SPEED MUL	FW block: PROFILE GENERATOR (see above)
	Defines the online positioning speed multiplier. The speed is multiplied with the selected value.	
	0…1	Online positioning speed multiplier.

66.03	PROF ACC WEAK SP	FW block: PROFILE GENERATOR (see above)
	Defines a positioning speed value (for the profile generator), above which the acceleration/ deceleration time is reduced. Because the drive power depends on the torque and angular velocity, this parameter defines the power limit used in the position reference calculation. P = T × ω and T = J × dω/dt, where T = torque ω = angular speed J = Inertia dω/dt = angular acceleration I.e. when the angular velocity exceeds the defined speed value, the power is limited by reducing the angular acceleration(/deceleration). The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	0…32768	Acceleration/deceleration time breakpoint.
66.04	POS WIN	FW block: PROFILE GENERATOR (see above)
	Defines the absolute value for the positioning window supervision. When the final position is within the limits defined by this parameter, the positioning is completed. Parameter value must be smaller than the value set by parameter 71.06 POS ERR LIM. The unit depends on parameter 60.05 POS UNIT selection.	
	0…32768	Absolute value for positioning window supervision.
66.05	POS ENABLE	FW block: PROFILE GENERATOR (see above)
	Selects the source for enabling the position reference generator and the calculation of the position reference. 1 = Enable / Continue position reference calculation. 0 = Disable. Position reference calculation is stopped. Generator output speed is decreased to zero along the position deceleration ramp.	
	Bit pointer: Group, index and bit.	

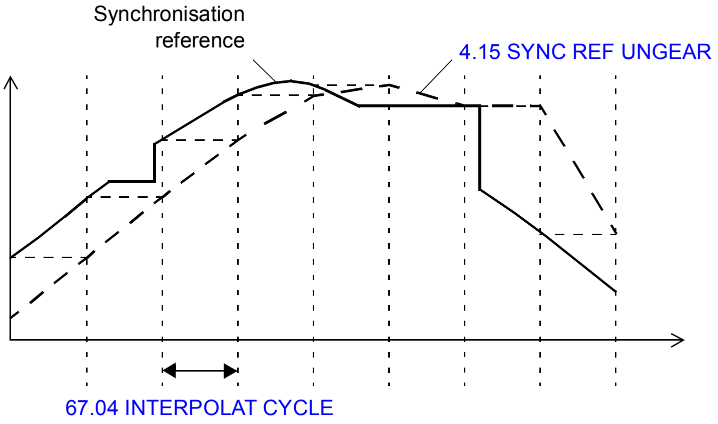
Group 67 SYNC REF SEL

Synchronisation reference source selection that is used in synchron control mode. Synchron reference can be smoothed with fine interpolation if the reference is updated too slowly or changes drastically because of missing data. If the reference is taken from the virtual master, a rotating position reference is calculated according to the configured virtual master speed.



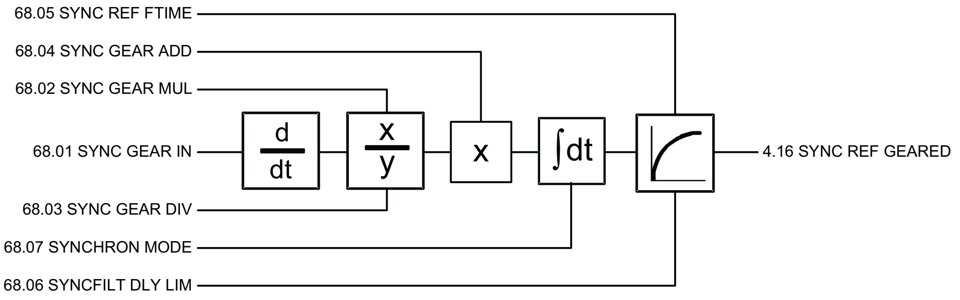
67 SYNC REF SEL		
Firmware block: **SYNC REF SEL** (67) This block • selects the source for the position reference in synchron control • shows the ungeared synchron reference input.		
Block outputs located in other parameter groups		4.15 SYNC REF UNGEAR (page 90)
67.01	SYNC REF SEL	FW block: SYNC REF SEL (see above)
	Selects the source for the position reference in synchron control.	
	(0) ZERO	Zero position reference.
	(1) AI1	Analogue input 1.
	(2) AI2	Analogue input 2.
	(3) FBA REF1	Fieldbus reference 1.

	(4) FBA REF2	Fieldbus reference 2.
	(5) D2D REF1	Drive-to-drive reference 1.
	(6) D2D REF2	Drive-to-drive reference 2.
	(7)	Reserved.
	(8) POS 2ND ENC	Encoder 2.
	(9) VIRT MAST	Virtual master reference.
67.02	SPEED REF VIRT M	FW block: SYNC REF SEL (see above)
	Selects the source for the virtual master speed reference.	
	(0) ZERO	Zero position reference.
	(1) AI1	Analogue input 1.
	(2) AI2	Analogue input 2.
	(3) FBA REF1	Fieldbus reference 1.
	(4) FBA REF2	Fieldbus reference 2.
	(5) D2D REF1	Drive-to-drive reference 1.
	(6) D2D REF2	Drive-to-drive reference 2.
	(7) ENC1 SPEED	Encoder 1.
	(8) ENC2 SPEED	Encoder 2.
67.03	INTERPOLAT MODE	FW block: SYNC REF SEL (see above)
	Selects whether the synchronisation reference selected by parameter 67.01 SYNC REF SEL is interpolated or not. This function can be used to smooth out short breaks in the reference.	
	(0) NONE	Interpolation is not used. The synchronisation reference is reflected directly by actual signal 4.15 SYNC REF UNGEAR.

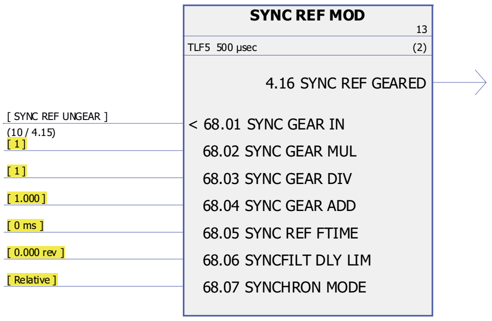
	(1) INTERPOLATE	The synchronisation reference is interpolated as shown in the diagram below. The synchronisation reference is sampled at intervals defined by parameter 67.04 INTERPOLAT CYCLE. Signal 4.15 SYNC REF UNGEAR is updated to the sampled reference value after one cycle. 
67.04	INTERPOLAT CYCLE	FW block: SYNC REF SEL (see above)
	Interpolation cycle. See parameter 67.03 INTERPOLAT MODE.	
	1…10000 ms	Interpolation cycle.

Group 68 SYNC REF MOD

Synchronisation reference modification settings that are used to select between absolute or relative synchronisation, to set an electrical gear ratio between the synchronisation reference and the drive positioning system, and to filter the reference.



<table>
<tr><th colspan="3">68 SYNC REF MOD</th></tr>
<tr><td colspan="2">Firmware block:
SYNC REF MOD
(68)

This block
• selects the source for the synchron reference chain
• defines the gear ratio and selects a scaling factor for the ratio (in synchron control the position reference is first multiplied with the defined gear ratio and then with the defined gear scaling factor)
• defines the synchron speed reference filter time
• defines the maximum position difference between the unfiltered and filtered synchron speed reference
• selects the synchronisation of the follower drive in synchron mode
• shows the position reference in synchron control mode.</td><td>
</td></tr>
<tr><td colspan="2">Block outputs located in other parameter groups</td><td>4.16 SYNC REF GEARED (page 90)</td></tr>
<tr><td>68.01</td><td>SYNC GEAR IN</td><td>FW block: SYNC REF MOD (see above)</td></tr>
<tr><td></td><td colspan="2">Selects the source for the synchron reference chain. The default value is P.4.15, i.e. signal 4.15 SYNC REF UNGEAR, which is the output of the SYNC REF SEL firmware block (on page 214).</td></tr>
<tr><td></td><td colspan="2">Value pointer: Group and index</td></tr>
</table>

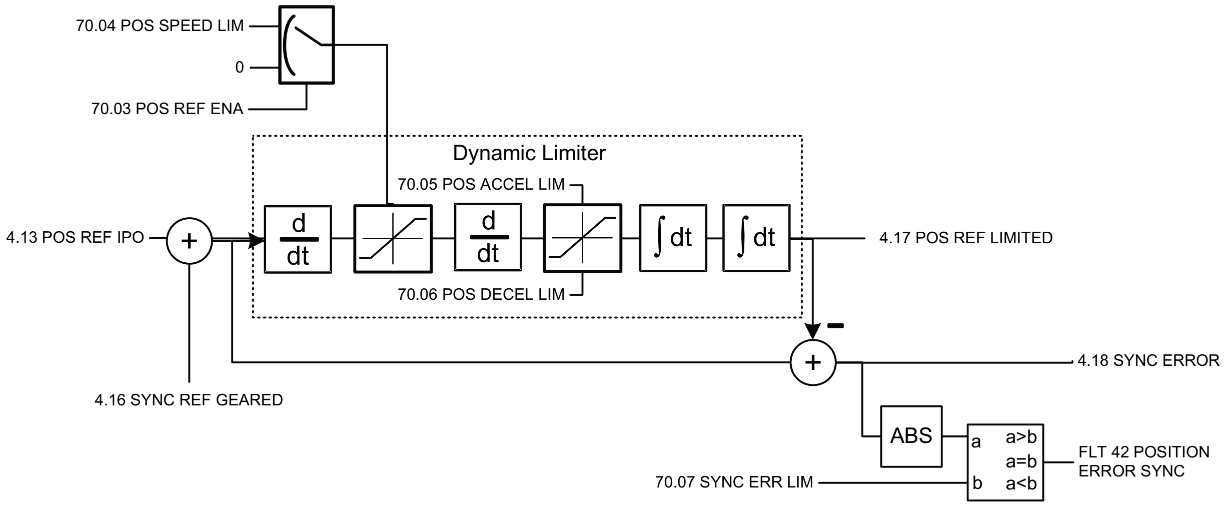
68.02	SYNC GEAR MUL	FW block: SYNC REF MOD (see above)
	Defines the numerator for the synchron gear function. The gear function modifies the position alterations of the synchron position reference value in order to obtain a certain ratio between the master and follower motion. See also parameter 68.03 SYNC GEAR DIV. $\frac{\text{68.02 SYNC GEAR MUL}}{\text{68.03 SYNC GEAR DIV}} = \frac{\text{Follower speed}}{\text{Master speed}}$ Example: Parameter 68.02 SYNC GEAR MUL is set to the value of 253 and parameter 68.03 SYNC GEAR DIV is set to the value of 100. Gear ratio is 2.53, i.e. follower speed is 2.53 times the master speed.	
	$-2^{31}...2^{31}-1$	Numerator for synchron gear function.
68.03	SYNC GEAR DIV	FW block: SYNC REF MOD (see above)
	Defines the denominator for the synchron gear function. See parameter 68.02 SYNC GEAR MUL.	
	$1...2^{31}-1$	Denominator for synchron gear function.
68.04	SYNC GEAR ADD	FW block: SYNC REF MOD (see above)
	Selects the scaling factor for the gear ratio (defined by parameters 68.02 SYNC GEAR MUL and 68.03 SYNC GEAR DIV) during operation. The gear ratio is multiplied with the selected value.	
	-30…30	Scaling factor for gear ratio.
68.05	SYNC REF FTIME	FW block: SYNC REF MOD (see above)
	Defines the synchron speed reference filter time. The filter filters synchron reference disturbances caused by encoder pulse changes. This parameter is used together with parameter 68.06 SYNCFILT DLY LIM to minimise synchron speed reference disturbances. Adjust parameter 68.06 SYNCFILT DLY LIM to maintain dynamic operation during fast reference changes.	
	0…1000 ms	Synchron speed reference filter time.
68.06	SYNCFILT DLY LIM	FW block: SYNC REF MOD (see above)
	Defines the maximum position difference between the unfiltered and filtered synchron speed reference. If the maximum difference is exceeded, the filter output is forced to follow the filter input. This parameter is used together with parameter 68.05 SYNC REF FTIME to minimise synchron speed reference disturbances. The unit depends on parameter 60.05 POS UNIT selection.	
	0…0.4	Maximum difference between unfiltered and filtered synchron speed references.
68.07	SYNCHRON MODE	FW block: SYNC REF MOD (see above)
	Selects the synchronisation of the follower drive in synchron mode.	
	(0) ABSOLUTE	Absolute synchronisation of the follower. The follower follows the master position after the start.
	(1) RELATIVE	Relative synchronisation of the follower. Only master position changes which take place after the follower is started are taken into account.

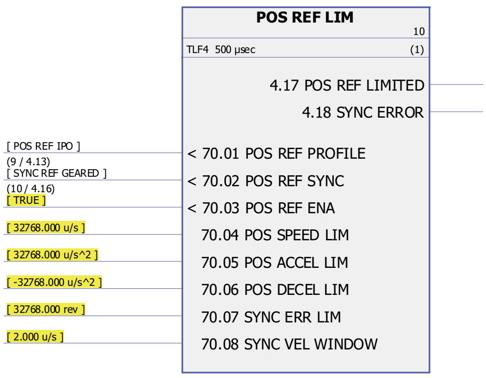
Group 70 POS REF LIMIT

Position reference (dynamic) limiter and synchronisation error supervision settings.

The limiter adds the changes from the profile reference generator and synchron reference. The limiter monitors speed, acceleration and deceleration changes in the positioning reference. The limits should be set according to the mechanical limits of the driven machinery.

See also section *Dynamic position reference limiter* on page 55.



70 POS REF LIMIT	
Firmware block: **POS REF LIM** (70) This block • selects the source for the dynamic limiter input • selects the source for the position reference enable command • selects the positioning speed, acceleration rate and deceleration limits • defines the synchron error supervision window • shows the limited position reference and the synchronising error caused by the dynamic limitations or position correction.	
Block outputs located in other parameter groups	4.17 POS REF LIMITED (page 90) 4.18 SYNC ERROR (page 90)

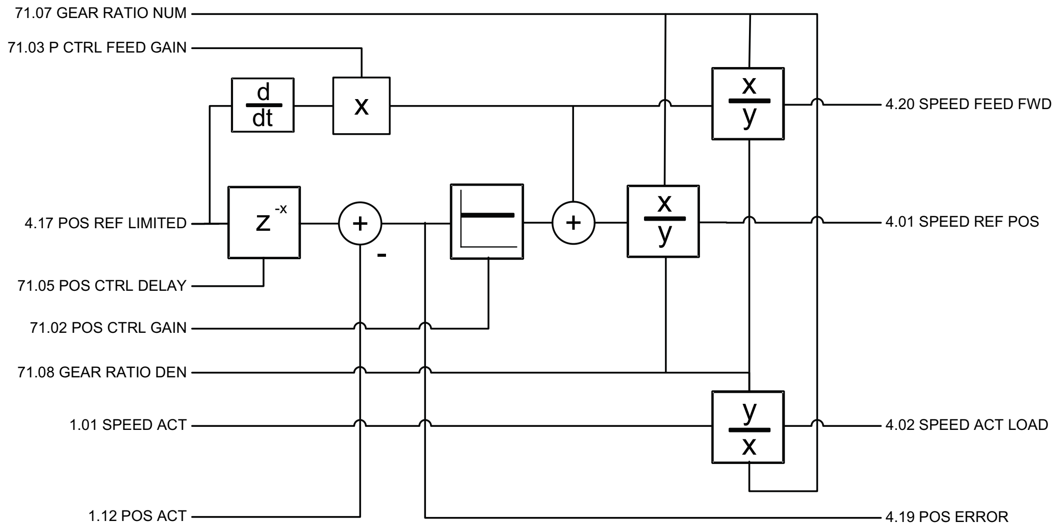
70.01	POS REF PROFILE	FW block: POS REF LIM (see above)
	Selects the source for the position reference for the dynamic limiter. Default value is P.4.13, i.e. 4.13 POS REF IPO, which is an output of the PROFILE GENERATOR firmware block (see page 212).	
	Value pointer: Group and index.	
70.02	POS REF SYNC	FW block: POS REF LIM (see above)
	Selects the source for the position reference for the dynamic limiter (added to 70.01 POS REF PROFILE). Default value is P.4.16, i.e. 4.16 SYNC REF GEARED, which is the output of the SYNC REF MOD firmware block (see page 217).	
	Value pointer: Group and index.	
70.03	POS REF ENA	FW block: POS REF LIM (see above)
	Selects the source for the position reference enable command. 1 = Enabled. 0 = Disabled, position reference speed limit is set to zero.	
	Bit pointer: Group, index and bit	
70.04	POS SPEED LIM	FW block: POS REF LIM (see above)
	Limits the positioning reference speed. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	0…32768	Position reference speed limit.
70.05	POS ACCEL LIM	FW block: POS REF LIM (see above)
	Limits the positioning acceleration rate. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	0…32768	Positioning acceleration rate limit.
70.06	POS DECEL LIM	FW block: POS REF LIM (see above)
	Limits the positioning deceleration rate. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	-32768…0	Positioning deceleration rate limit.
70.07	SYNC ERR LIM	FW block: POS REF LIM (see above)
	Defines the absolute value for the synchron error supervision window. The unit depends on parameter 60.05 POS UNIT selection.	
	0…32768	Absolute value for synchron error supervision window.
70.08	SYNC VEL WINDOW	FW block: POS REF LIM (see above)
	Defines the absolute value for a synchronous velocity supervision window. If the difference between synchronous speed and drive load speed is within the window, the limit bit 2 (IN SYNC) is set in actual signal 6.10 POS CTRL STATUS2. The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.	
	0…32768	Absolute value for synchronous velocity supervision window.

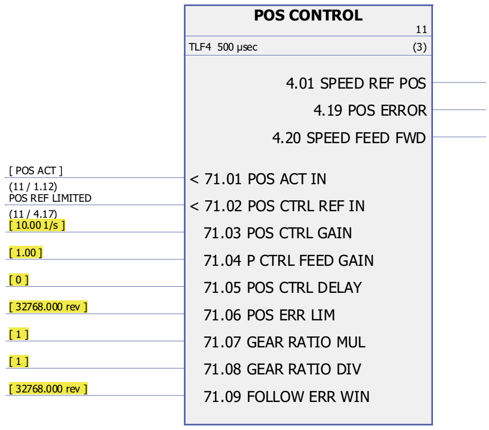
Group 71 POSITION CTRL

Settings for the position controller.

The position controller calculates a speed reference that is used to minimise the difference between position reference and actual values. The user can set the controller gain, the feed forward value and a cyclical delay between the reference and the actual value. The output of the position controller has a gear for transferring position and speed data from the load side to the motor side.

The position controller also supervises the error between the reference position and actual position in position and synchron control modes. The drive trips on a POSITION ERROR fault if the limit (71.06 POS ERR LIM) is exceeded.



71 POSITION CTRL		
Firmware block: **POS CONTROL** (71) This block • selects the sources for the actual and reference position inputs of the position controller • defines the position control loop gain and the speed feed forward gain • defines a delay for the position reference • configures position error supervision • shows the speed reference, position error and position speed reference multiplied by the speed feed forward gain.		
Block outputs located in other parameter groups		4.01 SPEED REF POS (page 89) 4.19 POS ERROR (page 90) 4.20 SPEED FEED FWD (page 90)
71.01	POS ACT IN	FW block: POS CONTROL (see above)
	Selects the source for the actual position input of the position controller. The default value is P.1.12, i.e. signal 1.12 POS ACT, which is an output of the POS FEEDBACK firmware block (see page 192).	
	Value pointer: Group and index.	
71.02	POS CTRL REF IN	FW block: POS CONTROL (see above)
	Selects the source for the position reference input of the position controller. The default value is P.4.17, i.e. signal 4.17 POS REF LIMITED, which is the output of the POS REF LIM firmware block (see page 219). **Note:** This parameter has been locked, i.e. no user setting is possible.	
	Value pointer: Group and index.	
71.03	POS CTRL GAIN	FW block: POS CONTROL (see above)
	Defines the gain for the position control loop. A value of 1 produces a 1 rev/s speed reference when the position difference between the reference and actual position is 1 revolution.	
	0…10000 1/s	Gain for position control loop.
71.04	P CTRL FEED GAIN	FW block: POS CONTROL (see above)
	Defines the speed feed forward gain. The default gain value is suitable for most applications. In some cases the gain can be used to compensate the difference between the reference position and actual position caused by external disturbances.	
	0…10	Speed feed forward gain.

71.05	POS CTRL DELAY	FW block: POS CONTROL (see above)
	Defines the delay for the position reference. The selected number corresponds to the number of the position control cycles: If parameter value is set to 1, the position reference used in the position error calculation is the reference value updated during the previous position control cycle.	
	0…15	Delay for position reference.
71.06	POS ERR LIM	FW block: POS CONTROL (see above)
	Defines the absolute value for the position error supervision window. The drive trips on fault POSERR if the position error is exceeded. The supervision is active when position feedback is available. The unit depends on parameter 60.05 POS UNIT selection. If parameter value is set to zero, the supervision is disabled.	
	0…32768	Absolute value for position error supervision window.
71.07	GEAR RATIO MUL	FW block: POS CONTROL (see above)
	Defines the numerator for the gear function between the position control (load side) and speed control (motor side). The gear function is formed from the motor gear function and inverted load gear function. The gear function is applied to the position controller output (speed reference). $\frac{\text{71.07 GEAR RATIO MUL}}{\text{71.08 GEAR RATIO DIV}} = \frac{\text{Motor speed}}{\text{Load speed}}$ **Note:** When motor or load gear functions are set, the gear function must also be set.	
	$-2^{31}...2^{31}-1$	Numerator for gear function.
71.08	GEAR RATIO DIV	FW block: POS CONTROL (see above)
	Defines the denominator for the gear function between the position control (load side) and speed control (motor side). See parameter 71.07 GEAR RATIO MUL.	
	$1...2^{31}-1$	Denominator for gear function.
71.09	FOLLOW ERR WIN	FW block: POS CONTROL (see above)
	Defines the position window for the following error supervision. The error is defined as the difference between the reference and actual position. If the error is outside the defined window, 6.09 POS CTRL STATUS bit 7 FOLLOW ERR is set to 1 (also 2.13 FBA MAIN SW bit 18 FOLLOWING ERROR is set to 1). The supervision is active when position feedback is available. The unit depends on parameter 60.05 POS UNIT selection.	
	0…32768	Position window for error supervision.

Group 90 ENC MODULE SEL

Settings for encoder activation, emulation, TTL echo, and communication fault detection.

The firmware supports two encoders (or resolvers), encoder 1 and 2. Multiturn encoders are supported only as encoder 1. The following optional interface modules are available:

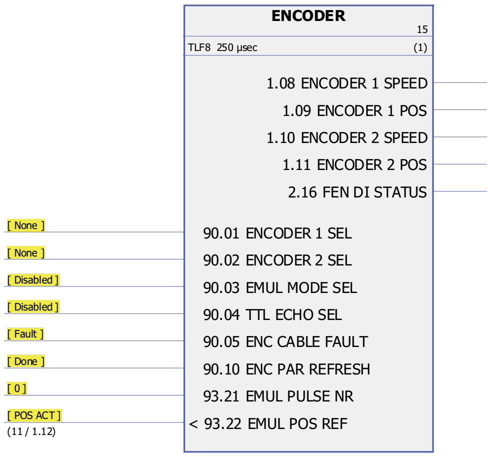
- TTL Encoder Interface Module FEN-01: two TTL inputs, TTL output (for encoder emulation and echo) and two digital inputs for position latching
- Absolute Encoder Interface FEN-11: absolute encoder input, TTL input, TTL output (for encoder emulation and echo) and two digital inputs for position latching
- Resolver Interface Module FEN-21: resolver input, TTL input, TTL output (for encoder emulation echo) and two digital inputs for position latching
- HTL Encoder Interface Module FEN-31: HTL encoder input, TTL output (for encoder emulation and echo) and two digital inputs for position latching.

The interface module is connected to drive option Slot 1 or 2. **Note:** Two encoder interface modules of the same type are not allowed.

For encoder/resolver configuration, see parameter groups 91 ABSOL ENC CONF (page 229), 92 RESOLVER CONF (page 234) and 93 PULSE ENC CONF (page 235).

Note: Configuration data is written into the logic registers of the interface module once after the power-up. If parameter values are changed, save values into the permanent memory using parameter 16.07 PARAM SAVE. The new settings will take effect when the drive is powered up again, or after re-configuration is forced using parameter 90.10 ENC PAR REFRESH.

<table>
<tr><th colspan="3">90 ENC MODULE SEL</th></tr>
<tr><td colspan="2">Firmware block:
ENCODER
(3)

This block
• activates the communication to encoder interface 1/2
• enables encoder emulation/echo
• shows encoder 1/2 speed and actual position.</td><td>
</td></tr>
<tr><td colspan="2">Block inputs located in other parameter groups</td><td>93.21 EMUL PULSE NR (page 237)
93.22 EMUL POS REF (page 237)</td></tr>
<tr><td colspan="2">Block outputs located in other parameter groups</td><td>1.08 ENCODER 1 SPEED (page 77)
1.09 ENCODER 1 POS (page 77)
1.10 ENCODER 2 SPEED (page 78)
1.11 ENCODER 2 POS (page 78)
2.16 FEN DI STATUS (page 85)</td></tr>
<tr><td>90.01</td><td>ENCODER 1 SEL</td><td>FW block: ENCODER (see above)</td></tr>
<tr><td></td><td colspan="2">Activates the communication to optional encoder/resolver interface 1.
Note: It is recommended that encoder interface 1 is used whenever possible since the data received through that interface is fresher than the data received through interface 2. On the other hand, when position values used in emulation are determined by the drive software, the use of encoder interface 2 is recommended as the values are transmitted earlier through interface 2 than through interface 1.</td></tr>
<tr><td></td><td>(0) NONE</td><td>Inactive.</td></tr>
<tr><td></td><td>(1) FEN-01 TTL+</td><td>Communication active. Module type: FEN-01 TTL Encoder interface Module. Input: TTL encoder input with commutation support (X32). See parameter group 93 PULSE ENC CONF.</td></tr>
<tr><td></td><td>(2) FEN-01 TTL</td><td>Communication active. Module type: FEN-01 TTL Encoder interface Module. Input: TTL encoder input (X31). See parameter group 93 PULSE ENC CONF.</td></tr>
<tr><td></td><td>(3) FEN-11 ABS</td><td>Communication active. Module type: FEN-11 Absolute Encoder Interface. Input: Absolute encoder input (X42). See parameter group 91 ABSOL ENC CONF.</td></tr>
<tr><td></td><td>(4) FEN-11 TTL</td><td>Communication active. Module type: FEN-11 Absolute Encoder Interface. Input: TTL encoder input (X41). See parameter group 93 PULSE ENC CONF.</td></tr>
</table>

	(5) FEN-21 RES	Communication active. Module type: FEN-21 Resolver Interface. Input: Resolver input (X52). See parameter group 92 RESOLVER CONF.
	(6) FEN-21 TTL	Communication active. Module type: FEN-21 Resolver Interface. Input: TTL encoder input (X51). See parameter group 93 PULSE ENC CONF.
	(7) FEN-31 HTL	Communication active. Module type: FEN-31 HTL Encoder Interface. Input: HTL encoder input (X82). See parameter group 93 PULSE ENC CONF.
90.02	ENCODER 2 SEL	FW block: ENCODER (see above)
	Activates the communication to the optional encoder/resolver interface 2. For selections, see parameter 90.01 ENCODER 1 SEL. **Note:** The counting of full shaft revolutions is not supported for encoder 2.	
90.03	EMUL MODE SEL	FW block: ENCODER (see above)
	Enables the encoder emulation and selects the position value and the TTL output used in the emulation process. In encoder emulation a calculated position difference is transformed to a corresponding number of TTL pulses to be transmitted via encoder TTL output. The position difference is the difference between the latest and the previous position values. The position value used in emulation can be either a position determined by the drive software or a position measured by an encoder. If drive software position is used, the source for the used position is selected by parameter 93.22 EMUL POS REF. Because the software causes a delay, it is recommended that actual position is always taken from an encoder. Drive software is recommended to be used only with position reference emulation. Encoder emulation can be used to increase or decrease the pulse number when TTL encoder data is transmitted via the TTL output e.g. to another drive. If the pulse number requires no alternation, use encoder echo for data transformation. See parameter 90.04 TTL ECHO SEL. **Note:** If encoder emulation and echo are enabled for the same FEN-xx TTL output, the emulation overrides the echo. If an encoder input is selected as emulation source, the corresponding selection must be activated either with parameter 90.01 ENCODER 1 SEL or 90.02 ENCODER 2 SEL. The TTL encoder pulse number used in emulation must be defined by parameter 93.21 EMUL PULSE NR. See parameter group 93 PULSE ENC CONF.	
	(0) DISABLED	Emulation disabled.
	(1) FEN-01 SWREF	Module type: FEN-01 TTL Encoder interface Module. Emulation: Drive software position (source selected by par. 93.22 EMUL POS REF) is emulated to FEN-01 TTL output.
	(2) FEN-01 TTL+	Module type: FEN-01 TTL Encoder interface Module. Emulation: FEN-01 TTL encoder input (X32) position is emulated to FEN-01 TTL output.
	(3) FEN-01 TTL	Module type: FEN-01 TTL Encoder interface Module. Emulation: FEN-01 TTL encoder input (X31) position is emulated to FEN-01 TTL output.
	(4) FEN-11 SWREF	Module type: FEN-11 Absolute Encoder Interface. Emulation: Drive software position (source selected by par. 93.22 EMUL POS REF) is emulated to FEN-11 TTL output.

	(5) FEN-11 ABS	Module type: FEN-11 Absolute Encoder Interface. Emulation: FEN-11 absolute encoder input (X42) position is emulated to FEN-11 TTL output.
	(6) FEN-11 TTL	Module type: FEN-11 Absolute Encoder Interface. Emulation: FEN-11 TTL encoder input (X41) position is emulated to FEN-11 TTL output.
	(7) FEN-21 SWREF	Module type: FEN-21 Resolver Interface. Emulation: Drive software position (source selected by par. 93.22 EMUL POS REF) is emulated to FEN-21 TTL output.
	(8) FEN-21 RES	Module type: FEN-21 Resolver Interface. Emulation: FEN-21 resolver input (X52) position is emulated to FEN-11 TTL output.
	(9) FEN-21 TTL	Module type: FEN-21 Resolver Interface. Emulation: FEN-21 TTL encoder input (X51) position is emulated to FEN-21 TTL output.
	(10) FEN-31 SWREF	Module type: FEN-31 HTL Encoder Interface. Emulation: Drive software position (source selected by par. 93.22 EMUL POS REF) is emulated to FEN-31 TTL output.
	(11) FEN-31 HTL	Module type: FEN-31 HTL Encoder Interface. Emulation: FEN-31 HTL encoder input (X82) position is emulated to FEN-31 TTL output.
90.04	TTL ECHO SEL	FW block: ENCODER (see above)
	Enables and selects the interface for the TTL encoder signal echo. **Note:** If encoder emulation and echo are enabled for the same FEN-xx TTL output, the emulation overrides the echo.	
	(0) DISABLED	No echo interface enabled.
	(1) FEN-01 TTL+	Module type: FEN-01 TTL Encoder Interface. Echo: TTL encoder input (X32) pulses are echoed to the TTL output.
	(2) FEN-01 TTL	Module type: FEN-01 TTL Encoder Interface. Echo: TTL encoder input (X31) pulses are echoed to the TTL output.
	(3) FEN-11 TTL	Module type: FEN-11 Absolute Encoder Interface. Echo: TTL encoder input (X41) pulses are echoed to the TTL output.
	(4) FEN-21 TTL	Module type: FEN-21 Resolver Interface. Echo: TTL encoder input (X51) pulses are echoed to the TTL output.
	(5) FEN-31 HTL	Module type: FEN-31 HTL Encoder Interface. Echo: HTL encoder input (X82) pulses are echoed to the TTL output.
90.05	ENC CABLE FAULT	FW block: ENCODER (see above)
	Selects the action in case an encoder cable fault is detected by the FEN-xx encoder interface. **Note:** At the time of printing, this functionality is only available with the absolute encoder input of the FEN-11 based on sine/cosine incremental signals, and with the HTL input of the FEN-31.	
	(0) NO	Cable fault detection inactive.
	(1) FAULT	The drive trips on an ENCODER 1/2 CABLE fault.

	(2) WARNING	The drive generates an ENCODER 1/2 CABLE warning. This is the recommended setting if the maximum pulse frequency of sine/cosine incremental signals exceeds 100 kHz; at high frequencies, the signals may attenuate enough to invoke the function. The maximum pulse frequency can be calculated as follows: $$\frac{\text{Pulses per revolution (par. 91.01)} \times \text{Maximum speed in rpm}}{60}$$
90.10	ENC PAR REFRESH	FW block: ENCODER (see above)
	Setting this parameter to 1 forces a reconfiguration of the FEN-xx interfaces, which is needed for any parameter changes in groups 90…93 to take effect. The parameter is read-only when the drive is running.	
	(0) DONE	Refreshing done.
	(1) CONFIGURE	Reconfigure. The value will automatically revert to DONE.

Group 91 ABSOL ENC CONF

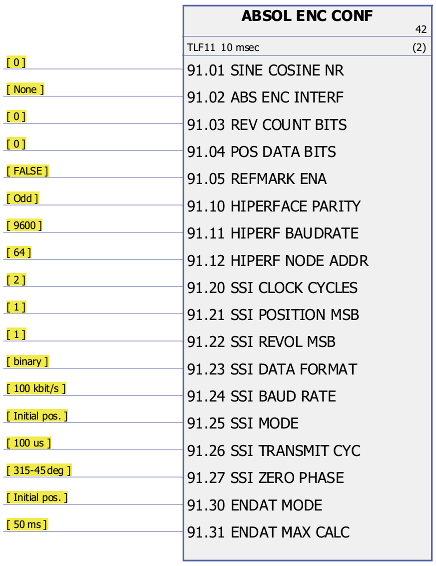
Absolute encoder configuration; used when parameter 90.01 ENCODER 1 SEL / 90.02 ENCODER 2 SEL is set to (3) FEN-11 ABS.

The optional FEN-11 Absolute Encoder Interface module supports the following absolute encoders:

- Incremental sin/cos encoders with or without zero pulse and with or without
- sin/cos commutation signals
- Endat 2.1/2.2 with incremental sin/cos signals (partially without sin/cos incremental signals*)
- Hiperface encoders with incremental sin/cos signals
- SSI (Synchronous Serial Interface) with incremental sin/cos signals (partially without sin/cos incremental signals*).

* EnDat and SSI encoders without incremental sin/cos signals are partially supported only as encoder 1: Speed is not available and the time instant of the position data (delay) depends on the encoder.

See also parameter group 90 ENC MODULE SEL on page 225, and *FEN-11 Absolute Encoder Interface User's Manual* (3AFE68784841 [English]).

91 ABSOL ENC CONF		
Firmware block: **ABSOL ENC CONF** (91) This block configures the absolute encoder connection.		
91.01	SINE COSINE NR	FW block: ABSOL ENC CONF (see above)
	Defines the number of sine/cosine wave cycles within one revolution. **Note:** This parameter does not need to be set when EnDat or SSI encoders are used in continuous mode. See parameter 91.25 SSI MODE / 91.30 ENDAT MODE.	
	0…65535	Number of sine/cosine wave cycles within one revolution.

91.02	ABS ENC INTERF	FW block: ABSOL ENC CONF (see above)
	Selects the source for the encoder position (absolute position).	
	(0) NONE	Not selected.
	(1) COMMUT SIG	Commutation signals.
	(2) ENDAT	Serial interface: EnDat encoder.
	(3) HIPERFACE	Serial interface: HIPERFACE encoder.
	(4) SSI	Serial interface: SSI encoder.
	(5) TAMAG. 17/33B	Serial interface: Tamagawa 17/33-bit encoder.
91.03	REV COUNT BITS	FW block: ABSOL ENC CONF (see above)
	Defines the number of bits used in revolution count (for multi turn encoders). Used with serial interfaces, i.e. when parameter 91.02 ABS ENC INTERF is set to (2) ENDAT, (3) HIPERFACE, (4) SSI or (5) TAMAG. 17/33B.	
	0…32	Number of bits used in revolution count. E.g. 4096 revolutions => 12 bits.
91.04	POS DATA BITS	FW block: ABSOL ENC CONF (see above)
	Defines the number of bits used within one revolution. Used with serial interfaces, i.e. when parameter 91.02 ABS ENC INTERF is set to (2) ENDAT, (3) HIPERFACE, (4) SSI or (5) TAMAG. 17/33B.	
	0…32	Number of bits used within one revolution. E.g. 32768 positions per revolution => 15 bits.
91.05	REFMARK ENA	FW block: ABSOL ENC CONF (see above)
	Enables the encoder zero pulse for FEN-11 encoder input (if exists). Zero pulse can be used for position latching. **Note:** With serial interfaces (i.e. when parameter 91.02 ABS ENC INTERF is set to (2) ENDAT, (3) HIPERFACE, (4) SSI or (5) TAMAG. 17/33B), the zero pulse does not exist.	
	(0) FALSE	Zero pulse disabled.
	(1) TRUE	Zero pulse enabled.
91.10	HIPERFACE PARITY	FW block: ABSOL ENC CONF (see above)
	Defines the use of parity and stop bit(s) for HIPERFACE encoder (i.e. when parameter 91.02 ABS ENC INTERF is set to (3) HIPERFACE). Typically this parameter does not need to be set.	
	(0) ODD	Odd parity indication bit, one stop bit.
	(1) EVEN	Even parity indication bit, one stop bit.
91.11	HIPERF BAUDRATE	FW block: ABSOL ENC CONF (see above)
	Defines the transfer rate of the link for HIPERFACE encoder (i.e. when parameter 91.02 ABS ENC INTERF is set to (3) HIPERFACE). Typically this parameter does not need to be set.	

	(0) 4800	4800 bits/s.
	(1) 9600	9600 bits/s.
	(2) 19200	19200 bits/s.
	(3) 38400	38400 bits/s.
91.12	HIPERF NODE ADDR	FW block: ABSOL ENC CONF (see above)
	Defines the node address for HIPERFACE encoder (i.e. when parameter 91.02 ABS ENC INTERF is set to (3) HIPERFACE). Typically this parameter does not need to be set.	
	0…255	HIPERFACE encoder node address.
91.20	SSI CLOCK CYCLES	FW block: ABSOL ENC CONF (see above)
	Defines the length of the SSI message. The length is defined as the number of clock cycles. The number of cycles can be calculated by adding 1 to the number of the bits in a SSI message frame. Used with SSI encoders, i.e. when parameter 91.02 ABS ENC INTERF is set to (4) SSI.	
	2…127	SSI message length.
91.21	SSI POSITION MSB	FW block: ABSOL ENC CONF (see above)
	Defines the location of the MSB (main significant bit) of the position data within a SSI message. Used with SSI encoders, i.e. when parameter 91.02 ABS ENC INTERF is set to (4) SSI.	
	1…126	Position data MSB location (bit number).
91.22	SSI REVOL MSB	FW block: ABSOL ENC CONF (see above)
	Defines the location of the MSB (main significant bit) of the revolution count within a SSI message. Used with SSI encoders, i.e. when parameter 91.02 ABS ENC INTERF is set to (4) SSI.	
	1…126	Revolution count MSB location (bit number).
91.23	SSI DATA FORMAT	FW block: ABSOL ENC CONF (see above)
	Selects the data format for SSI encoder (i.e. when parameter 91.02 ABS ENC INTERF is set to (4) SSI).	
	(0) BINARY	Binary code.
	(1) GRAY	Gray code.
91.24	SSI BAUD RATE	FW block: ABSOL ENC CONF (see above)
	Selects the baud rate for SSI encoder (i.e. when parameter 91.02 ABS ENC INTERF is set to (4) SSI).	
	(0) 10 kbit/s	10 kbit/s.
	(1) 50 kbit/s	50 kbit/s.
	(2) 100 kbit/s	100 kbit/s.
	(3) 200 kbit/s	200 kbit/s.

	(4) 500 kbit/s	500 kbit/s.
	(5) 1000 kbit/s	1000 kbit/s.
91.25	SSI MODE	FW block: ABSOL ENC CONF (see above)
	Selects the SSI encoder mode. **Note:** Parameter needs to be set only when an SSI encoder is used in continuous mode, i.e. SSI encoder without incremental sin/cos signals (supported only as encoder 1). SSI encoder is selected by setting parameter 91.02 ABS ENC INTERF to (4) SSI.	
	(0) INITIAL POS.	Single position transfer mode (initial position).
	(1) CONTINUOUS	Continuous position transfer mode.
91.26	SSI TRANSMIT CYC	FW block: ABSOL ENC CONF (see above)
	Selects the transmission cycle for SSI encoder. **Note:** This parameter needs to be set only when an SSI encoder is used in continuous mode, i.e. SSI encoder without incremental sin/cos signals (supported only as encoder 1). SSI encoder is selected by setting parameter 91.02 ABS ENC INTERF to (4) SSI.	
	(0) 50 us	50 µs.
	(1) 100 us	100 µs.
	(2) 200 us	200 µs.
	(3) 500 us	500 µs.
	(4) 1 ms	1 ms.
	(5) 2 ms	2 ms.
91.27	SSI ZERO PHASE	FW block: ABSOL ENC CONF (see above)
	Defines the phase angle within one sine/cosine signal period that corresponds to the value of zero on the SSI serial link data. The parameter is used to adjust the synchronization of the SSI position data and the position based on sine/cosine incremental signals. Incorrect synchronization may cause an error of ±1 incremental period. **Note:** This parameter needs to be set only when an SSI encoder with sine/cosine incremental signals is used in initial position mode.	
	(0) 315–45 DEG	315–45 degrees.
	(1) 45–135 DEG	45–135 degrees.
	(2) 135–225 DEG	135–225 degrees.
	(3) 225–315 DEG	225–315 degrees.
91.30	ENDAT MODE	FW block: ABSOL ENC CONF (see above)
	Selects the EnDat encoder mode. **Note:** This parameter needs to be set only when an EnDat encoder is used in continuous mode, i.e. EnDat encoder without incremental sin/cos signals (supported only as encoder 1). EnDat encoder is selected by setting parameter 91.02 ABS ENC INTERF to (2) ENDAT.	
	(0) INITIAL POS.	Single position transfer mode (initial position).

	(1) CONTINUOUS	Continuous position data transfer mode.
91.31	ENDAT MAX CALC	FW block: ABSOL ENC CONF (see above)
	Selects the maximum encoder calculation time for EnDat encoder. **Note:** This parameter needs to be set only when an EnDat encoder is used in continuous mode, i.e. EnDat encoder without incremental sin/cos signals (supported only as encoder 1). EnDat encoder is selected by setting parameter 91.02 ABS ENC INTERF to (2) ENDAT.	
	(0) 10 us	10 µs.
	(1) 100 us	100 µs.
	(2) 1 ms	1 ms.
	(3) 50 ms	50 ms.

Group 92 RESOLVER CONF

Resolver configuration; used when parameter 90.01 ENCODER 1 SEL /90.02 ENCODER 2 SEL is set to (5) FEN-21 RES.

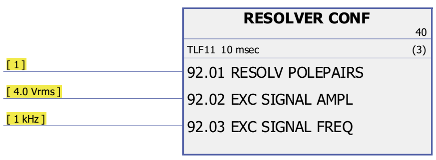
The optional FEN-21 Resolver Interface module is compatible with resolvers which are excited by sinusoidal voltage (to the rotor winding) and which generate sine and cosine signals proportional to the rotor angle (to stator windings).

Note: Configuration data is written into the logic registers of the adapter once after the power-up. If parameter values are changed, save values into the permanent memory by parameter 16.07 PARAM SAVE. The new settings will take effect when the drive is powered up again, or after re-configuration is forced by parameter 90.10 ENC PAR REFRESH.

Resolver autotuning is performed automatically whenever the resolver input is activated after changes to parameters 92.02 EXC SIGNAL AMPL or 92.03 EXC SIGNAL FREQ. Autotuning must be forced after any changes in the resolver cable connection. This can be done by setting either 92.02 EXC SIGNAL AMPL or 92.03 EXC SIGNAL FREQ to its already existing value, and then setting parameter 90.10 ENC PAR REFRESH to 1.

If the resolver (or absolute encoder) is used for feedback from a permanent magnet motor, an AUTOPHASING ID run should be performed after replacement or any parameter changes. See parameter 99.13 IDRUN MODE and section *Autophasing* on page 40.

See also parameter group 90 ENC MODULE SEL on page 225, and *FEN-21 Resolver Interface User's Manual* (3AFE68784859 [English]).

92 RESOLVER CONF		
Firmware block: **RESOLVER CONF** (92) This block configures the resolver connection.		
92.01	RESOLV POLEPAIRS	FW block: RESOLVER CONF (see above)
	Selects the number of pole pairs.	
	1…32	Number of pole pairs.
92.02	EXC SIGNAL AMPL	FW block: RESOLVER CONF (see above)
	Defines the amplitude of the excitation signal.	
	4.0…12.0 Vrms	Excitation signal amplitude.
92.03	EXC SIGNAL FREQ	FW block: RESOLVER CONF (see above)
	Defines the frequency of the excitation signal.	
	1…20 kHz	Excitation signal frequency.

Group 93 PULSE ENC CONF

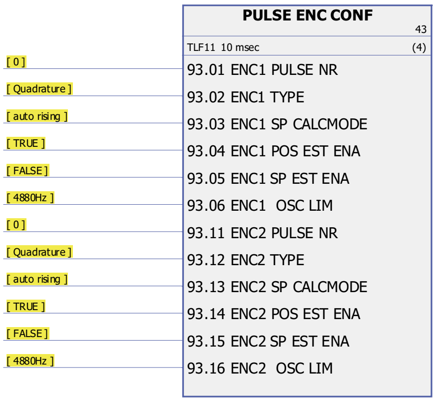
TTL/HTL input and TTL output configuration. See also parameter group 90 ENC MODULE SEL on page 225, and the appropriate encoder extension module manual.

Parameters 93.01…93.06 are used when a TTL/HTL encoder is used as encoder 1 (see parameter 90.01 ENCODER 1 SEL).

Parameters 93.11…93.16 are used when a TTL/HTL encoder is used as encoder 2 (see parameter 90.02 ENCODER 2 SEL).

In normal operation, only parameter 93.01/93.11 needs to be set for TTL/HTL encoders.

Note: Configuration data is written into the logic registers of the adapter once after the power-up. If parameter values are changed, save values into the permanent memory by parameter 16.07 PARAM SAVE. The new settings will take effect when the drive is powered up again, or after re-configuration is forced by parameter 90.10 ENC PAR REFRESH.

93 PULSE ENC CONF		
Firmware block: **PULSE ENC CONF** (93) This block configures the TTL/HTL input and TTL output.		
93.01	ENC1 PULSE NR	FW block: PULSE ENC CONF (see above)
	Defines the pulse number per revolution for encoder 1.	
	0…65535	Pulses per revolution for encoder 1.
93.02	ENC1 TYPE	FW block: PULSE ENC CONF (see above)
	Selects the type of encoder 1.	
	(0) QUADRATURE	Quadrature encoder (two channels, channels A and B).
	(1) SINGLE TRACK	Single track encoder (one channel, channel A).
93.03	ENC1 SP CALCMODE	FW block: PULSE ENC CONF (see above)
	Selects the speed calculation mode for encoder 1. *When single track mode has been selected by parameter 93.02 ENC1 TYPE, the speed is always positive.	

	(0) A&B ALL	Channels A and B: Rising and falling edges are used for speed calculation. Channel B: Defines the direction of rotation. * **Note:** When single track mode has been selected by parameter 93.02 ENC1 TYPE, setting 0 acts like setting 1.
	(1) A ALL	Channel A: Rising and falling edges are used for speed calculation. Channel B: Defines the direction of rotation. *
	(2) A RISING	Channel A: Rising edges are used for speed calculation. Channel B: Defines the direction of rotation. *
	(3) A FALLING	Channel A: Falling edges are used for speed calculation. Channel B: Defines the direction of rotation. *
	(4) AUTO RISING **(5)** AUTO FALLING	Used mode (1, 2 or 3) is changed automatically depending on the pulse frequency according to the following table: **93.03 = 4** / **93.03 = 5** — **Used mode** — **Pulse frequency of the channel(s)** 0 / 0 — < 2442 Hz 1 / 1 — 2442…4884 Hz 2 / 3 — > 4884 Hz
93.04	ENC1 POS EST ENA	FW block: PULSE ENC CONF (see above)
	Selects whether measured and estimated position is used with encoder 1.	
	(0) FALSE	Measured position (Resolution: 4 x pulses per revolution for quadrature encoders, 2 x pulses per revolution for single track encoders.)
	(1) TRUE	Estimated position. (Uses position extrapolation. Extrapolated at the time of data request.)
93.05	ENC1 SP EST ENA	FW block: PULSE ENC CONF (see above)
	Selects whether calculated or estimated speed is used with encoder 1.	
	(0) FALSE	Last calculated speed (calculation interval is 62.5 µs…4 ms).
	(1) TRUE	Estimated speed (estimated at the time of data request) Estimation increases the speed ripple in steady state operation, but improves the dynamics.
93.06	ENC1 OSC LIM	FW block: PULSE ENC CONF (see above)
	Selects the maximum pulse frequency for the changing of the direction of rotation (used with encoder 1).	
	(0) 4880HZ	4880 Hz.
	(1) 2440HZ	2440 Hz.
	(2) 1220HZ	1220 Hz.
	(3) DISABLED	Not selected.

93.11	ENC2 PULSE NR	FW block: PULSE ENC CONF (see above)
	Defines the pulse number per revolution for encoder 2.	
	0…65535	Pulses per revolution for encoder 2.
93.12	ENC2 TYPE	FW block: PULSE ENC CONF (see above)
	Selects the type of encoder 2. For selections, see parameter 93.02 ENC1 TYPE.	
93.13	ENC2 SP CALCMODE	FW block: PULSE ENC CONF (see above)
	Selects the speed calculation mode for encoder 2. For selections, see parameter 93.03 ENC1 SP CALCMODE.	
93.14	ENC2 POS EST ENA	FW block: PULSE ENC CONF (see above)
	Selects whether measured and estimated position is used with encoder 2. For selections, see parameter 93.04 ENC1 POS EST ENA.	
93.15	ENC2 SP EST ENA	FW block: PULSE ENC CONF (see above)
	Selects whether calculated or estimated speed is used with encoder 2. For selections, see parameter 93.05 ENC1 SP EST ENA.	
93.16	ENC2 OSC LIM	FW block: PULSE ENC CONF (see above)
	Selects the maximum pulse frequency for the changing of the direction of rotation (used with encoder 2). For selections, see parameter 93.06 ENC1 OSC LIM.	
93.21	EMUL PULSE NR	FW block: ENCODER (page 225)
	Defines the number of TTL/HTL pulses per revolution used in encoder emulation. Encoder emulation is enabled by parameter 90.03 EMUL MODE SEL.	
	0…65535	TTL pulses used in encoder emulation.
93.22	EMUL POS REF	FW block: ENCODER (page 225)
	Selects the source for the position value used in encoder emulation when parameter 90.03 EMUL MODE SEL is set to (1) FEN-01 SWREF, (4) FEN-11 SWREF, (7) FEN-21 SWREF or (10) FEN-31 SWREF. See parameter group 90 ENC MODULE SEL. The source can be any actual or reference position value (except 1.09 ENCODER 1 POS and 1.11 ENCODER 2 POS).	
	Value pointer: Group and index	

Group 95 HW CONFIGURATION

Miscellaneous hardware-related settings.

95 HW CONFIGURATION		
95.01	CTRL UNIT SUPPLY	FW block: None
	Defines the manner in which the drive control unit is powered.	
	(0) INTERNAL 24V	The drive control unit is powered from the drive power unit it is mounted on.
	(1) EXTERNAL 24V	The drive control unit is powered from an external power supply.
95.02	EXTERNAL CHOKE	FW block: None
	Defines if the drive is equipped with an AC choke or not.	
	(0) NO	The drive is not equipped with an AC choke.
	(1) YES	The drive is equipped with an AC choke.

Group 97 USER MOTOR PAR

User adjustment of motor model values estimated during ID run. The values can be entered in either “per unit” or SI.

97 USER MOTOR PAR		
97.01	USE GIVEN PARAMS	FW block: None
	Activates the motor model parameters 97.02…97.14. The value is automatically set to zero when ID run is selected by parameter 99.13 IDRUN MODE. The values of parameters 97.02…97.14 are updated according to the motor characteristics identified during the ID run. **Note:** This parameter cannot be changed while the drive is running.	
	(0) NO	Inactive.
	(1) USE GIVEN	The values of parameters 97.02…97.14 are used in the motor model.
97.02	RS USER	FW block: None
	Defines the stator resistance R_S of the motor model.	
	0…0.5 p.u. (per unit)	Stator resistance.
97.03	RR USER	FW block: None
	Defines the rotor resistance R_R of the motor model. **Note:** This parameter is valid only for asynchronous motors.	
	0…0.5 p.u. (per unit)	Rotor resistance.
97.04	LM USER	FW block: None
	Defines the main inductance L_M of the motor model. **Note:** This parameter is valid only for asynchronous motors.	
	0…10 p.u. (per unit)	Main inductance.
97.05	SIGMAL USER	FW block: None
	Defines the leakage inductance σL_S. **Note:** This parameter is valid only for asynchronous motors.	
	0…1 p.u. (per unit)	Leakage inductance.
97.06	LD USER	FW block: None
	Defines the direct axis (synchronous) inductance. **Note:** This parameter is valid only for permanent magnet motors.	
	0…10 p.u. (per unit)	Direct axis (synchronous) inductance.

97.07	LQ USER	FW block: None
	Defines the quadrature axis (synchronous) inductance. **Note:** This parameter is valid only for permanent magnet motors.	
	0…10 p.u. (per unit)	Quadrature axis (synchronous) inductance.
97.08	PM FLUX USER	FW block: None
	Defines the permanent magnet flux. **Note:** This parameter is valid only for permanent magnet motors.	
	0…2 p.u. (per unit)	Permanent magnet flux.
97.09	RS USER SI	FW block: None
	Defines the stator resistance R_S of the motor model.	
	0.00000…100.00000 ohm	Stator resistance.
97.10	RR USER SI	FW block: None
	Defines the rotor resistance R_R of the motor model. **Note:** This parameter is valid only for asynchronous motors.	
	0.00000…100.00000 ohm	Rotor resistance.
97.11	LM USER SI	FW block: None
	Defines the main inductance L_M of the motor model. **Note:** This parameter is valid only for asynchronous motors.	
	0.00…100000.00 mH	Main inductance.
97.12	SIGL USER SI	FW block: None
	Defines the leakage inductance σL_S. **Note:** This parameter is valid only for asynchronous motors.	
	0.00…100000.00 mH	Leakage inductance.
97.13	LD USER SI	FW block: None
	Defines the direct axis (synchronous) inductance. **Note:** This parameter is valid only for permanent magnet motors.	
	0.00…100000.00 mH	Direct axis (synchronous) inductance.
97.14	LQ USER SI	FW block: None
	Defines the quadrature axis (synchronous) inductance. **Note:** This parameter is valid only for permanent magnet motors.	
	0.00…100000.00 mH	Quadrature axis (synchronous) inductance.

Group 98 MOTOR CALC VALUES

Calculated motor values.

98 MOTOR CALC VALUES		
98.01	TORQ NOM SCALE	FW block: None
	Nominal torque in N•m which corresponds to 100%. **Note:** This parameter is copied from parameter 99.12 MOT NOM TORQUE if given. Otherwise the value is calculated.	
	0…2147483 Nm	Nominal torque.
98.02	POLEPAIRS	FW block: None
	Calculated number of motor pole pairs. **Note:** This parameter cannot be set by the user.	
	0…1000	Calculated number of motor pole pairs.

Group 99 START-UP DATA

Start-up settings such as language, motor data and motor control mode.

The nominal motor values must be set before the drive is started; for detailed instructions, see chapter *Start-up* on page 15.

With DTC motor control mode, parameters 99.06…99.10 must be set; better control accuracy is achieved by also setting parameters 99.11 and 99.12.

With scalar control, parameters 99.06…99.09 must be set.

99 START-UP DATA		
99.01	LANGUAGE	FW block: None
	Selects the language.	
	(0809h) ENGLISH	English.
	(0407h) DEUTSCH	German.
	(0410h) ITALIANO	Italian.
	(040Ah) ESPAÑOL	Spanish.
	(041Dh) SVENSKA	Swedish.
	(041Fh) TÜRKÇE	Turkish.
99.04	MOTOR TYPE	FW block: None
	Selects the motor type. **Note:** This parameter cannot be changed while the drive is running.	
	(0) AM	Asynchronous motor. Three phase AC voltage supplied induction motor with squirrel cage rotor.
	(1) PMSM	Permanent magnet motor. Three phase AC voltage supplied synchronous motor with permanent magnet rotor and sinusoidal BackEMF voltage.

99.05	MOTOR CTRL MODE	FW block: None
	Selects the motor control mode. DTC (Direct torque control) mode is suitable for most applications. Scalar control is suitable for special cases where DTC cannot be applied. In Scalar Control, the drive is controlled with a frequency reference. The outstanding motor control accuracy of DTC cannot be achieved in scalar control. There are some standard features that are disabled in the scalar control mode, for example motor identification run (99.13), torque limits in parameter group 20 LIMITS, DC hold and DC magnetising (11.04…11.06, 11.01). **Note:** Correct motor run requires that the magnetising current of the motor does not exceed 90 percent of the nominal current of the inverter. **Note:** Scalar control mode must be used • with multimotor applications 1) if the load is not equally shared between the motors, 2) if the motors are of different sizes, or 3) if the motors are going to be changed after the motor identification, • if the nominal current of the motor is less than 1/6 of the nominal output current of the drive, or • if the drive is used with no motor connected (e.g. for test purposes).	
	(0) DTC	Direct torque control mode.
	(1) SCALAR	Scalar control mode.
99.06	MOT NOM CURRENT	FW block: None
	Defines the nominal motor current. Must be equal to the value on the motor rating plate. If several motors are connected to the inverter, enter the total current of the motors. **Note:** Correct motor run requires that the magnetising current of the motor does not exceed 90 percent of the nominal current of the inverter. **Note:** This parameter cannot be changed while the drive is running.	
	0…32767 A	Nominal motor current. **Note:** The allowed range is 1/6…2 × I_{2N} of drive for direct control mode (parameter 99.05 MOTOR CTRL MODE = (0) DTC). For scalar control mode (parameter 99.05 MOTOR CTRL MODE = (1) SCALAR), the allowed range is 0…2 × I_{2N} of drive.
99.07	MOT NOM VOLTAGE	FW block: None
	Defines the nominal motor voltage. Nominal voltage is a fundamental phase to phase rms voltage, which is supplied to the motor at the nominal operating point. This parameter value must be equal to the value on the asynchronous motor name plate. **Note:** Make sure the motor is connected correctly (star or delta) in accordance to the rating plate. **Note:** With permanent magnet motors, the nominal voltage is the BackEMF voltage (at motor nominal speed). If the voltage is given as voltage per rpm, e.g. 60 V per 1000 rpm, the voltage for 3000 rpm nominal speed is 3 × 60 V = 180 V. Note that the nominal voltage is not equal to the equivalent DC motor voltage (E.D.C.M.) value given by some motor manufactures. The nominal voltage can be calculated by dividing the E.D.C.M. voltage by 1.7 (= square root of 3). **Note:** The stress on the motor insulations is always dependent on the drive supply voltage. This also applies to the case where the motor voltage rating is lower than the rating of the drive and the supply of the drive. **Note:** This parameter cannot be changed while the drive is running.	
	0…32767 V	Nominal motor voltage. **Note:** The allowed range is 1/6…2 × U_N of drive.

99.08	MOT NOM FREQ	FW block: None
	Defines the nominal motor frequency. **Note:** This parameter cannot be changed while the drive is running.	
	5…500 Hz	Nominal motor frequency.
99.09	MOT NOM SPEED	FW block: None
	Defines the nominal motor speed. Must be equal to the value on the motor rating plate. When parameter value is changed, check the speed limits in parameter group 20 LIMITS. **Note:** This parameter cannot be changed while the drive is running.	
	0…30000 rpm	Nominal motor speed.
99.10	MOT NOM POWER	FW block: None
	Defines the nominal motor power. Must be equal to the value on the motor rating plate. If several motors are connected to the inverter, enter the total power of the motors. Set also parameter 99.11 MOT NOM COSFII. **Note:** This parameter cannot be changed while the drive is running.	
	0…10000 kW	Nominal motor power.
99.11	MOT NOM COSFII	FW block: None
	Defines the cosphi (not applicable to permanent magnet motors) for a more accurate motor model. Not obligatory; if set, should be equal to the value on the motor rating plate. **Note:** This parameter cannot be changed while the drive is running.	
	0…1	Cosphi (0 = parameter disabled).
99.12	MOT NOM TORQUE	FW block: None
	Defines the nominal motor shaft torque for a more accurate motor model. Not obligatory. **Note:** This parameter cannot be changed while the drive is running.	
	0…2147483 Nm	Nominal motor shaft torque.

99.13	IDRUN MODE	FW block: None
	Selects the type of the motor identification performed at the next start of the drive in DTC mode. During the identification, the drive will identify the characteristics of the motor for optimum motor control. After the ID run, the drive is stopped. **Note:** This parameter cannot be changed while the drive is running. Once the ID run is activated, it can be cancelled by stopping the drive: If ID run has already been performed once, parameter is automatically set to (0) NO. If no ID run has been performed yet, parameter is automatically set to (3) STANDSTILL. In this case, the ID run must be performed. **Notes:** • ID run can only be performed in local control (i.e. when drive is controlled via PC tool or control panel). • ID run cannot be performed if parameter 99.05 MOTOR CTRL MODE is set to (1) SCALAR. • ID run must be performed every time any of the motor parameters (99.04, 99.06…99.12) have been changed. Parameter is automatically set to STANDSTILL after the motor parameters have been set. • With permanent magnet motor, the motor shaft must NOT be locked and the load torque must be < 10% during the ID run (Normal/Reduced/Standstill). • Mechanical brake (if present) is not opened during the ID run. • Ensure that possible Safe Torque Off and emergency stop circuits are closed during ID run.	
	(0) NO	No motor ID run is requested. This mode can be selected only if the ID run (Normal/Reduced/Standstill) has already been performed once.
	(1) NORMAL	Guarantees the best possible control accuracy. The ID run takes about 90 seconds. This mode should be selected whenever it is possible. **Note:** The driven machinery must be de-coupled from the motor with Normal ID run: • if the load torque is higher than 20%. • if the machinery is not able to withstand the nominal torque transient during the ID run. **Note:** Check the direction of rotation of the motor before starting the ID run. During the run, the motor will rotate in the forward direction. **WARNING!** The motor will run at up to approximately 50…100% of the nominal speed during the ID run. ENSURE THAT IT IS SAFE TO RUN THE MOTOR BEFORE PERFORMING THE ID RUN!
	(2) REDUCED	Reduced ID Run. This mode should be selected instead of the Normal ID Run • if mechanical losses are higher than 20% (i.e. the motor cannot be de-coupled from the driven equipment), or • if flux reduction is not allowed while the motor is running (i.e. in case of a motor with an integrated brake supplied from the motor terminals). With Reduced ID run, the control in the field weakening area or at high torques is not necessarily as accurate as with the Normal ID run. Reduced ID run is completed faster than the Normal ID Run (< 90 seconds). **Note:** Check the direction of rotation of the motor before starting the ID run. During the run, the motor will rotate in the forward direction. **WARNING!** The motor will run at up to approximately 50…100% of the nominal speed during the ID run. ENSURE THAT IT IS SAFE TO RUN THE MOTOR BEFORE PERFORMING THE ID RUN!

	(3) STANDSTILL	Standstill ID run. The motor is injected with DC current. With asynchronous motor, the motor shaft is not rotating (with permanent magnet motor the shaft can rotate < 0.5 revolution). **Note:** This mode should be selected only if the Normal or Reduced ID run is not possible due to the restrictions caused by the connected mechanics (e.g. with lift or crane applications).
	(4) AUTOPHASING	During autophasing, the start angle of the motor is determined. Note that other motor model values are not updated. See also parameter 11.07 AUTOPHASING MODE, and section *Autophasing* on page 40. **Notes:** • Autophasing can only be selected after the Normal/Reduced/ Standstill ID run has been performed once. Autophasing is used when an absolute encoder has been added/changed to a permanent magnet motor and there is no need to perform the Normal/Reduced/Standstill ID run again. • During Autophasing the motor shaft must NOT be locked and the load torque must be < 5%.
	(5) CUR MEAS CAL	Current offset and gain measurement calibration. The calibration will be performed at next start.

Parameter data

What this chapter contains

This chapter lists the parameters of the drive with some additional data. For the parameter descriptions, see chapter Parameters and firmware blocks.

Terms

Term	Definition
Actual signal	Signal measured or calculated by the drive. Can be monitored by the user. No user setting is possible.
Def	Default value
enum	Enumerated list, i.e. selection list
FbEq	Fieldbus equivalent: The scaling between the value shown on the panel and the integer used in serial communication.
Page no.	Page number for more information
INT32	32-bit integer value (31 bits + sign)
Bit pointer	Bit pointer. A bit pointer points to a single bit in the value of another parameter.
Val pointer	Value pointer. A value pointer points to the value of another parameter.
Parameter	An operation instruction of the drive that is often user-adjustable. Parameters that are signals measured or calculated by the drive are called actual signals.
Pb	Packed boolean
PT	Parameter protection type. See WP and WPD.
REAL	16-bit value 16-bit value (31 bits + sign) = integer value = fractional value
REAL24	8-bit value 24-bit value (31 bits + sign) = integer value = fractional value
Save PF	Parameter is saved to flash memory at 1 minute intervals to prevent data loss if power supply to the drive control unit is lost.
Type	Data type. See enum, INT32, Bit pointer, Val pointer, Pb, REAL, REAL24, UINT32.
UINT32	32-bit unsigned integer value
WP	Write protected parameter (i.e. read only)
WPD	Write protected parameter while drive is running

Fieldbus equivalent

Serial communication data between fieldbus adapter and drive is transferred in integer format. Thus the drive actual and reference signal values must be scaled to 16/32-bit integer values. Fieldbus equivalent defines the scaling between the signal value and the integer used in serial communication.

All the read and sent values are limited to 16/32 bits.

Example: If 32.04 MAXIMUM TORQ REF is set from external control system, an integer value of 10 corresponds to 1%.

Fieldbus addresses

For FPBA-01 Profibus Adapter, FDNA-01 DeviceNet Adapter and FCAN-01 CANopen Adapter, see the User's Manual of the fieldbus adapter module.

Pointer parameter format in fieldbus communication

Value and bit pointer parameters are transferred between the fieldbus adapter and drive as 32-bit integer values.

32-bit integer value pointers

When value pointer parameter is connected to the value of another parameter or signal, the format is as follows:

	Bit			
	30…31	**16…29**	**8…15**	**0…7**
Name	Source type		Group	Index
Value	1	-	1…255	1…255
Description	Value pointer is connected to parameter/signal.	-	Group of source parameter	Index of source parameter

When value pointer parameter is connected to an application program, the format is as follows:

	Bit		
	30…31	**24…29**	**0…23**
Name	Source type	Not in use	Address
Value	2	-	$0…2^{23}$
Description	Value pointer is connected to application program.	-	Relative address of application program variable

Note: Value pointer parameters which are connected to an application program cannot be set via fieldbus (i.e. read access only).

32-bit integer bit pointers

When bit pointer parameter is connected to value 0 or 1, the format is as follows:

	Bit		
	30…31	**16…29**	**0**
Name	Source type	Not in use	Value
Value	0	-	0…1
Description	Bit pointer is connected to 0/1.	-	0 = False, 1 = True

When bit pointer is connected to a bit value of another signal, the format is as follows:

	Bit				
	30…31	**24…29**	**16…23**	**8…15**	**0…7**
Name	Source type	Not in use	Bit sel	Group	Index
Value	1	-	0…31	2…255	1…255
Description	Bit pointer is connected to signal bit value.	-	Bit selection	Group of source parameter	Index of source parameter

When bit pointer parameter is connected to an application program, the format is as follows:

	Bit		
	30…31	**24…29**	**0…23**
Name	Source type	Bit sel	Address
Value	2	0…31	$0…2^{23}$
Description	Bit pointer is connected to application program.	Bit selection	Relative address of application program variable

Note: Bit pointer parameters which are connected to an application program cannot be set via fieldbus (i.e. read access only).

Actual signals (Parameter groups 1…9)

Index	Name	Type	Range	Unit	FbEq	Update time	Data length	PT	Save PF	Page no.
01	ACTUAL VALUES									
1.01	SPEED ACT	REAL	-30000…30000	rpm	1 = 100	250 µs	32	WP		77
1.02	SPEED ACT PERC	REAL	-1000…1000	%	1 = 100	2 ms	32	WP		77
1.03	FREQUENCY	REAL	-30000…30000	Hz	1 = 100	2 ms	32	WP		77
1.04	CURRENT	REAL	0…30000	A	1 = 100	10 ms	32	WP		77
1.05	CURRENT PERC	REAL	0…1000	%	1 = 10	2 ms	16	WP		77
1.06	TORQUE	REAL	-1600…1600	%	1 = 10	2 ms	16	WP		77
1.07	DC-VOLTAGE	REAL	-	V	1 = 100	2 ms	32	WP		77
1.08	ENCODER 1 SPEED	REAL	-	rpm	1 = 100	250 µs	32	WP		77
1.09	ENCODER 1 POS	REAL24	-	rev	1=100000000	250 µs	32	WP		77
1.10	ENCODER 2 SPEED	REAL	-	rpm	1 = 100	250 µs	32	WP		78
1.11	ENCODER 2 POS	REAL24	-	rev	1=100000000	250 µs	32	WP		78
1.12	POS ACT	REAL	-32768…32767	*	See 60.09	250 µs	32	WP		78
1.13	POS 2ND ENC	REAL	-32768…32767	revs	1 = 1	250 µs	32	WP		78
1.14	SPEED ESTIMATED	REAL	-30000…30000	rpm	1 = 100	2 ms	32	WP		78
1.15	TEMP INVERTER	REAL24	-40…160	°C	1 = 10	2 ms	16	WP		78
1.16	TEMP BC	REAL24	-40…160	°C	1 = 10	2 ms	16	WP		78
1.17	MOTOR TEMP	REAL	-10…250	°C	1 = 10	10 ms	16	WP		78
1.18	MOTOR TEMP EST	INT32	-60…1000	°C	1 = 1	-	16	WP	x	78
1.19	USED SUPPLY VOLT	REAL	0…1000	V	1 = 10	10 ms	16	WP		78
1.20	BRAKE RES LOAD	REAL24	0…1000	%	1 = 1	50 ms	16	WP		78
1.21	CPU USAGE	UINT32	0…100	%	1 = 1	-	16	WP		78
1.22	INVERTER POWER	REAL	$-2^{31}…2^{31}$ - 1	kW	1 = 100	10 ms	32	WP		78
1.26	ON TIME COUNTER	INT32	0…35791394.1	h	1 = 100	10 ms	32	WP	x	78
1.27	RUN TIME COUNTER	INT32	0…35791394.1	h	1 = 100	10 ms	32	WP	x	79
1.31	MECH TIME CONST	REAL	0…32767	s	1 = 1000	10 ms	32	WP	x	79
02	I/O VALUES									
2.01	DI STATUS	Pb	0…0x3F	-	1 = 1	2 ms	16	WP		80
2.02	RO STATUS	Pb	-	-	1 = 1	2 ms	16	WP		80
2.03	DIO STATUS	Pb	-	-	1 = 1	2 ms	16	WP		80
2.04	AI1	REAL	-	V or mA	1 = 1000	2 ms	16	WP		80
2.05	AI1 SCALED	REAL	-	-	1 = 1000	250 µs	32	WP		80
2.06	AI2	REAL	-	V or mA	1 = 1000	2 ms	16	WP		80
2.07	AI2 SCALED	REAL	-	-	1 = 1000	250 µs	32	WP		80
2.08	AO1	REAL	-	mA	1 = 1000	2 ms	16	WP		80
2.09	AO2	REAL	-	V	1 = 1000	2 ms	16	WP		80
2.10	DIO2 FREQ IN	REAL	0…32767	Hz	1 = 1000	2 ms	32	WP		80
2.11	DIO3 FREQ OUT	REAL	0…32767	Hz	1 = 1000	2 ms	32	WP		80
2.12	FBA MAIN CW	Pb	0 … 0xFFFFFFFF	-	1 = 1	500 µs	32	WP		81
2.13	FBA MAIN SW	Pb	0 … 0xFFFFFFFF	-	1 = 1	-	32	WP		84
2.14	FBA MAIN REF1	INT32	$-2^{31}…2^{31}$ - 1	-	1 = 1	500 µs	32	WP		85

Index	Name	Type	Range	Unit	FbEq	Update time	Data length	PT	Save PF	Page no.
2.15	FBA MAIN REF2	INT32	$-2^{31}...2^{31} - 1$	-	1 = 1	500 µs	32	WP		85
2.16	FEN DI STATUS	Pb	0…0x33	-	1 = 1	500 µs	16	WP		85
2.17	D2D MAIN CW	Pb	0…0xFFFF	-	1 = 1	500 µs	16	WP		86
2.18	D2D FOLLOWER CW	Pb	0…0xFFFF	-	1 = 1	2 ms	16	WP		86
2.19	D2D REF1	REAL	$-2^{31}...2^{31} - 1$	-	1 = 1	500 µs	32	WP		86
2.20	D2D REF2	REAL	$-2^{31}...2^{31} - 1$	-	1 = 1	2 ms	32	WP		86
03	CONTROL VALUES									
3.01	SPEED REF1	REAL	-30000…30000	rpm	1 = 100	500 µs	32	WP		87
3.02	SPEED REF2	REAL	-30000…30000	rpm	1 = 100	500 µs	32	WP		87
3.03	SPEEDREF RAMP IN	REAL	-30000…30000	rpm	1 = 100	500 µs	32	WP		87
3.04	SPEEDREF RAMPED	REAL	-30000…30000	rpm	1 = 100	500 µs	32	WP		87
3.05	SPEEDREF USED	REAL	-30000…30000	rpm	1 = 100	250 µs	32	WP		87
3.06	SPEED ERROR FILT	REAL	-30000…30000	rpm	1 = 100	250 µs	32	WP		87
3.07	ACC COMP TORQ	REAL	-1600…1600	%	1 = 10	250 µs	16	WP		87
3.08	TORQ REF SP CTRL	REAL	-1600…1600	%	1 = 10	250 µs	16	WP		87
3.09	TORQ REF1	REAL	-1000…1000	%	1 = 10	500 µs	16	WP		87
3.10	TORQ REF RAMPED	REAL	-1000…1000	%	1 = 10	500 µs	16	WP		87
3.11	TORQ REF RUSHLIM	REAL	-1000…1000	%	1 = 10	250 µs	16	WP		87
3.12	TORQUE REF ADD	REAL	-1000…1000	%	1 = 10	250 µs	16	WP		87
3.13	TORQ REF TO TC	REAL	-1600…1600	%	1 = 10	250 µs	16	WP		88
3.14	BRAKE TORQ MEM	REAL	-1000…1000	%	1 = 10	2 ms	16	WP	x	88
3.15	BRAKE COMMAND	enum	0…1	-	1 = 1	2 ms	16	WP		88
3.16	FLUX REF USED	REAL24	0…200	%	1 = 1	2 ms	16	WP		88
3.17	TORQUE REF USED	REAL	-1600…1600	%	1 = 10	250 µs	32	WP		88
4	POS CTRL VALUES									
4.01	SPEED REF POS	REAL	-32768…32768	rpm	1 = 100	250 µs	32	WP		89
4.02	SPEED ACT LOAD	REAL	-32768…32768	**	See 60.10	500 µs	32	WP		89
4.03	PROBE1 POS MEAS	REAL	-32768…32768	*	See 60.09	2 ms	32	WP		89
4.04	PROBE2 POS MEAS	REAL	-32768…32768	*	See 60.09	2 ms	32	WP		89
4.05	CYCLIC POS ERR	REAL	-32768…32768	*	See 60.09	2 ms	32	WP		89
4.06	POS REF	REAL	-32768…32768	*	See 60.09	500 µs	32	WP		89
4.07	PROF SPEED	REAL	-32768…32768	**	See 60.10	500 µs	32	WP		89
4.08	PROF ACC	REAL	0…32768	**	See 60.10	500 µs	32	WP		89
4.09	PROF DEC	REAL	-32768…0	**	See 60.10	500 µs	32	WP		89
4.10	PROF FILT TIME	REAL	0…1000	ms	1 = 1	500 µs	16	WP		89
4.11	POS STYLE	Pb	0…0x1FF	-	1 = 1	500 µs	16	WP		89
4.12	POS END SPEED	REAL	0…32768	**	See 60.10	500 µs	32	WP		90
4.13	POS REF IPO	REAL	-32768…32768	*	See 60.09	500 µs	32	WP		90
4.14	DIST TGT	REAL	-32768…32768	*	See 60.09	500 µs	32	WP		90
4.15	SYNC REF UNGEAR	REAL	-32768…32768	*	See 60.09	500 µs	32	WP		90
4.16	SYNC REF GEARED	REAL	-32768…32768	*	See 60.09	500 µs	32	WP		90
4.17	POS REF LIMITED	REAL	-32768…32768	*	See 60.09	250 µs	32	WP		90
4.18	SYNC ERROR	REAL	-32768…32768	*	See 60.09	250 µs	32	WP		90
4.19	POS ERROR	REAL	-32768…32768	*	See 60.09	250 µs	32	WP		90

Index	Name	Type	Range	Unit	FbEq	Update time	Data length	PT	Save PF	Page no.
4.20	SPEED FEED FWD	REAL	-32768…32768	rpm	1 = 100	250 µs	32	WP		90
06	DRIVE STATUS									
6.01	STATUS WORD 1	Pb	0…65535	-	1 = 1	2 ms	16	WP		91
6.02	STATUS WORD 2	Pb	0…65535	-	1 = 1	2 ms	16	WP		92
6.03	SPEED CTRL STAT	Pb	0…31	-	1 = 1	250 µs	16	WP		93
6.05	LIMIT WORD 1	Pb	0…255	-	1 = 1	250 µs	16	WP		93
6.07	TORQ LIM STATUS	Pb	0…65535	-	1 = 1	250 µs	16	WP		94
6.09	POS CTRL STATUS	Pb	0…65535	-	1 = 1	2 ms	16	WP		95
6.10	POS CTRL STATUS2	Pb	0…65535	-	1 = 1	2 ms	16	WP		96
6.11	POS CORR STATUS	Pb	0…65535	-	1 = 1	2 ms	16	WP		97
6.12	OP MODE ACK	enum	0…11	-	1 = 1	2 ms	16	WP		97
6.14	SUPERV STATUS	Pb	0…65535	-	1 = 1	2 ms	16	WP		98
08	ALARMS & FAULTS									
8.01	ACTIVE FAULT	enum	0…65535	-	1 = 1	-	16	WP		99
8.02	LAST FAULT	enum	0…65535	-	1 = 1	-	16	WP		99
8.03	FAULT TIME HI	INT32	$-2^{31}…2^{31} - 1$	days	1 = 1	-	32	WP		99
8.04	FAULT TIME LO	INT32	$-2^{31}…2^{31} - 1$	time	1 = 1	-	32	WP		99
8.05	ALARM WORD 1	UINT32	-	-	1 = 1	2 ms	16	WP		99
8.06	ALARM WORD 2	UINT32	-	-	1 = 1	2 ms	16	WP		100
8.07	ALARM WORD 3	UINT32	-	-	1 = 1	2 ms	16	WP		100
8.08	ALARM WORD 4	UINT32	-	-	1 = 1	2 ms	16	WP		100
09	SYSTEM INFO									
9.01	DRIVE TYPE	INT32	0…65535	-	1 = 1	-	16	WP		101
9.02	DRIVE RATING ID	INT32	0…65535	-	1 = 1	-	16	WP		101
9.03	FIRMWARE ID	Pb	-	-	1 = 1	-	16	WP		101
9.04	FIRMWARE VER	Pb	-	-	1 = 1	-	16	WP		101
9.05	FIRMWARE PATCH	Pb	-	-	1 = 1	-	16	WP		101
9.10	INT LOGIC VER	Pb	-	-	1 = 1	-	32	WP		101
9.20	OPTION SLOT 1	INT32	0…18	-	1 = 1	-	16	WP		101
9.21	OPTION SLOT 2	INT32	0…18	-	1 = 1	-	16	WP		101
9.22	OPTION SLOT 3	INT32	0…18	-	1 = 1	-	16	WP		102

Parameter groups 10…99

Index	Parameter	Type	Range	Unit	FbEq	Update time	Data len.	Def	PT	Save PF	Page no.
10	START/STOP										
10.01	EXT1 START FUNC	enum	0…6	-	-	2 ms	16	1	WPD		104
10.02	EXT1 START IN1	Bit pointer		-		2 ms	32	P.02.01.00	WPD		105
10.03	EXT1 START IN2	Bit pointer		-		2 ms	32	C.False	WPD		105
10.04	EXT2 START FUNC	enum	0…6	-	-	2 ms	16	1	WPD		105
10.05	EXT2 START IN1	Bit pointer		-		2 ms	32	P.02.01.00	WPD		106
10.06	EXT2 START IN2	Bit pointer		-		2 ms	32	C.False	WPD		106
10.07	JOG1 START	Bit pointer		-		2 ms	32	C.False	WPD		106
10.08	FAULT RESET SEL	Bit pointer		-		2 ms	32	P.02.01.02			106
10.09	RUN ENABLE	Bit pointer		-		2 ms	32	C.True	WPD		106
10.10	EM STOP OFF3	Bit pointer		-		2 ms	32	C.True	WPD		106
10.11	EM STOP OFF1	Bit pointer		-		2 ms	32	C.True	WPD		107
10.12	START INHIBIT	enum	0…1	-	1 = 1	2 ms	16	0			107
10.13	FB CW USED	Val pointer		-		2 ms	32	P.02.12	WPD		107
10.14	JOG2 START	Bit pointer		-		2 ms	32	C.False	WPD		107
10.15	JOG ENABLE	Bit pointer		-		2 ms	32	C.False	WPD		107
10.16	D2D CW USED	Val pointer		-		2 ms	32	P.02.17	WPD		107
10.17	START ENABLE	Bit pointer		-		2 ms	32	C.True	WPD		108
11	START/STOP MODE										
11.01	START MODE	enum	0…2	-	1 = 1	-	16	1	WPD		109
11.02	DC MAGN TIME	UINT32	0…10000	ms	1 = 1	-	16	500	WPD		110
11.03	STOP MODE	enum	1…2	-	1 = 1	2 ms	16	2			110
11.04	DC HOLD SPEED	REAL	0…1000	rpm	1 = 10	2 ms	16	5			110
11.05	DC HOLD CUR REF	UINT32	0…100	%	1 = 1	2 ms	16	30			110
11.06	DC HOLD	enum	0…1	-	1 = 1	2 ms	16	0			111
11.07	AUTOPHASING MODE	enum	0…2	-	1 = 1	-	16	1			111
12	DIGITAL IO										
12.01	DIO1 CONF	enum	0…1	-	1 = 1	10 ms	16	0			112
12.02	DIO2 CONF	enum	0…2	-	1 = 1	10 ms	16	0			113
12.03	DIO3 CONF	enum	0…3	-	1 = 1	10 ms	16	0			113
12.04	DIO1 OUT PTR	Bit pointer		-		10 ms	32	P.06.02.02			113
12.05	DIO2 OUT PTR	Bit pointer		-		10 ms	32	P.06.02.03			113
12.06	DIO3 OUT PTR	Bit pointer		-		10 ms	32	P.06.01.10			113
12.07	DIO3 F OUT PTR	Val pointer		-		10 ms	32	P.01.01			113
12.08	DIO3 F MAX	REAL	3…32768	Hz	1 = 1	10 ms	16	1000			113
12.09	DIO3 F MIN	REAL	3…32768	Hz	1 = 1	10 ms	16	3			114
12.10	DIO3 F MAX SCALE	REAL	0…32768	-	1 = 1	10 ms	16	1500			114
12.11	DIO3 F MIN SCALE	REAL	0…32768	-	1 = 1	10 ms	16	0			114
12.12	RO1 OUT PTR	Bit pointer		-		10 ms	32	P.03.15.00			114
12.13	DI INVERT MASK	UINT32	0…63	-	1 = 1	10 ms	16	0			115
12.14	DIO2 F MAX	REAL	3…32768	Hz	1 = 1	10 ms	16	1000			115
12.15	DIO2 F MIN	REAL	3…32768	Hz	1 = 1	10 ms	16	3			115

Index	Parameter	Type	Range	Unit	FbEq	Update time	Data len.	Def	PT	Save PF	Page no.
12.16	DIO2 F MAX SCALE	REAL	-32768… 32768	-	1 = 1	10 ms	16	1500			115
12.17	DIO2 F MIN SCALE	REAL	-32768… 32768	-	1 = 1	10 ms	16	0			115
13	ANALOGUE INPUTS										
13.01	AI1 FILT TIME	REAL	0…30	s	1 = 1000	10 ms	16	0			116
13.02	AI1 MAX	REAL	-11…11/ -22…22	V or mA	1 = 1000	10 ms	16	10			116
13.03	AI1 MIN	REAL	-11…11/ -22…22	V or mA	1 = 1000	10 ms	16	-10			117
13.04	AI1 MAX SCALE	REAL	-32768… 32767	-	1 = 1000	10 ms	32	1500			117
13.05	AI1 MIN SCALE	REAL	-32768… 32767	-	1 = 1000	10 ms	32	-1500			117
13.06	AI2 FILT TIME	REAL	0…30	s	1 = 1000	10 ms	16	0			117
13.07	AI2 MAX	REAL	-11…11/ -22…22	V or mA	1 = 1000	10 ms	16	10			117
13.08	AI2 MIN	REAL	-11…11/ -22…22	V or mA	1 = 1000	10 ms	16	-10			118
13.09	AI2 MAX SCALE	REAL	-32768… 32767	-	1 = 1000	10 ms	32	100			118
13.10	AI2 MIN SCALE	REAL	-32768… 32767	-	1 = 1000	10 ms	32	-100			118
13.11	AITUNE	enum	0…4	-	1 = 1	10 ms	16	0			118
13.12	AI SUPERVISION	enum	0…3	-	1 = 1	2 ms	16	0			119
13.13	AI SUPERVIS ACT	UINT32	0000… 1111	-	1 = 1	2 ms	32	0			119
15	ANALOGUE OUTPUTS										
15.01	AO1 PTR	Val pointer		-		-	32	P.01.05			120
15.02	AO1 FILT TIME	REAL	0…30	s	1 = 1000	10 ms	16	0.1			120
15.03	AO1 MAX	REAL	0…22.7	mA	1 = 1000	10 ms	16	20			120
15.04	AO1 MIN	REAL	0…22.7	mA	1 = 1000	10 ms	16	4			121
15.05	AO1 MAX SCALE	REAL	-32768… 32767	-	1 = 1000	10 ms	32	100			121
15.06	AO1 MIN SCALE	REAL	-32768… 32767	-	1 = 1000	10 ms	32	0			121
15.07	AO2 PTR	Val pointer		-		-	32	P.01.02			121
15.08	AO2 FILT TIME	REAL	0…30	s	1 = 1000	10 ms	16	0.1			121
15.09	AO2 MAX	REAL	-10…10	V	1 = 1000	10 ms	16	10			122
15.10	AO2 MIN	REAL	-10…10	V	1 = 1000	10 ms	16	-10			122
15.11	AO2 MAX SCALE	REAL	-32768… 32767	-	1 = 1000	10 ms	32	100			122
15.12	AO2 MIN SCALE	REAL	-32768… 32767	-	1 = 1000	10 ms	32	-100			122
16	SYSTEM										
16.01	LOCAL LOCK	Bit pointer		-		2 ms	32	C.False			123
16.02	PARAMETER LOCK	enum	0…2	-	1 = 1	2 ms	16	1			123

Index	Parameter	Type	Range	Unit	FbEq	Update time	Data len.	Def	PT	Save PF	Page no.
16.03	PASS CODE	INT32	$0…2^{31}-1$	-	1 = 1	-	32	0			123
16.04	PARAM RESTORE	enum	0…2	-	1 = 1	-	16	0	WPD		123
16.07	PARAM SAVE	enum	0…1	-	1 = 1	-	16	0			123
16.09	USER SET SEL	enum	1…10	-	1 = 1	-	32	1	WPD		124
16.10	USER SET LOG	Pb	0…0x7FF	-	1 = 1	-	32	0	WP		124
16.11	USER IO SET LO	Bit pointer		-		-	32	C.False			125
16.12	USER IO SET HI	Bit pointer		-		-	32	C.False			125
16.13	TIME SOURCE PRIO	enum	0…8	-	1 = 1	-	16	0			125
17	PANEL DISPLAY										
17.01	SIGNAL1 PARAM	INT32	00.00… 255.255	-	1 = 1		16	01.03			126
17.02	SIGNAL2 PARAM	INT32	00.00… 255.255	-	1 = 1		16	01.04			126
17.03	SIGNAL3 PARAM	INT32	00.00… 255.255	-	1 = 1		16	01.06			126
20	LIMITS										
20.01	MAXIMUM SPEED	REAL	0…30000	rpm	1 = 1	2 ms	32	1500			127
20.02	MINIMUM SPEED	REAL	-30000…0	rpm	1 = 1	2 ms	32	-1500			127
20.03	POS SPEED ENA	Bit pointer		-		2 ms	32	C.True			128
20.04	NEG SPEED ENA	Bit pointer		-		2 ms	32	C.True			128
20.05	MAXIMUM CURRENT	REAL	0…30000	A	1 = 100	10 ms	32	-			128
20.06	MAXIMUM TORQUE	REAL	0…1600	%	1 = 10	2 ms	16	300			128
20.07	MINIMUM TORQUE	REAL	-1600…0	%	1 = 10	2 ms	16	-300			128
20.08	THERM CURR LIM	enum	0…1	-	1 = 1	-	16	1			129
22	SPEED FEEDBACK										
22.01	SPEED FB SEL	enum	0…2	-	1 = 1	10 ms	16	0			131
22.02	SPEED ACT FTIME	REAL	0…10000	ms	1 = 1000	10 ms	32	3			131
22.03	MOTOR GEAR MUL	INT32	$-2^{31}…2^{31}-1$	-	1 = 1	10 ms	32	1			132
22.04	MOTOR GEAR DIV	UINT32	$1…2^{31}-1$	-	1 = 1	10 ms	32	1			132
22.05	ZERO SPEED LIMIT	REAL	0…30000	rpm	1 = 1000	2 ms	32	30			132
22.06	ZERO SPEED DELAY	UINT32	0…30000	ms	1 = 1	2 ms	16	0			132
22.07	ABOVE SPEED LIM	REAL	0…30000	rpm	1 = 1	2 ms	16	0			133
22.08	SPEED TRIPMARGIN	REAL	0…10000	rpm	1 = 10	2 ms	32	500			133
22.09	SPEED FB FAULT	enum	0…2	-	1 = 1	10 ms	16	0			133
24	SPEED REF MOD										
24.01	SPEED REF1 SEL	enum	0…8	-	1 = 1	10 ms	16	1			135
24.02	SPEED REF2 SEL	enum	0…8	-	1 = 1	10 ms	16	0			136
24.03	SPEED REF1 IN	Val pointer		-		10 ms	32	P.03.01			136
24.04	SPEED REF2 IN	Val pointer		-		10 ms	32	P.03.02			136
24.05	SPEED REF 1/2SEL	Bit pointer		-		2 ms	32	C.False			136
24.06	SPEED SHARE	REAL	-8…8	-	1 = 1000	2 ms	16	1			136
24.07	SPEEDREF NEG ENA	Bit pointer		-		2 ms	32	C.False			137
24.08	CONST SPEED	REAL	-30000…. 30000	rpm	1 = 1	2 ms	16	0			137

Index	Parameter	Type	Range	Unit	FbEq	Update time	Data len.	Def	PT	Save PF	Page no.
24.09	CONST SPEED ENA	Bit pointer		-		2 ms	32	C.False			137
24.10	SPEED REF JOG1	REAL	-30000…. 30000	rpm	1 = 1	2 ms	16	0			137
24.11	SPEED REF JOG2	REAL	-30000…. 30000	rpm	1 = 1	2 ms	16	0			137
24.12	SPEED REFMIN ABS	REAL	0…30000	rpm	1 = 1	2 ms	16	0			137
25	SPEED REF RAMP										
25.01	SPEED RAMP IN	Val pointer		-		10 ms	32	P.03.03	WP		139
25.02	SPEED SCALING	REAL	0…30000	rpm	1 = 1	10 ms	16	1500			139
25.03	ACC TIME	REAL	0…1800	s	1 = 1000	10 ms	32	1			139
25.04	DEC TIME	REAL	0…1800	s	1 = 1000	10 ms	32	1			140
25.05	SHAPE TIME ACC1	REAL	0…1000	s	1 = 1000	10 ms	32	0			140
25.06	SHAPE TIME ACC2	REAL	0…1000	s	1 = 1000	10 ms	32	0			140
25.07	SHAPE TIME DEC1	REAL	0…1000	s	1 = 1000	10 ms	32	0			140
25.08	SHAPE TIME DEC2	REAL	0…1000	s	1 = 1000	10 ms	32	0			141
25.09	ACC TIME JOGGING	REAL	0…1800	s	1 = 1000	10 ms	32	0			141
25.10	DEC TIME JOGGING	REAL	0…1800	s	1 = 1000	10 ms	32	0			141
25.11	EM STOP TIME	REAL	0…1800	s	1 = 1000	10 ms	32	1			141
25.12	SPEEDREF BAL	REAL	-30000… 30000	rpm	1 = 1000	2 ms	32	0			141
25.13	SPEEDREF BAL ENA	Bit pointer		-		2 ms	32	C.False			141
26	SPEED ERROR										
26.01	SPEED ACT NCTRL	Val pointer		-		2 ms	32	P.01.01	WP		143
26.02	SPEED REF NCTRL	Val pointer		-		2 ms	32	P.03.04	WP		143
26.03	SPEED REF PCTRL	Val pointer		-		2 ms	32	P.04.01			143
26.04	SPEED FEED PCTRL	Val pointer		-		2 ms	32	P.04.20			144
26.05	SPEED STEP	REAL	-30000… 30000	rpm	1 = 100	2 ms	32	0			144
26.06	SPD ERR FTIME	REAL	0…1000	ms	1 = 10	2 ms	16	0			144
26.07	SPEED WINDOW	REAL	0…30000	rpm	1 = 1	250 µs	16	100			144
26.08	ACC COMP DERTIME	REAL	0…600	s	1 = 100	2 ms	32	0			145
26.09	ACC COMP FTIME	REAL	0…1000	ms	1 = 10	2 ms	16	8			145
26.10	SPEED WIN FUNC	UINT32	0…2	-	1 = 1	250 µs	16	0			145
26.11	SPEED WIN HI	REAL	0…3000	rpm	1 = 1	250 µs	16	0		x	146
26.12	SPEED WIN LO	REAL	0…3000	rpm	1 = 1	250 µs	16	0		x	146
28	SPEED CONTROL										
28.01	SPEED ERR NCTRL	Val pointer		-		2 ms	32	P.03.06	WP		148
28.02	PROPORT GAIN	REAL	0…200	-	1 = 100	2 ms	16	10			148
28.03	INTEGRATION TIME	REAL	0…600	s	1 = 1000	2 ms	32	0.5			149
28.04	DERIVATION TIME	REAL	0…10	s	1 = 1000	2 ms	16	0			149
28.05	DERIV FILT TIME	REAL	0…1000	ms	1 = 10	2 ms	16	8			150
28.06	ACC COMPENSATION	Val pointer		-		2 ms	32	P.03.07	WP		150
28.07	DROOPING RATE	REAL	0…100	%	1 = 100	2 ms	16	0			150

Index	Parameter	Type	Range	Unit	FbEq	Update time	Data len.	Def	PT	Save PF	Page no.
28.08	BAL REFERENCE	REAL	-1600… 1600	%	1 = 10	2 ms	16	0			150
28.09	SPEEDCTRL BAL EN	Bit pointer		-		2 ms	32	C.False			150
28.10	MIN TORQ SP CTRL	REAL	-1600… 1600	%	1 = 10	2 ms	16	-300			151
28.11	MAX TORQ SP CTRL	REAL	-1600… 1600	%	1 = 10	2 ms	16	300			151
28.12	PI ADAPT MAX SPD	REAL	0…30000	rpm	1 = 1	10 ms	16	0			151
28.13	PI ADAPT MIN SPD	REAL	0…30000	rpm	1 = 1	10 ms	16	0			151
28.14	P GAIN ADPT COEF	REAL	0…10	-	1 = 1000	10 ms	16	0			151
28.15	I TIME ADPT COEF	REAL	0…10	-	1 = 1000	10 ms	16	0			152
32	TORQUE REFERENCE										
32.01	TORQ REF1 SEL	enum	0…4	-	1 = 1	10 ms	16	2			153
32.02	TORQ REF ADD SEL	enum	0…4	-	1 = 1	10 ms	16	0			154
32.03	TORQ REF IN	Val pointer		-		250 µs	32	P.03.09			154
32.04	MAXIMUM TORQ REF	REAL	0…1000	%	1 = 10	250 µs	16	300			155
32.05	MINIMUM TORQ REF	REAL	-1000…0	%	1 = 10	250 µs	16	-300			155
32.06	LOAD SHARE	REAL	-8…8	-	1 = 1000	250 µs	16	1			155
32.07	TORQ RAMP UP	UINT32	0…60	s	1 = 1000	10 ms	32	0			155
32.08	TORQ RAMP DOWN	UINT32	0…60	s	1 = 1000	10 ms	32	0			155
33	SUPERVISION										
33.01	SUPERV1 FUNC	UINT32	0…4	-	1 = 1	2 ms	16	0			156
33.02	SUPERV1 ACT	Val pointer		-		2 ms	32	P.01.01			156
33.03	SUPERV1 LIM HI	REAL	-32768… 32768	-	1 = 100	2 ms	32	0			156
33.04	SUPERV1 LIM LO	REAL	-32768… 32768	-	1 = 100	2 ms	32	0			157
33.05	SUPERV2 FUNC	UINT32	0…4	-	1 = 1	2 ms	16	0			157
33.06	SUPERV2 ACT	Val pointer		-		2 ms	32	P.01.04			157
33.07	SUPERV2 LIM HI	REAL	-32768… 32768	-	1 = 100	2 ms	32	0			157
33.08	SUPERV2 LIM LO	REAL	-32768… 32768	-	1 = 100	2 ms	32	0			157
33.09	SUPERV3 FUNC	UINT32	0…4	-	1 = 1	2 ms	16	0			157
33.10	SUPERV3 ACT	Val pointer		-		2 ms	32	P.01.06			158
33.11	SUPERV3 LIM HI	REAL	-32768… 32768	-	1 = 100	2 ms	32	0			158
33.12	SUPERV3 LIM LO	REAL	-32768… 32768	-	1 = 100	2 ms	32	0			158
34	REFERENCE CTRL										
34.01	EXT1/EXT2 SEL	Bit pointer		-		2 ms	32	P.02.01.01			160
34.02	EXT1 MODE 1/2SEL	Bit pointer		-		2 ms	32	C.False (P.02.01.05 for pos. appl.)			160

Index	Parameter	Type	Range	Unit	FbEq	Update time	Data len.	Def	PT	Save PF	Page no.
34.03	EXT1 CTRL MODE1	enum	1…5 (1…9 for pos. appl.)	-	1 = 1	2 ms	16	1			160
34.04	EXT1 CTRL MODE2	enum	1…5 (1…9 for pos. appl.)	-	1 = 1	2 ms	16	2 (8 for pos. appl.)			161
34.05	EXT2 CTRL MODE1	enum	1…5 (1…9 for pos. appl.)	-	1 = 1	2 ms	16	2 (6 for pos. appl.)			161
34.07	LOCAL CTRL MODE	enum	1…2 (1…6 for pos. appl.)	-	1 = 1	2 ms	16	1	WPD		161
34.08	TREF SPEED SRC	Val pointer		-		250 µs	32	P.03.08	WP		162
34.09	TREF TORQ SRC	Val pointer		-		250 µs	32	P.03.11	WP		162
34.10	TORQ REF ADD SRC	Val pointer		-		250 µs	32	P.03.12	WP		162
35	MECH BRAKE CTRL										
35.01	BRAKE CONTROL	enum	0…2	-	1 = 1	2 ms	16	0	WPD		163
35.02	BRAKE ACKNOWL	Bit pointer		-		2 ms	32	C.False	WPD		163
35.03	BRAKE OPEN DELAY	UINT32	0…5	s	1 = 100	2 ms	16	0			164
35.04	BRAKE CLOSE DLY	UINT32	0…60	s	1 = 100	2 ms	16	0			164
35.05	BRAKE CLOSE SPD	REAL	0…1000	rpm	1 = 10	2 ms	16	100			164
35.06	BRAKE OPEN TORQ	REAL	0…1000	%	1 = 10	2 ms	16	0			164
35.07	BRAKE CLOSE REQ	Bit pointer		-		2 ms	32	C.False	WPD		164
35.08	BRAKE OPEN HOLD	Bit pointer		-		2 ms	32	C.False	WPD		164
35.09	BRAKE FAULT FUNC	enum	0…2	-	1 = 1	2 ms	16	0			164
40	MOTOR CONTROL										
40.01	FLUX REF	REAL	0…200	%	1 = 1	10 ms	16	100			166
40.02	SF REF	enum	0…16	kHz	1 = 1	-	16	4			166
40.03	SLIP GAIN	REAL	0…200	%	1 = 1	-		100			167
40.04	VOLTAGE RESERVE	REAL		V/%	1 = 1	-		-			167
40.05	FLUX OPT	enum	0…1	-	1 = 1	-		-			167
40.06	FORCE OPEN LOOP	enum	0…1	-	1 = 1	250 µs	16	0			167
40.07	IR COMPENSATION	REAL24	0…50	%	1 = 100	2 ms	32	0			167
45	MOT THERM PROT										
45.01	MOT TEMP PROT	enum	0…2	-	1 = 1	10 ms	16	0			169
45.02	MOT TEMP SOURCE	enum	0…6	-	1 = 1	10 ms	16	0			169
45.03	MOT TEMP ALM LIM	INT32	0…200	°C	1 = 1	-	16	90			170
45.04	MOT TEMP FLT LIM	INT32	0…200	°C	1 = 1	-	16	110			170
45.05	AMBIENT TEMP	INT32	-60…100	°C	1 = 1	-	16	20			171
45.06	MOT LOAD CURVE	INT32	50…150	%	1 = 1	-	16	100			171
45.07	ZERO SPEED LOAD	INT32	50…150	%	1 = 1	-	16	100			171
45.08	BREAK POINT	INT32	0.01…500	Hz	1 = 100	-	16	45			171
45.09	MOTNOMTEMPRISE	INT32	0…300	°C	1 = 1	-	16	80			172
45.10	MOT THERM TIME	INT32	100…10000	s	1 = 1	-	16	256			172
46	FAULT FUNCTIONS										
46.01	EXTERNAL FAULT	Bit pointer		-		2 ms	32	C.True			173

Index	Parameter	Type	Range	Unit	FbEq	Update time	Data len.	Def	PT	Save PF	Page no.
46.02	SPEED REF SAFE	REAL	-30000… 30000	rpm	1 = 1	2 ms	16	0			173
46.03	LOCAL CTRL LOSS	enum	0…3	-	1 = 1	-	16	1			173
46.04	MOT PHASE LOSS	enum	0…1	-	1 = 1	2 ms	16	1			174
46.05	EARTH FAULT	enum	0…2	-	1 = 1	-	16	2			174
46.06	SUPPL PHS LOSS	enum	0…1	-	1 = 1	2 ms	16	1			174
46.07	STO DIAGNOSTIC	enum	1…3	-	1 = 1	10 ms	16	1			174
46.08	CROSS CONNECTION	enum	0…1	-	1 = 1	-	16	1			175
47	VOLTAGE CTRL										
47.01	OVERVOLTAGE CTRL	enum	0…1	-	1 = 1	10 ms	16	1			176
47.02	UNDERVOLT CTRL	enum	0…1	-	1 = 1	10 ms	16	1			176
47.03	SUPPLVOLTAUTO-ID	enum	0…1	-	1 = 1	10 ms	16	1			176
47.04	SUPPLY VOLTAGE	REAL	0…1000	V	1 = 10	2 ms	16	400			177
48	BRAKE CHOPPER										
48.01	BC ENABLE	enum	0…2	-	1 = 1	-	16	0			178
48.02	BC RUN-TIME ENA	Bit pointer		-		2 ms	32	C.True			178
48.03	BRTHERMTIMECON ST	REAL24	0…10000	s	1 = 1	-	32	0			178
48.04	BR POWER MAX CNT	REAL24	0…10000	kW	1 = 10000	-	32	0			178
48.05	R BR	REAL24	0.1…1000	ohm	1 = 10000	-	32	-			179
48.06	BR TEMP FAULTLIM	REAL24	0…150	%	1 = 1	-	16	105			179
48.07	BR TEMP ALARMLIM	REAL24	0…150	%	1 = 1	-	16	95			179
50	FIELDBUS										
50.01	FBA ENABLE	enum	0…1	-	1 = 1	-	16	0			180
50.02	COMM LOSS FUNC	enum	0…3	-	1 = 1	-	16	0			180
50.03	COMM LOSS T OUT	UINT32	0.3…6553.5	s	1 = 10	-	16	0.3			181
50.04	FBA REF1 MODESEL	enum	0…2 (0…4 for pos. appl.)	-	1 = 1	10 ms	16	2			181
50.05	FBA REF2 MODESEL	enum	0…2 (0…4 for pos. appl.)	-	1 = 1	10 ms	16	3			181
50.06	FBA ACT1 TR SRC	Val pointer		-		10 ms	32	P.01.01			182
50.07	FBA ACT2 TR SRC	Val pointer		-		10 ms	32	P.01.06			182
50.08	FBA SW B12 SRC	Bit pointer		-		500 µs	32	C.False			182
50.09	FBA SW B13 SRC	Bit pointer		-		500 µs	32	C.False			182
50.10	FBA SW B14 SRC	Bit pointer		-		500 µs	32	C.False			182
50.11	FBA SW B15 SRC	Bit pointer		-		500 µs	32	C.False			182
51	FBA SETTINGS										
51.01	FBA TYPE	UINT32	0…65536	-	1 = 1		16	0			183
51.02	FBA PAR2	UINT32	0…65536	-	1 = 1		16	0		x	183
…	…	…	…	…	…		….	…			
51.26	FBA PAR26	UINT32	0…65536	-	1 = 1		16	0		x	183

Index	Parameter	Type	Range	Unit	FbEq	Update time	Data len.	Def	PT	Save PF	Page no.
51.27	FBA PAR REFRESH	UINT32	0…1	-	1 = 1		16	0	WPD	x	183
51.28	PAR TABLE VER	UINT32	0…65536	-	1 = 1		16	0		x	183
51.29	DRIVE TYPE CODE	UINT32	0…65536	-	1 = 1		16	0		x	184
51.30	MAPPING FILE VER	UINT32	0…65536	-	1 = 1		16	0		x	184
51.31	D2FBA COMM STA	UINT32	0…6	-	1 = 1		16	0		x	184
51.32	FBA COMM SW VER	UINT32	0…65536	-	1 = 1		16	0		x	184
51.33	FBA APPL SW VER	UINT32	0…65536	-	1 = 1		16	0		x	184
52	FBA DATA IN										
52.01	FBA DATA IN1	UINT32	0…9999	-	1 = 1		16	0		x	185
…	…	…	…	…	…		…	…			-
52.12	FBA DATA IN12	UINT32	0…9999	-	1 = 1		16	0		x	185
53	FBA DATA OUT										
53.01	FBA DATA OUT1	UINT32	0…9999	-	1 = 1		16	0		x	186
…	…	…	…	…	…		…	…			
53.12	FBA DATA OUT12	UINT32	0…9999	-	1 = 1		16	0		x	186
57	D2D COMMUNICATION										
57.01	LINK MODE	UINT32	0…2	-	1 = 1	10 ms	16	0	WPD		187
57.02	COMM LOSS FUNC	UINT32	0…2	-	1 = 1	10 ms	16	1			187
57.03	NODE ADDRESS	UINT32	1…62	-	1 = 1	10 ms	16	1	WPD		188
57.04	FOLLOWER MASK 1	UINT32	0…2^{31}	-	1 = 1	10 ms	32	0	WPD		188
57.05	FOLLOWER MASK 2	UINT32	0…2^{31}	-	1 = 1	10 ms	32	0	WPD		188
57.06	REF 1 SRC	Val pointer		-		10 ms	32	P.03.04			188
57.07	REF 2 SRC	Val pointer		-		10 ms	32	P.03.13			188
57.08	FOLLOWER CW SRC	Val pointer		-		10 ms	32	P.02.18			188
57.09	KERNEL SYNC MODE	enum	0…3	-	1 = 1	10 ms	16	0	WPD		188
57.10	KERNEL SYNC OFFS	REAL	-4999… 5000	ms	1 = 1	10 ms	16	0	WPD		189
57.11	REF 1 MSG TYPE	UINT32	0…1	-	1 = 1	10 ms	16	0			189
57.12	REF1 MC GROUP	UINT32	0…62	-	1 = 1	10 ms	16	0			189
57.13	NEXT REF1 MC GRP	UINT32	0…62	-	1 = 1	10 ms	16	0			189
57.14	NR REF1 MC GRPS	UINT32	1…62	-	1 = 1	10 ms	16	1			190
57.15	D2D COMM PORT	UINT32	0…3	-	1 = 1		16	0	WPD		190
60	POS FEEDBACK										
60.01	POS ACT SEL	enum	0…1	-	1 = 1	10 ms	16	0			192
60.02	POS AXIS MODE	enum	0…1	-	1 = 1	2 ms	16	0	WPD		192
60.03	LOAD GEAR MUL	INT32	$-2^{31}…2^{31}-1$	-	1 = 1	2 ms	32	1			193
60.04	LOAD GEAR DIV	UINT32	$1…2^{31}-1$	-	1 = 1	2 ms	32	1			193
60.05	POS UNIT	enum	0…3	-	1 = 1	10 ms	16	0			193
60.06	FEED CONST MUL	UINT32	$1…2^{31}-1$	-	1 = 1	10 ms	32	1			193
60.07	FEED CONST DEN	UINT32	$1…2^{31}-1$	-	1 = 1	10 ms	32	1			193
60.08	POS2INT SCALE	enum	1…1000000	-	1 = 1	10 ms	32	1000			194
60.09	POS RESOLUTION	enum	10…24	-	1 = 1	10 ms	16	16	WPD		194

Index	Parameter	Type	Range	Unit	FbEq	Update time	Data len.	Def	PT	Save PF	Page no.
60.10	POS SPEED UNIT	enum	0…2	-	1 = 1	10 ms	16	0			194
60.11	POS SPEED2INT	enum	1…1000000	-	1 = 1	10 ms	32	1000			194
60.12	POS SPEED SCALE	REAL	0…32768	-	1 = 10000	10 ms	32	1			194
60.13	MAXIMUM POS	REAL	0…32768	*	See 60.09	2 ms	32	32768			195
60.14	MINIMUM POS	REAL	-32768…0	*	See 60.09	2 ms	32	-32768			195
60.15	POS THRESHOLD	REAL	-32768… 32768	*	See 60.09	2 ms	32	0			195
62	POS CORRECTION										
62.01	HOMING METHOD	UINT32	0…35	-	1 = 1	10 ms	16	0			196
62.02	HOMING STARTFUNC	enum	0…1	-	1 = 1	10 ms	16	0			197
62.03	HOMING START	Bit pointer	-	-		10 ms	32	P.02.01.05			197
62.04	HOME SWITCH TRIG	enum	0…3	-	1 = 1	10 ms	16	0			197
62.05	NEG LIMIT SWITCH	Bit pointer	-	-		10 ms	32	C.False			197
62.06	POS LIMIT SWITCH	Bit pointer	-	-		10 ms	32	C.False			197
62.07	HOMING SPEEDREF1	REAL	0…32768	**	See 60.10	10 ms	32	1			197
62.08	HOMING SPEEDREF2	REAL	0…32768	**	See 60.10	10 ms	32	0.25			197
62.09	HOME POSITION	REAL	-32768… 32768	*	See 60.09	10 ms	32	0			198
62.10	HOME POS OFFSET	REAL	-32768… 32768	*	See 60.09	10 ms	32	0			198
62.11	PRESET MODE	enum	0…3	-	1 = 1	10 ms	16	0			198
62.12	PRESET TRIG	enum	0…12	-	1 = 1	10 ms	16	0			198
62.13	PRESET POSITION	REAL	-32768… 32768	*	See 60.09	10 ms	32	0			199
62.14	CYCLIC CORR MODE	enum	0…5	-	1 = 1	10 ms	16	0			199
62.15	TRIG PROBE1	enum	0…28	-	1 = 1	10 ms	16	0			200
62.16	PROBE1 POS	REAL	-32768… 32768	*	See 60.09	10 ms	32	0			201
62.17	TRIG PROBE2	enum	0…28	-	1 = 1	10 ms	16	0			201
62.18	PROBE2 POS	REAL	-32768… 32768	*	See 60.09	10 ms	32	0			201
62.19	MAX CORRECTION	REAL	0…32768	*	See 60.09	10 ms	32	50			201
62.20	POS ACT OFFSET	REAL	-32768… 32768	*	See 60.09		32	0			202
62.21	POS COR MODE	enum	0…1	-	1 = 1	10 ms	16	0			202
65	PROFILE REFERENCE										
65.01	POS REFSOURCE	enum	0…2	-	1 = 1	2 ms	16	0			204
65.02	PROF SET SEL	Bit pointer	-	-	-	2 ms	32	P.02.01.04			205
65.03	POS START 1	Bit pointer	-	-	-	2 ms	32	P.02.01.03			205
65.04	POS REF 1 SEL	enum	0…8	-	1 = 1	2 ms	16	7			205
65.05	POS SPEED 1	REAL	0…32768	**	See 60.10	2 ms	32	5			205
65.06	PROF ACC 1	REAL	0…32768	**	See 60.10	2 ms	32	10			205

Index	Parameter	Type	Range	Unit	FbEq	Update time	Data len.	Def	PT	Save PF	Page no.
65.07	PROF DEC 1	REAL	-32768…0	**	See 60.10	2 ms	32	-10			205
65.08	PROF FILT TIME 1	REAL	0…1000	ms	1 = 1	2 ms	16	0			205
65.09	POS STYLE 1	UINT32	0…0xFFFF	-	1 = 1	2 ms	16	20			206
65.10	POS END SPEED 1	REAL	-32768…32768	**	See 60.10	2 ms	32	0			208
65.11	POS START 2	Bit pointer	-	-	-	2 ms	32	P.02.01.03			208
65.12	POS REF 2 SEL	enum	0…8	-	1 = 1	2 ms	32	8			208
65.13	POS SPEED 2	REAL	0…32768	**	See 60.10	2 ms	32	5			208
65.14	PROF ACC 2	REAL	0…32768	**	See 60.10	2 ms	32	10			208
65.15	PROF DEC 2	REAL	-32768…0	**	See 60.10	2 ms	32	-10			208
65.16	PROF FILT TIME 2	REAL	0…1000	ms	1 = 1	2 ms	16	0			208
65.17	POS STYLE 2	UINT32	0…0xFFFF	-	1 = 1	2 ms	16	20			208
65.18	POS END SPEED 2	REAL	-32768…32768	**	See 60.10	2 ms	32	0			209
65.19	POS REF 1	REAL	-32760…32760	*	See 60.09	2 ms	32	0			209
65.20	POS REF 2	REAL	-32760…32760	*	See 60.09	2 ms	32	0			209
65.21	POS REF ADD SEL	enum	0…8	-	1 = 1	2 ms	16	0			209
65.22	PROF VEL REF SEL	enum	0…7	-	1 = 1	2 ms	16	7			209
65.23	PROF VEL REF1	REAL	-32768…32768	**	See 60.10	500 µs	32	0			210
65.24	POS START MODE	enum	0…1	-	1 = 1	2 ms	16	0			210
66	PROFILE GENERATOR										
66.01	PROF GENERAT IN	Val pointer	-	-	-	10 ms	32	P.04.06	WP		212
66.02	PROF SPEED MUL	REAL	0…1	-	1 = 1000	500 µs	32	1			212
66.03	PROF ACC WEAK SP	REAL	0…32768	**	See 60.10	10 ms	32	32768			213
66.04	POS WIN	REAL	0…32768	*	See 60.09	500 µs	32	0.1			213
66.05	POS ENABLE	Bit pointer	-	-	-	500 µs	32	C.True			213
67	SYNC REF SEL										
67.01	SYNC REF SEL	enum	0…9	-	1 = 1	10 ms	16	8			214
67.02	SPEED REF VIRT M	enum	0…8	-	1 = 1	10 ms	16	0			215
67.03	INTERPOLAT MODE	enum	0…1	-	1 = 1	10 ms	16	0			215
67.04	INTERPOLAT CYCLE	UINT32	1…10000	ms	1 = 1	10 ms	16	1			216
68	SYNC REF MOD										
68.01	SYNC GEAR IN	Val pointer	-	-	-	10 ms	32	P.04.15			217
68.02	SYNC GEAR MUL	INT32	$-2^{31}…2^{31}-1$	-	1 = 1	10 ms	32	1			218
68.03	SYNC GEAR DIV	UINT32	$1…2^{31}-1$	-	1 = 1	10 ms	32	1			218
68.04	SYNC GEAR ADD	REAL	-30…30	-	1 = 1000	500 µs	32	1			218
68.05	SYNC REF FTIME	REAL	0…1000	ms	1 = 1	10 ms	16	0			218
68.06	SYNCFILT DLY LIM	REAL	0…0.4	*	See 60.09	10 ms	32	0			218
68.07	SYNCHRON MODE	enum	0…1	-	1 = 1	2 ms	16	1			218
70	POS REF LIMIT										
70.01	POS REF PROFILE	Val pointer	-	-	-	500 µs	32	P.04.13			220

Index	Parameter	Type	Range	Unit	FbEq	Update time	Data len.	Def	PT	Save PF	Page no.
70.02	POS REF SYNC	Val pointer	-	-	-	500 µs	32	P.04.16			220
70.03	POS REF ENA	Bit pointer	-	-	-	500 µs	32	C.True			220
70.04	POS SPEED LIM	REAL	0…32768	**	See 60.10	2 ms	32	32768			220
70.05	POS ACCEL LIM	REAL	0…32768	**	See 60.10	2 ms	32	32768			220
70.06	POS DECEL LIM	REAL	-32768…0	**	See 60.10	2 ms	32	-32768			220
70.07	SYNC ERR LIM	REAL	0…32768	*	See 60.09	500 µs	32	32768			220
70.08	SYNC VEL WINDOW	REAL	0…32768	**	See 60.10	2 ms	32	2			220
71	POSITION CTRL										
71.01	POS ACT IN	Val pointer	-	-	-	500 µs	32	P.01.12	WP		222
71.02	POS CTRL REF IN	Val pointer	-	-	-	500 µs	32	P.04.17			222
71.03	POS CTRL GAIN	REAL	0…10000	1/s	1 = 100	500 µs	32	10			222
71.04	P CTRL FEED GAIN	REAL	0…10	-	1 = 100	500 µs	16	1			222
71.05	POS CTRL DELAY	UINT32	0…15	-	1 = 1	2 ms	16	0			223
71.06	POS ERR LIM	REAL	0…32768	*	See 60.09	500 µs	32	32768			223
71.07	GEAR RATIO MUL	INT32	$-2^{31}…2^{31}-1$	-	1 = 1	10 ms	32	1			223
71.08	GEAR RATIO DIV	UINT32	$1…2^{31}-1$	-	1 = 1	10 ms	32	1			223
71.09	FOLLOW ERR WIN	REAL	0…32768	*	See 60.09	500 µs	32	32768			223
90	ENC MODULE SEL										
90.01	ENCODER 1 SEL	enum	0…6	-	1 = 1		16	0			225
90.02	ENCODER 2 SEL	enum	0…6	-	1 = 1		16	0			226
90.03	EMUL MODE SEL	enum	0…9	-	1 = 1		16	0			226
90.04	TTL ECHO SEL	enum	0…4	-	1 = 1		16	0			227
90.05	ENC CABLE FAULT	UINT32	0…2	-	1 = 1		16	1			227
90.10	ENC PAR REFRESH	UINT32	0…1	-	1 = 1		16	0	WPD		228
91	ABSOL ENC CONF										
91.01	SINE COSINE NR	UINT32	0…65535	-	1 = 1		16	0			229
91.02	ABS ENC INTERF	UINT32	0…4	-	1 = 1		16	0			230
91.03	REV COUNT BITS	UINT32	0…32	-	1 = 1		16	0			230
91.04	POS DATA BITS	UINT32	0…32	-	1 = 1		16	0			230
91.05	REFMARK ENA	UINT32	0…1	-	1 = 1		16	0			230
91.10	HIPERFACE PARITY	UINT32	0…1	-	1 = 1		16	0			230
91.11	HIPERF BAUDRATE	UINT32	0…3	-	1 = 1		16	1			230
91.12	HIPERF NODE ADDR	UINT32	0…255	-	1 = 1		16	64			231
91.20	SSI CLOCK CYCLES	UINT32	2…127	-	1 = 1		16	2			231
91.21	SSI POSITION MSB	UINT32	1…126	-	1 = 1		16	1			231
91.22	SSI REVOL MSB	UINT32	1…126	-	1 = 1		16	1			231
91.23	SSI DATA FORMAT	UINT32	0…1	-	1 = 1		16	0			231
91.24	SSI BAUD RATE	UINT32	0…5	-	1 = 1		16	2			231
91.25	SSI MODE	UINT32	0…1	-	1 = 1		16	0			232
91.26	SSI TRANSMIT CYC	UINT32	0…5	-	1 = 1		16	1			232
91.27	SSI ZERO PHASE	UINT32	0…3	-	1 = 1		16	0			232
91.30	ENDAT MODE	UINT32	0…1	-	1 = 1		16	0			232
91.31	ENDAT MAX CALC	UINT32	0…3	-	1 = 1		16	3			233

Index	Parameter	Type	Range	Unit	FbEq	Update time	Data len.	Def	PT	Save PF	Page no.
92	RESOLVER CONF										
92.01	RESOLV POLEPAIRS	UINT32	1…32	-	1 = 1		16	1			234
92.02	EXC SIGNAL AMPL	UINT32	4…12	Vrms	1 = 10		16	4			234
92.03	EXC SIGNAL FREQ	UINT32	1…20	kHz	1 = 1		16	1			234
93	PULSE ENC CONF										
93.01	ENC1 PULSE NR	UINT32	0…65535	-	1 = 1		16	0			235
93.02	ENC1 TYPE	enum	0…1	-	1 = 1		16	0			235
93.03	ENC1 SP CALCMODE	enum	0….5	-	1 = 1		16	4			235
93.04	ENC1 POS EST ENA	enum	0…1	-	1 = 1		16	1			236
93.05	ENC1 SP EST ENA	enum	0…1	-	1 = 1		16	0			236
93.06	ENC1 OSC LIM	enum	0…3	-	1 = 1		16	0			236
93.11	ENC2 PULSE NR	UINT32	0…65535	-	1 = 1		16	0			237
93.12	ENC2 TYPE	enum	0…1	-	1 = 1		16	0			237
93.13	ENC2 SP CALCMODE	enum	0….5	-	1 = 1		16	4			237
93.14	ENC2 POS EST ENA	enum	0…1	-	1 = 1		16	1			237
93.15	ENC2 SP EST ENA	enum	0…1	-	1 = 1		16	0			237
93.16	ENC2 OSC LIM	enum	0…3	-	1 = 1		16	0			237
93.21	EMUL PULSE NR	UINT32	0…65535	-	1 = 1		16	0			237
93.22	EMUL POS REF	Val pointer		-			32	P.01.12 (P.04.17 for pos. appl.)			237
95	HW CONFIGURATION										
95.01	CTRL UNIT SUPPLY	enum	0…1	-	1 = 1		16	0			238
95.02	EXTERNAL CHOKE	enum	0…1	-	1 = 1		16	0			238
97	USER MOTOR PAR										
97.01	USE GIVEN PARAMS	enum	0…1	-	1 = 1		16	0	WPD		239
97.02	RS USER	REAL24	0…0.5	p.u.	1 = 100000		32	0			239
97.03	RR USER	REAL24	0…0.5	p.u.	1 = 100000		32	0			239
97.04	LM USER	REAL24	0…10	p.u.	1 = 100000		32	0			239
97.05	SIGMAL USER	REAL24	0…1	p.u.	1 = 100000		32	0			239
97.06	LD USER	REAL24	0…10	p.u.	1 = 100000		32	0			239
97.07	LQ USER	REAL24	0…10	p.u.	1 = 100000		32	0			240
97.08	PM FLUX USER	REAL24	0…2	p.u.	1 = 100000		32	0			240
97.09	RS USER SI	REAL24	0…100	ohm	1 = 100000		32	0			240
97.10	RR USER SI	REAL24	0…100	ohm	1 = 100000		32	0			240
97.11	LM USER SI	REAL24	0…100000	mH	1 = 100000		32	0			240
97.12	SIGL USER SI	REAL24	0…100000	mH	1 = 100000		32	0			240
97.13	LD USER SI	REAL24	0…100000	mH	1 = 100000		32	0			240
97.14	LQ USER SI	REAL24	0…100000	mH	1 = 100000		32	0			240
98	MOTOR CALC VALUES										
98.01	TORQ NOM SCALE	UINT32	0…2147483	Nm	1 = 1000		32	0	WP		241

Index	Parameter	Type	Range	Unit	FbEq	Update time	Data len.	Def	PT	Save PF	Page no.
98.02	POLEPAIRS	UINT32	0…1000	-	1 = 1		16	0	WP		241
99	START-UP DATA										
99.01	LANGUAGE	enum		-	1 = 1		16				242
99.04	MOTOR TYPE	enum	0…1	-	1 = 1		16	0	WPD		242
99.05	MOTOR CTRL MODE	enum	0…1	-	1 = 1		16	0			243
99.06	MOT NOM CURRENT	REAL	0…6400	A	1 = 10		32	0	WPD		243
99.07	MOT NOM VOLTAGE	REAL	120…960	V	1 = 10		32	0	WPD		243
99.08	MOT NOM FREQ	REAL	0…500	Hz	1 = 10		32	0	WPD		244
99.09	MOT NOM SPEED	REAL	0…30000	rpm	1 = 1		32	0	WPD		244
99.10	MOT NOM POWER	REAL	0…10000	kW	1 = 100		32	0	WPD		244
99.11	MOT NOM COSFII	REAL24	0…1	-	1 = 100		32	0	WPD		244
99.12	MOT NOM TORQUE	INT32	0…2147483	Nm	1 = 1000		32	0	WPD		244
99.13	IDRUN MODE	enum	0…5	-	1 = 1		16	0	WPD		245

* The unit depends on parameter 60.05 POS UNIT selection.

** The unit depends on parameter 60.05 POS UNIT and 60.10 POS SPEED UNIT selections.

Fault tracing

What this chapter contains

The chapter lists all alarm and fault messages including the possible cause and corrective actions.

Safety

WARNING! Only qualified electricians are allowed to maintain the drive. The *Safety Instructions* on the first pages of the appropriate hardware manual must be read before you start working with the drive.

Alarm and fault indications

An alarm or a fault message indicates abnormal drive status. Most alarm and fault causes can be identified and corrected using this information. If not, an ABB representative should be contacted.

The four-digit code number in brackets after the message is for the fieldbus communication.

The alarm/fault code is displayed on the 7-segment display of the drive. The following table describes the indications given by the 7-segment display.

Display	Meaning
"E-" followed by error code	System error. See appropriate drive hardware manual.
"A-" followed by error code	Alarm. See section *Alarm messages generated by the drive* on page 269.
"F-" followed by error code	Fault. See section *Fault messages generated by the drive* on page 276.

How to reset

The drive can be reset either by pressing the reset key on the PC tool ( ) or control panel (***RESET***) or switching the supply voltage off for a while. When the fault has been removed, the motor can be restarted.

A fault can also be reset from an external source by parameter 10.08 FAULT RESET SEL.

Fault history

When fault is detected, it is stored in the fault logger with a time stamp. The fault history stores information on the 16 latest faults of the drive. Three of the latest faults are stored at the beginning of a power switch off.

Signals 8.01 ACTIVE FAULT and 8.02 LAST FAULT store the fault codes of the most recent faults.

Alarms can be monitored via alarm words 8.05 ALARM WORD 1 … 8.08 ALARM WORD 4. Alarm information is lost at power switch off or fault reset.

Alarm messages generated by the drive

Code	Alarm (fieldbus code)	Cause	What to do
2000	BRAKE START TORQUE (0x7185) Programmable fault: 35.09 BRAKE FAULT FUNC	Mechanical brake alarm. Alarm is activated if required motor starting torque, 35.06 BRAKE OPEN TORQ, is not achieved.	Check brake open torque setting, parameter 35.06. Check drive torque and current limits. See firmware block LIMITS on page 127.
2001	BRAKE NOT CLOSED (0x7186) Programmable fault: 35.09 BRAKE FAULT FUNC	Mechanical brake control alarm. Alarm is activated e.g. if brake acknowledgement is not as expected during brake closing.	Check mechanical brake connection. Check mechanical brake settings, parameters 35.01…35.09. To determine whether problem is with acknowledgement signal or brake: Check if brake is closed or open.
2002	BRAKE NOT OPEN (0x7187) Programmable fault: 35.09 BRAKE FAULT FUNC	Mechanical brake control alarm. Alarm is activated e.g. if brake acknowledgement is not as expected during brake opening.	Check mechanical brake connection. Check mechanical brake settings, parameters 35.01…35.08. To determine whether problem is with acknowledgement signal or brake: Check if brake is closed or open.
2003	SAFE TORQUE OFF (0xFF7A) Programmable fault: 46.07 STO DIAGNOSTIC	Safe Torque Off function is active, i.e. safety circuit signal(s) connected to connector X6 is lost while drive is stopped and parameter 46.07 STO DIAGNOSTIC is set to (2) ALARM.	Check safety circuit connections. For more information, see appropriate drive hardware manual.
2004	STO MODE CHANGE (0xFF7A)	Error in changing Safe Torque Off supervision, i.e. parameter 46.07 STO DIAGNOSTIC setting could not be changed to value (2) ALARM.	Contact your local ABB representative.
2005	MOTOR TEMPERATURE (0x4310) Programmable fault: 45.01 MOT TEMP PROT	Estimated motor temperature (based on motor thermal model) has exceeded alarm limit defined by parameter 45.03 MOT TEMP ALM LIM.	Check motor ratings and load. Let motor cool down. Ensure proper motor cooling: Check cooling fan, clean cooling surfaces, etc. Check value of alarm limit. Check motor thermal model settings, parameters 45.06…45.08 and 45.10 MOT THERM TIME.
		Measured motor temperature has exceeded alarm limit defined by parameter 45.03 MOT TEMP ALM LIM.	Check that actual number of sensors corresponds to value set by parameter 45.02 MOT TEMP SOURCE. Check motor ratings and load. Let motor cool down. Ensure proper motor cooling: Check cooling fan, clean cooling surfaces, etc. Check value of alarm limit.

Code	Alarm (fieldbus code)	Cause	What to do
2006	EMERGENCY OFF (0xF083)	Drive has received emergency OFF2 command.	To restart drive, activate RUN ENABLE signal (source selected by parameter 10.09 RUN ENABLE) and start drive.
2007	RUN ENABLE (0xFF54)	No Run enable signal is received.	Check setting of parameter 10.09 RUN ENABLE. Switch signal on (e.g. in the fieldbus Control Word) or check wiring of selected source.
2008	ID-RUN (0xFF84)	Motor identification run is on.	This alarm belongs to normal start-up procedure. Wait until drive indicates that motor identification is completed.
		Motor identification is required.	This alarm belongs to normal start-up procedure. Select how motor identification should be performed, parameter 99.13 IDRUN MODE. Start identification routines by pressing Start key.
2009	EMERGENCY STOP (0xF081)	Drive has received emergency stop command (OFF1/OFF3).	Check that it is safe to continue operation. Return emergency stop push button to normal position (or adjust the fieldbus Control Word accordingly). Restart drive.
2010	POSITION SCALING (0x8584)	Overflow or underflow in position calculation (caused by used position scaling).	Check position scaling parameter settings: 60.06 FEED CONST NUM…60.09 POS RESOLUTION. Check speed scaling parameter settings: 60.11 POS SPEED2INT and 60.12 POS SPEED SCALE.
2011	BR OVERHEAT (0x7112)	Brake resistor temperature has exceeded alarm limit defined by parameter 48.07 BR TEMP ALARMLIM.	Stop drive. Let resistor cool down. Check resistor overload protection function settings, parameters 48.01…48.05. Check alarm limit setting, parameter 48.07. Check that braking cycle meets allowed limits.
2012	BC OVERHEAT (0x7181)	Brake chopper IGBT temperature has exceeded internal alarm limit.	Let chopper cool down. Check resistor overload protection function settings, parameters 48.01…48.05. Check that braking cycle meets allowed limits. Check that drive supply AC voltage is not excessive.
2013	DEVICE OVERTEMP (0x4210)	Measured drive temperature has exceeded internal alarm limit.	Check ambient conditions. Check air flow and fan operation. Check heatsink fins for dust pick-up. Check motor power against unit power.

Code	Alarm (fieldbus code)	Cause	What to do
2014	INTBOARD OVERTEMP (0x7182)	Interface board (between power unit and control unit) temperature has exceeded internal alarm limit.	Let drive cool down.
2015	BC MOD OVERTEMP (0x7183)	Input bridge or brake chopper temperature has exceeded internal alarm limit.	Let drive cool down.
2016	IGBT OVERTEMP (0x7184)	Drive temperature based on thermal model has exceeded internal alarm limit.	Check ambient conditions. Check air flow and fan operation. Check heatsink fins for dust pick-up. Check motor power against unit power.
2017	FIELDBUS COMM (0x7510) Programmable fault: 50.02 COMM LOSS FUNC	Cyclical communication between drive and fieldbus adapter module or between PLC and fieldbus adapter module is lost.	Check status of fieldbus communication. See appropriate User's Manual of fieldbus adapter module. Check fieldbus parameter settings. See parameter group 50 FIELDBUS on page 180. Check cable connections. Check if communication master can communicate.
2018	LOCAL CTRL LOSS (0x5300) Programmable fault: 46.03 LOCAL CTRL LOSS	Control panel or PC tool selected as active control location for drive has ceased communicating.	Check PC tool or control panel connection. Check control panel connector. Replace control panel in mounting platform.
2019	AI SUPERVISION (0x8110) Programmable fault: 13.12 AI SUPERVISION	Analogue input AI1 or AI2 signal has reached limit defined by parameter 13.13 AI SUPERVIS ACT.	Check analogue input AI1/2 source and connections. Check analogue input AI1/2 minimum and maximum limit settings, parameters 13.02 and 13.03 / 13.07 and 13.08.
2020	FB PAR CONF (0x6320)	The drive does not have a functionality requested by PLC, or requested functionality has not been activated.	Check PLC programming. Check fieldbus parameter settings. See parameter group 50 FIELDBUS on page 180.
2021	NO MOTOR DATA (0x6381)	Parameters in group 99 have not been set.	Check that all the required parameters in group 99 have been set.
2022	ENCODER 1 FAILURE (0x7301)	Encoder 1 has been activated by parameter but the encoder interface (FEN-xx) cannot be found.	Check parameter 90.01 ENCODER 1 SEL setting corresponds to encoder interface 1 (FEN-xx) installed in drive Slot 1/2 (signal 9.20 OPTION SLOT 1 / 9.21 OPTION SLOT 2). **Note:** The new setting will only take effect after parameter 90.10 ENC PAR REFRESH is used or after the JCU control unit is powered up the next time.

Code	Alarm (fieldbus code)	Cause	What to do
2023	ENCODER 2 FAILURE (0x7381)	Encoder 2 has been activated by parameter but the encoder interface (FEN-xx) cannot be found.	Check parameter 90.02 ENCODER 2 SEL setting corresponds to encoder interface 2 (FEN-xx) installed in drive Slot 1/2 (signal 9.20 OPTION SLOT 1 / 9.21 OPTION SLOT 2). **Note:** The new setting will only take effect after parameter 90.10 ENC PAR REFRESH is used or after the JCU control unit is powered up the next time.
		EnDat or SSI encoder is used in continuous mode as encoder 2. [I.e. 90.02 ENCODER 2 SEL = (3) FEN-11 ABS and 91.02 ABS ENC INTERF = (2) ENDAT or (4) SSI) and 91.30 ENDAT MODE = (1) CONTINUOUS (or 91.25 SSI MODE = (1) CONTINUOUS).]	If possible, use single position transfer instead of continuous position transfer (i.e. if encoder has incremental sin/cos signals): - Change parameter 91.25 SSI MODE / 91.30 ENDAT MODE to value (0) INITIAL POS.. Otherwise use EnDat/SSI encoder as encoder 1: - Change parameter 90.01 ENCODER 1 SEL to value (3) FEN-11 ABS and parameter 90.02 ENCODER 2 SEL to value (0) NONE. **Note:** The new setting will only take effect after parameter 90.10 ENC PAR REFRESH is used or after the JCU control unit is powered up the next time.
2024	LATCH POS 1 FAILURE (0x7382)	Position latch 1 from encoder 1 or 2 has failed.	Check latch source parameter settings: 62.04 HOME SWITCH TRIG, 62.12 PRESET TRIG, 62.15 TRIG PROBE1 and 62.17 TRIG PROBE2. Note that zero pulse is not always supported. * Check that appropriate encoder interface 1/2 is activated by parameter 90.10 ENCODER 1 SEL / 90.02 ENCODER 2 SEL. **Note:** The new setting will only take effect after parameter 90.10 ENC PAR REFRESH is used or after the JCU control unit is powered up the next time. * - Zero pulse is supported when TTL input of encoder interface module is selected (i.e. par. 90.01/90.02 = (1) FEN-01 TTL+, (2) FEN-01 TTL, (4) FEN-11 TTL or (6) FEN-21 TTL. - Zero pulse is supported when absolute encoder input of encoder interface module is selected and zero pulse is enabled (i.e. 90.01/90.02 = (3) FEN-11 ABS and 91.02 = (0) NONE / (1) COMMUT SIG and 91.05 = (1) TRUE). - Zero pulse is not supported when resolver input is selected (i.e. 90.01/90.02 = (5) FEN-21 RES).

Code	Alarm (fieldbus code)	Cause	What to do
2025	LATCH POS 2 FAILURE (0x7383)	Position latch 2 from encoder 1 or 2 has failed.	See alarm LATCH POS 1 FAILURE.
2026	ENC EMULATION FAILURE (0x7384)	Encoder emulation error	If position value used in emulation is measured by encoder: - Check that FEN-xx encoder used in emulation (90.03 EMUL MODE SEL) corresponds to FEN-xx encoder interface 1 or (and) 2 activated by parameter 90.01 ENCODER 1 SEL / 90.02 ENCODER 2 SEL. (Parameter 90.01/90.02 activates the position calculation of the used FEN-xx input). If position value used in emulation is determined by drive software: - Check that FEN-xx encoder used in emulation (90.03 EMUL MODE SEL) corresponds to FEN-xx encoder interface 1 or (and) 2 activated by parameter 90.01 ENCODER 1 SEL / 90.02 ENCODER 2 SEL (because position data used in emulation is written to FEN-xx during encoder data request). Encoder interface 2 is recommended. **Note:** The new setting will only take effect after parameter 90.10 ENC PAR REFRESH is used or after the JCU control unit is powered up the next time.
2027	FEN TEMP MEAS FAILURE (0x7385)	Error in temperature measurement when temperature sensor (KTY or PTC) connected to encoder interface FEN-xx is used.	Check that parameter 45.02 MOT TEMP SOURCE setting corresponds to encoder interface installation (9.20 OPTION SLOT 1 / 9.21 OPTION SLOT 2): If one FEN-xx module is used: - Parameter 45.02 MOT TEMP SOURCE setting must be either (2) KTY 1st FEN or (5) PTC 1st FEN. FEN-xx module can be in either Slot 1 or Slot 2. If two FEN-xx modules are used: - When parameter 45.02 MOT TEMP SOURCE setting is (2) KTY 1st FEN or (5) PTC 1st FEN, the encoder installed in drive Slot 1 is used. - When parameter 45.02 MOT TEMP SOURCE setting is (3) KTY 2nd FEN or (6) PTC 2nd FEN, the encoder installed in drive Slot 2 is used.
		Error in temperature measurement when KTY sensor connected to encoder interface FEN-01 is used.	FEN-01 does not support temperature measurement with KTY sensor. Use PTC sensor or other encoder interface module.

Code	Alarm (fieldbus code)	Cause	What to do
2028	ENC EMUL MAX FREQ (0x7386)	TTL pulse frequency used in encoder emulation.exceeds maximum allowed limit (500 kHz).	Decrease parameter 93.21 EMUL PULSE NR value. **Note:** The new setting will only take effect after parameter 90.10 ENC PAR REFRESH is used or after the JCU control unit is powered up the next time.
2029	ENC EMUL REF ERROR (0x7387)	Encoder emulation has failed due to failure in writing new (position) reference for emulation.	Contact your local ABB representative.
2030	RESOLVER AUTOTUNE ERR (0x7388)	Resolver autotuning routines, which are automatically started when resolver input is activated for the first time, have failed.	Check cable between resolver and resolver interface module (FEN-21) and order of connector signal wires at both ends of cable. Check resolver parameter settings. For resolver parameters and information, see parameter group 92 RESOLVER CONF on page 234. **Note:** Resolver autotuning routines should always be performed after resolver cable connection has been modified. Autotuning routines can be activated by setting parameter 92.02 EXC SIGNAL AMPL or 92.03 EXC SIGNAL FREQ, and then setting parameter 90.10 ENC PAR REFRESH to (1) CONFIGURE.
2031	ENCODER 1 CABLE (0x7389)	Encoder 1 cable fault detected.	Check cable between FEN-xx interface and encoder 1. After any modifications in cabling, re-configure interface by switching drive power off and on, or by activating parameter 90.10 ENC PAR REFRESH.
2032	ENCODER 2 CABLE (0x738A)	Encoder 2 cable fault detected.	Check cable between FEN-xx interface and encoder 2. After any modifications in cabling, re-configure interface by switching drive power off and on, or by activating parameter 90.10 ENC PAR REFRESH.
2033	D2D COMMUNICATION (0x7520) Programmable fault: 57.02 COMM LOSS FUNC	On the master drive: The drive has not been replied to by an activated follower for five consecutive polling cycles.	Check that all drives that are polled (parameters 57.04 and 57.05) on the drive-to-drive link are powered, properly connected to the link, and have the correct node address. Check the drive-to-drive link wiring.
		On a follower drive: The drive has not received new reference 1 and/or 2 for five consecutive reference handling cycles.	Check the settings of parameters 57.06 and 57.07 on the master drive. Check the drive-to-drive link wiring.

Code	Alarm (fieldbus code)	Cause	What to do
2034	D2D BUFFER OVERLOAD (0x7520) Programmable fault: 57.02 COMM LOSS FUNC	Transmission of drive-to-drive references failed because of message buffer overflow.	Contact your local ABB representative.
2035	PS COMM (0x5480)	Communication errors detected between the JCU Control Unit and the power unit of the drive.	Check the connections between the JCU Control Unit and the power unit.
2036	RESTORE (0x630D)	Restoration of backed-up parameters failed.	Contact your local ABB representative.
2037	CUR MEAS CALIBRATION (0x2280)	Current measurement calibration will occur at next start.	Informative alarm.
2038	AUTOPHASING (0x3187)	Autophasing will occur at next start.	Informative alarm.
2039	EARTH FAULT (0x2330) Programmable fault: 46.05 EARTH FAULT	Drive has detected load unbalance typically due to earth fault in motor or motor cable.	Check there are no power factor correction capacitors or surge absorbers in motor cable. Check that there is no earth fault in motor or motor cables: - measure insulation resistances of motor and motor cable. If no earth fault can be detected, contact your local ABB representative.
2041	MOTOR NOM VALUE (0x6383)	The motor configuration parameters are set incorrectly.	Check the settings of the motor configuration parameters in group 99 START-UP DATA.
		The drive is not dimensioned correctly.	Check that the drive is sized correctly for the motor.
2042	D2D CONFIG (0x7583)	The settings of drive-to-drive link configuration parameters (group 57) are incompatible.	Check the settings of the parameters in group 57 D2D COMMUNICATION.
2047	SPEED FEEDBACK (0x8480)	No speed feedback is received.	Check the settings of the parameters in group 22 SPEED FEEDBACK. Check encoder installation. See the description of fault 0039 (ENCODER1) for more information.
2048	OPTION COMM LOSS (0x7000)	Communication between drive and option module (FEN-xx and/or FIO-xx) is lost.	Check that option modules are properly connected to Slot 1 and (or) Slot 2. Check that option modules or Slot 1/2 connectors are not damaged. To determine whether module or connector is damaged: Test each module individually in Slot 1 and Slot 2.

Fault messages generated by the drive

Code	Fault (fieldbus code)	Cause	What to do
0001	OVERCURRENT (0x2310)	Output current has exceeded internal fault limit.	Check motor load. Check acceleration time. See parameter group 25 SPEED REF RAMP on page 139. Check motor and motor cable (including phasing and delta/star connection). Check that the start-up data in parameter group 99 corresponds to the motor rating plate. Check that there are no power factor correction capacitors or surge absorbers in motor cable. Check encoder cable (including phasing).
0002	DC OVERVOLTAGE (0x3210)	Excessive intermediate circuit DC voltage.	Check that overvoltage controller is on, parameter 47.01 OVERVOLTAGE CTRL. Check mains for static or transient overvoltage. Check brake chopper and resistor (if used). Check deceleration time. Use coast-to-stop function (if applicable). Retrofit frequency converter with brake chopper and brake resistor.
0003	DEVICE OVERTEMP (0x4210)	Measured drive temperature has exceeded internal fault limit.	Check ambient conditions. Check air flow and fan operation. Check heatsink fins for dust pick-up. Check motor power against unit power.
0004	SHORT CIRCUIT (0x2340)	Short-circuit in motor cable(s) or motor.	Check motor and motor cable. Check there are no power factor correction capacitors or surge absorbers in motor cable.
0005	DC UNDERVOLTAGE (0x3220)	Intermediate circuit DC voltage is not sufficient due to missing mains phase, blown fuse or rectifier bridge internal fault.	Check mains supply and fuses.
0006	EARTH FAULT (0x2330) Programmable fault: 46.05 EARTH FAULT	Drive has detected load unbalance typically due to earth fault in motor or motor cable.	Check there are no power factor correction capacitors or surge absorbers in motor cable. Check that there is no earth fault in motor or motor cables: - measure insulation resistances of motor and motor cable. If no earth fault can be detected, contact your local ABB representative.
0007	FAN FAULT (0xFF83)	Fan is not able to rotate freely or fan is disconnected. Fan operation is monitored by measuring fan current.	Check fan operation and connection.

Code	Fault (fieldbus code)	Cause	What to do
0008	IGBT OVERTEMP (0x7184)	Drive temperature based on thermal model has exceeded internal fault limit.	Check ambient conditions. Check air flow and fan operation. Check heatsink fins for dust pick-up. Check motor power against unit power.
0009	BC WIRING (0x7111)	Brake resistor short circuit or brake chopper control fault.	Check brake chopper and brake resistor connection. Ensure brake resistor is not damaged.
0010	BC SHORT CIRCUIT (0x7113)	Short circuit in brake chopper IGBT.	Ensure brake resistor is connected and not damaged.
0011	BC OVERHEAT (0x7181)	Brake chopper IGBT temperature has exceeded internal fault limit.	Let chopper cool down. Check resistor overload protection function settings, parameters 48.03…48.05. Check that braking cycle meets allowed limits. Check that drive supply AC voltage is not excessive.
0012	BR OVERHEAT (0x7112)	Brake resistor temperature has exceeded fault limit defined by parameter 48.06 BR TEMP FAULTLIM.	Stop drive. Let resistor cool down. Check resistor overload protection function settings, parameters 48.01…48.05. Check fault limit setting, parameter 48.06. Check that braking cycle meets allowed limits.
0013	CURR MEAS GAIN (0x3183)	Difference between output phase U2 and W2 current measurement gain is too great.	Contact your local ABB representative.
0014	CABLE CROSS CON (0x3181) Programmable fault: 46.08 CROSS CONNECTION	Incorrect input power and motor cable connection (i.e. input power cable is connected to drive motor connection).	Check input power connections.
0015	SUPPLY PHASE (0x3130) Programmable fault: 46.06 SUPPL PHS LOSS	Intermediate circuit DC voltage is oscillating due to missing input power line phase or blown fuse.	Check input power line fuses. Check for input power supply imbalance.
0016	MOTOR PHASE (0x3182) Programmable fault: 46.04 MOT PHASE LOSS	Motor circuit fault due to missing motor connection (all three phases are not connected).	Connect motor cable.

Code	Fault (fieldbus code)	Cause	What to do
0017	ID-RUN FAULT (0xFF84)	Motor ID Run is not completed successfully.	Check the fault logger for a fault code extension. See appropriate actions for each extension below.
	Fault code extension: 1	The ID run cannot be completed because the maximum current setting and/or the internal current limit of the drive is too low.	Check setting of parameters 99.06 MOT NOM CURRENT and 20.05 MAXIMUM CURRENT. Make sure that 20.05 MAXIMUM CURRENT ≥ 99.06 MOT NOM CURRENT. Check that the drive is dimensioned correctly according to the motor.
	Fault code extension: 2	The ID run cannot be completed because the maximum speed setting and/or calculated field weakening point is too low.	Check setting of parameters 99.07 MOT NOM VOLTAGE, 99.08 MOT NOM FREQ, 99.09 MOT NOM SPEED, 20.01 MAXIMUM SPEED and 20.02 MINIMUM SPEED. Make sure that • 20.01 MAXIMUM SPEED > (0.55 × 99.09 MOT NOM SPEED), • 20.02 MINIMUM SPEED ≤ 0, and • supply voltage ≥ (0.65 × 99.07 MOT NOM VOLTAGE).
	Fault code extension: 3	The ID run cannot be completed because the maximum torque setting is too low.	Check setting of parameters 99.12 MOT NOM TORQUE and 20.06 MAXIMUM TORQUE. Make sure that 20.06 MAXIMUM TORQUE ≥ 100%.
	Fault code extension: 4…16	Internal error.	Contact your local ABB representative.
0018	CURR U2 MEAS (0x3184)	Measured offset error of U2 output phase current measurement is too great. (Offset value is updated during current calibration.)	Contact your local ABB representative.
0019	CURR V2 MEAS (0x3185)	Measured offset error of V2 output phase current measurement is too great. (Offset value is updated during current calibration.)	Contact your local ABB representative.
0020	CURR W2 MEAS (0x3186)	Measured offset error of W2 output phase current measurement is too great. (Offset value is updated during current calibration.)	Contact your local ABB representative.
0021	STO1 LOST (0x8182)	Safe Torque Off function is active, i.e. safety circuit signal 1 connected between X6:1 and X6:3 is lost while drive is at stopped state and parameter 46.07 STO DIAGNOSTIC setting is (2) ALARM or (3) NO.	Check safety circuit connections. For more information, see appropriate drive hardware manual.

Code	Fault (fieldbus code)	Cause	What to do
0022	STO2 LOST (0x8183)	Safe Torque Off function is active, i.e. safety circuit signal 2 connected between X6:2 and X6:4 is lost while drive is at stopped state and parameter 46.07 STO DIAGNOSTIC setting is (2) ALARM or (3) NO.	Check safety circuit connections. For more information, see appropriate drive hardware manual.
0023	STO MODE CHANGE (0xFF7A)	Error in changing Safe Torque Off supervision, i.e. parameter 46.07 STO DIAGNOSTIC setting could not be changed to value (1) FAULT.	Contact your local ABB representative.
0024	INTBOARD OVERTEMP (0x7182)	Interface board (between power unit and control unit) temperature has exceeded internal fault limit.	Let drive cool down.
0025	BC MOD OVERTEMP (0x7183)	Input bridge or brake chopper temperature has exceeded internal fault limit.	Let drive cool down.
0026	AUTOPHASING (0x3187)	Autophasing routine (see section *Autophasing* on page 40) failed.	Try other autophasing modes (see parameter 11.07 AUTOPHASING MODE) if possible.
0027	PU LOST (0x5400)	Connection between the JCU Control Unit and the power unit of the drive is lost.	Check the connections between the JCU Control Unit and the power unit.
0028	PS COMM (0x5480)	Communication errors detected between the JCU Control Unit and the power unit of the drive.	Check the connections between the JCU Control Unit and the power unit.
0029	IN CHOKE TEMP (0xFF81)	Temperature of internal AC choke excessive.	Check cooling fan.
0030	EXTERNAL (0x9000)	Fault in external device. (This information is configured through one of programmable digital inputs.)	Check external devices for faults. Check setting of parameter 46.01 EXTERNAL FAULT.
0031	SAFE TORQUE OFF (0xFF7A) Programmable fault: 46.07 STO DIAGNOSTIC	Safe Torque Off function is active, i.e. safety circuit signal(s) connected to connector X6 is lost - during drive start or drive run or - while drive is stopped and parameter 46.07 STO DIAGNOSTIC setting is (1) FAULT.	Check safety circuit connections. For more information, see appropriate drive hardware manual.

Code	Fault (fieldbus code)	Cause	What to do
0032	OVERSPEED (0x7310)	Motor is turning faster than highest allowed speed due to incorrectly set minimum/ maximum speed, insufficient braking torque or changes in load when using torque reference.	Check minimum/maximum speed settings, parameters 20.01 MAXIMUM SPEED and 20.02 MINIMUM SPEED. Check adequacy of motor braking torque. Check applicability of torque control. Check need for brake chopper and resistor(s).
0033	BRAKE START TORQUE (0x7185) Programmable fault: 35.09 BRAKE FAULT FUNC	Mechanical brake fault. Fault is activated if required motor starting torque, 35.06 BRAKE OPEN TORQ, is not achieved.	Check brake open torque setting, parameter 35.06. Check drive torque and current limits. See parameter group 20 LIMITS on page 127.
0034	BRAKE NOT CLOSED (0x7186) Programmable fault: 35.09 BRAKE FAULT FUNC	Mechanical brake control fault. Fault is activated if brake acknowledgement is not as expected during brake closing.	Check mechanical brake connection. Check mechanical brake settings, parameters 35.01…35.09. To determine whether problem is with acknowledgement signal or brake: Check if brake is closed or open.
0035	BRAKE NOT OPEN (0x7187) Programmable fault: 35.09 BRAKE FAULT FUNC	Mechanical brake control fault. Fault is activated if brake acknowledgement is not as expected during brake opening.	Check mechanical brake connection. Check mechanical brake settings, parameters 35.01…35.08. To determine whether problem is with acknowledgement signal or brake: Check if brake is closed or open.
0036	LOCAL CTRL LOSS (0x5300) Programmable fault: 46.03 LOCAL CTRL LOSS	Control panel or PC tool selected as active control location for drive has ceased communicating.	Check PC tool or control panel connection. Check control panel connector. Replace control panel in mounting platform.
0037	NVMEMCORRUPTED (0x6320)	Drive internal fault **Note:** This fault cannot be reset.	Contact your local ABB representative.
0038	OPTION COMM LOSS (0x7000)	Communication between drive and option module (FEN-xx and/or FIO-xx) is lost.	Check that option modules are properly connected to Slot 1 and (or) Slot 2. Check that option modules or Slot 1/2 connectors are not damaged. To determine whether module or connector is damaged: Test each module individually in Slot 1 and Slot 2.

Code	Fault (fieldbus code)	Cause	What to do
0039	ENCODER1 (0x7301)	Encoder 1 feedback fault	If fault appears during first start-up before encoder feedback is used: - Check cable between encoder and encoder interface module (FEN-xx) and order of connector signal wires at both ends of cable. If absolute encoder, EnDat/Hiperface/SSI, with incremental sin/cos pulses is used, incorrect wiring can be located as follows: Disable serial link (zero position) by setting parameter 91.02 ABS ENC INTERF to (0) NONE and test encoder operation: - If encoder fault is not activated, check serial link data wiring. Note that zero position is not taken into account when serial link is disabled. - If encoder fault is activated, check serial link and sin/cos signal wiring. **Note:** Because only zero position is requested through serial link and during run, position is updated according to sin/cos pulses. - Check encoder parameter settings. If fault appears after encoder feedback has already been used or during drive run: - Check that encoder connection wiring or encoder is not damaged. - Check that encoder interface module (FEN-xx) connection or module is not damaged. - Check earthings (when disturbances are detected in communication between encoder interface module and encoder). For more information on encoders, see parameter groups 90 ENC MODULE SEL (page 225), 91 ABSOL ENC CONF (page 229), 92 RESOLVER CONF (page 234) and 93 PULSE ENC CONF (page 235).

Code	Fault (fieldbus code)	Cause	What to do
0040	ENCODER2 (0x7381)	Encoder 2 feedback fault	See fault ENCODER1.
		EnDat or SSI encoder is used in continuous mode as encoder 2. [I.e. 90.02 ENCODER 2 SEL = (3) FEN-11 ABS and 91.02 ABS ENC INTERF = (2) ENDAT or (4) SSI and 91.30 ENDAT MODE = (1) CONTINUOUS (or 91.25 SSI MODE = (1) CONTINUOUS).]	If possible, use single position transfer instead of continuous position transfer (i.e. if encoder has incremental sin/cos signals): - Change parameter 91.25 SSI MODE / 91.30 ENDAT MODE to value (0) INITIAL POS.. Otherwise use Endat/SSI encoder as encoder 1: - Change parameter 90.01 ENCODER 1 SEL to value (3) FEN-11 ABS and parameter 90.02 ENCODER 2 SEL to value (0) NONE. **Note:** The new setting will only take effect after parameter 90.10 ENC PAR REFRESH is used or after the JCU control unit is powered up the next time.
0041	POSITION ERROR (0x8500)	Calculated position error, 4.19 POS ERROR, exceeds defined position error supervision window. Motor is stalled.	Check supervision window setting, parameter 71.06 POS ERR LIM. Check that no torque limit is exceeded during positioning.
0043	POSITION ERROR MIN (0x8582)	Actual position value exceeds defined minimum position value. Limit can be exceeded because no homing (or preset function) has been performed.	Check minimum position setting, parameter 60.14 MINIMUM POS. Perform homing (or preset function).
0044	POSITION ERROR MAX (0x8583)	Actual position value exceeds defined maximum position value. Limit can be exceeded because no homing (or preset function) has been performed.	Check maximum position setting, parameter 60.13 MAXIMUM POS. Perform homing (or preset function).
0045	FIELDBUS COMM (0x7510) Programmable fault: 50.02 COMM LOSS FUNC	Cyclical communication between drive and fieldbus adapter module or between PLC and fieldbus adapter module is lost.	Check status of fieldbus communication. See appropriate User's Manual of fieldbus adapter module. Check fieldbus parameter settings. See parameter group 50 FIELDBUS on page 180. Check cable connections. Check if communication master can communicate.
0046	FB MAPPING FILE (0x6306)	Drive internal fault	Contact your local ABB representative.

Code	Fault (fieldbus code)	Cause	What to do
0047	MOTOR OVERTEMP (0x4310) Programmable fault: 45.01 MOT TEMP PROT	Estimated motor temperature (based on motor thermal model) has exceeded fault limit defined by parameter 45.04 MOT TEMP FLT LIM.	Check motor ratings and load. Let motor cool down. Ensure proper motor cooling: Check cooling fan, clean cooling surfaces, etc. Check value of fault limit. Check motor thermal model settings, parameters 45.06…45.08 and 45.10 MOT THERM TIME.
		Measured motor temperature has exceeded fault limit defined by parameter 45.04 MOT TEMP FLT LIM.	Check that actual number of sensors corresponds to value set by parameter 45.02 MOT TEMP SOURCE. Check motor ratings and load. Let motor cool down. Ensure proper motor cooling: Check cooling fan, clean cooling surfaces, etc. Check value of fault limit.
0048	POS ACT MEAS (0x8584)	Selected operation mode requires position feedback data (actual position), but no feedback data is available.	Check actual position source setting, 60.01 POS ACT SEL. Check encoder installation. See ENCODER1 fault description for more information. (The used operation mode is indicated by signal 6.12 OP MODE ACK.)
0049	AI SUPERVISION (0x8110) Programmable fault: 13.12 AI SUPERVISION	Analogue input AI1 or AI2 signal has reached limit defined by parameter 13.13 AI SUPERVIS ACT.	Check analogue input AI1/2 source and connections. Check analogue input AI1/2 minimum and maximum limit settings, parameters 13.02 and 13.03 / 13.07 and 13.08.
0050	ENCODER 1 CABLE (0x7389) Programmable fault: 90.05 ENC CABLE FAULT	Encoder 1 cable fault detected.	Check cable between FEN-xx interface and encoder 1. After any modifications in cabling, re-configure interface by switching drive power off and on, or by activating parameter 90.10 ENC PAR REFRESH.
0051	ENCODER 2 CABLE (0x738A) Programmable fault: 90.05 ENC CABLE FAULT	Encoder 2 cable fault detected.	Check cable between FEN-xx interface and encoder 2. After any modifications in cabling, re-configure interface by switching drive power off and on, or by activating parameter 90.10 ENC PAR REFRESH.
0052	D2D CONFIG (0x7583)	Configuration of the drive-to-drive link has failed for a reason other than those indicated by alarm 2042, for example start inhibition is requested but not granted.	Contact your local ABB representative.

Code	Fault (fieldbus code)	Cause	What to do
0053	D2D COMM (0x7520) Programmable fault: 57.02 COMM LOSS FUNC	On the master drive: The drive has not been replied to by an activated follower for five consecutive polling cycles.	Check that all drives that are polled (parameters 57.04 FOLLOWER MASK 1 and 57.05 FOLLOWER MASK 2) on the drive-to-drive link are powered, properly connected to the link, and have the correct node address. Check the drive-to-drive link wiring.
		On a follower drive: The drive has not received new reference 1 and/or 2 for five consecutive reference handling cycles.	Check the settings of parameters 57.06 REF 1 SRC and 57.07 REF 2 SRC on the master drive. Check the drive-to-drive link wiring.
0054	D2D BUF OVLOAD (0x7520) Programmable fault: 57.02 COMM LOSS FUNC	Transmission of drive-to-drive references failed because of message buffer overflow.	Contact your local ABB representative.
0055	TECH LIB (0x6382)	Resettable fault generated by a technology library.	Refer to the documentation of the technology library.
0056	TECH LIB CRITICAL (0x6382)	Permanent fault generated by a technology library.	Refer to the documentation of the technology library.
0057	FORCED TRIP (0xFF90)	Generic Drive Communication Profile trip command.	Check PLC status.
0058	FIELDBUS PAR ERROR (0x6320)	The drive does not have a functionality requested by PLC, or requested functionality has not been activated.	Check PLC programming. Check fieldbus parameter settings. See parameter group 50 FIELDBUS on page 180.
0061	SPEED FEEDBACK (0x8480)	No speed feedback is received.	Check the settings of the parameters in group 22 SPEED FEEDBACK. Check encoder installation. See the description of fault 0039 (ENCODER1) for more information.
0062	D2D SLOT COMM (0x7584)	Drive-to-drive link is set to use an FMBA module for communication, but no module is detected in specified slot.	Check the settings of parameters 57.01 LINK MODE and 57.15 D2D COMM PORT. Ensure that the FMBA module has been detected by checking parameters 9.20…9.22. Check that the FMBA module is correctly wired. Try installing the FMBA module into another slot. If the problem persists, contact your local ABB representative.
0201	T2 OVERLOAD (0x0201)	Firmware time level 2 overload **Note:** This fault cannot be reset.	Contact your local ABB representative.
0202	T3 OVERLOAD (0x6100)	Firmware time level 3 overload **Note:** This fault cannot be reset.	Contact your local ABB representative.

Code	Fault (fieldbus code)	Cause	What to do
0203	T4 OVERLOAD (0x6100)	Firmware time level 4 overload **Note:** This fault cannot be reset.	Contact your local ABB representative.
0204	T5 OVERLOAD (0x6100)	Firmware time level 5 overload **Note:** This fault cannot be reset.	Contact your local ABB representative.
0205	A1 OVERLOAD (0x6100)	Application time level 1 fault **Note:** This fault cannot be reset.	Contact your local ABB representative.
0206	A2 OVERLOAD (0x6100)	Application time level 2 fault **Note:** This fault cannot be reset.	Contact your local ABB representative.
0207	A1 INIT FAULT (0x6100)	Application task creation fault **Note:** This fault cannot be reset.	Contact your local ABB representative.
0208	A2 INIT FAULT (0x6100)	Application task creation fault **Note:** This fault cannot be reset.	Contact your local ABB representative.
0209	STACK ERROR (0x6100)	Drive internal fault **Note:** This fault cannot be reset.	Contact your local ABB representative.
0210	FPGA ERROR (0xFF61)	Drive internal fault **Note:** This fault cannot be reset.	Contact your local ABB representative.
0301	UFF FILE READ (0x6300)	File read error **Note:** This fault cannot be reset.	Contact your local ABB representative.
0302	APPL DIR CREATION (0x6100)	Drive internal fault **Note:** This fault cannot be reset.	Contact your local ABB representative.
0303	FPGA CONFIG DIR (0x6100)	Drive internal fault **Note:** This fault cannot be reset.	Contact your local ABB representative.
0304	PU RATING ID (0x5483)	Drive internal fault **Note:** This fault cannot be reset.	Contact your local ABB representative.
0305	RATING DATABASE (0x6100)	Drive internal fault **Note:** This fault cannot be reset.	Contact your local ABB representative.
0306	LICENSING (0x6100)	Drive internal fault **Note:** This fault cannot be reset.	Contact your local ABB representative.

Code	Fault (fieldbus code)	Cause	What to do
0307	DEFAULT FILE (0x6100)	Drive internal fault **Note:** This fault cannot be reset.	Contact your local ABB representative.
0308	APPL FILE PAR CONF (0x6300)	Corrupted application file **Note:** This fault cannot be reset.	Reload application. If fault is still active, contact your local ABB representative.
0309	APPL LOADING (0x6300)	Corrupted application file **Note:** This fault cannot be reset.	Reload application. If fault is still active, contact your local ABB representative.
0310	USERSET LOAD (0xFF69)	Loading of user set is not successfully completed because: - requested user set does not exist - user set is not compatible with drive program - drive has been switched off during loading.	Reload.
0311	USERSET SAVE (0xFF69)	User set is not saved because of memory corruption.	Check the setting of parameter 95.01 CTRL UNIT SUPPLY. If the fault still occurs, contact your local ABB representative.
0312	UFF OVERSIZE (0x6300)	UFF file is too big.	Contact your local ABB representative.
0313	UFF EOF (0x6300)	UFF file structure failure	Delete faulty file or contact your local ABB representative.
0314	TECH LIB INTERFACE (0x6100)	Incompatible firmware interface **Note:** This fault cannot be reset.	Contact your local ABB representative.
0315	RESTORE FILE (0x630D)	Restoration of backed-up parameters failed.	Contact your local ABB representative.
0316	DAPS MISMATCH (0x5484)	Mismatch between JCU Control Unit firmware and power unit logic versions.	Contact your local ABB representative.
0317	SOLUTION FAULT (0x6200)	Fault generated by function block SOLUTION_FAULT in the application program.	Check the usage of the SOLUTION_FAULT block in the application program.

Standard function blocks

What this chapter contains

This chapter describes the standard function blocks. The blocks are grouped according to the grouping in the DriveSPC tool.

The number in brackets in the standard block heading is the block number.

Note: The given execution times can vary depending on the used drive application.

Terms

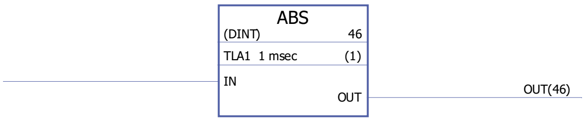
Data type	Description	Range
Boolean	Boolean	0 or 1
DINT	32-bit integer value (31 bits + sign)	-2147483648…2147483647
INT	16-bit integer value (15 bits + sign)	-32768…32767
PB	Packed Boolean	0 or 1 for each individual bit
REAL	16-bit value 16-bit value (31 bits + sign) = integer value = fractional value	-32768,99998…32767,9998
REAL24	8-bit value 24-bit value (31 bits + sign) = integer value = fractional value	-128,0…127,999

Arithmetic

ABS

(10001)

Illustration



Execution time 0.53 µs

Operation The output (OUT) is the absolute value of the input (IN).

OUT = | IN |

Inputs The input data type is selected by the user.

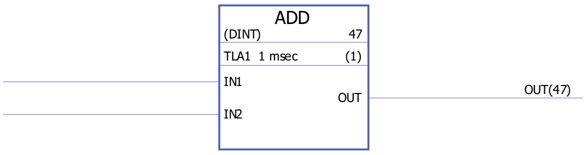
Input (IN): DINT, INT, REAL or REAL24

Outputs Output (OUT): DINT, INT, REAL or REAL24

ADD

(10000)

Illustration



Execution time 3.36 µs (when two inputs are used) + 0.52 µs (for every additional input). When all inputs are used, the execution time is 18.87 µs.

Operation The output (OUT) is the sum of the inputs (IN1…IN32).

OUT = IN1 + IN2 + … + IN32

The output value is limited to the maximum and minimum values defined by the selected data type range.

Inputs The input data type and the number of the inputs (2…23) are selected by the user.

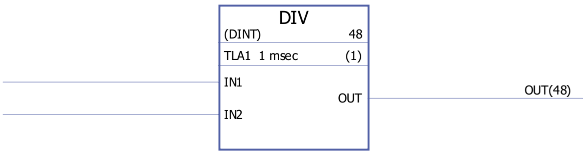
Input (IN1…IN32): DINT, INT, REAL or REAL24

Outputs Output (OUT): DINT, INT, REAL or REAL24

DIV

(10002)

Illustration



Execution time 2.55 µs

Operation The output (OUT) is input IN1 divided by input IN2.

OUT = IN1/IN2

The output value is limited to the maximum and minimum values defined by the selected data type range.

If the divider (IN2) is 0, the output is 0.

Inputs The input data type is selected by the user.

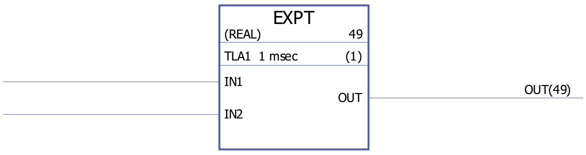
Input (IN1, IN2): INT, DINT, REAL, REAL24

Outputs Output (OUT): INT, DINT, REAL, REAL24

EXPT

(10003)

Illustration



Execution time 81.90 µs

Operation The output (OUT) is input IN1 raised to the power of the input IN2:

OUT = $IN1^{IN2}$

If input IN1 is 0, the output is 0.

The output value is limited to the maximum value defined by the selected data type range.

Note: The execution of the EXPT function is slow.

Inputs The input data type is selected by the user.

Input (IN1): REAL, REAL24

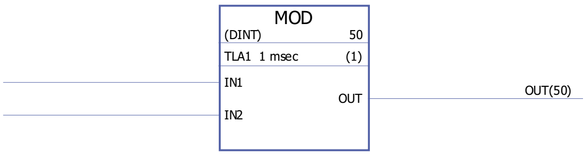
Input (IN2): REAL

Outputs Output (OUT): REAL, REAL24

MOD

(10004)

Illustration



Execution time 1.67 µs

Operation The output (OUT) is the remainder of the division of the inputs IN1 and IN2.

OUT = remainder of IN1/IN2

If input IN2 is zero, the output is zero.

Inputs The input data type is selected by the user.

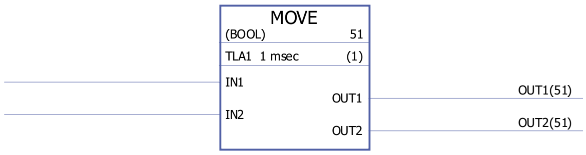
Input (IN1, IN2): INT, DINT

Outputs Output (OUT): INT, DINT

MOVE

(10005)

Illustration



Execution time 2.10 µs (when two inputs are used) + 0.42 µs (for every additional input). When all inputs are used, the execution time is 14.55 µs.

Operation Copies the input values (IN1…32) to the corresponding outputs (OUT1…32).

Inputs The input data type and number of inputs (2…32) are selected by the user.

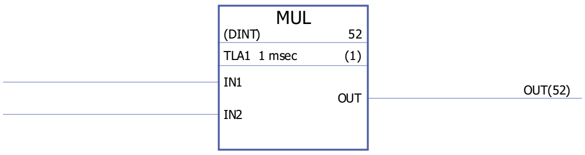
Input (IN1…IN32): INT, DINT, REAL, REAL24, Boolean

Outputs Output (OUT1…OUT32): INT, DINT, REAL, REAL24, Boolean

MUL

(10006)

Illustration



Execution time 3.47 µs (when two inputs are used) + 2.28 µs (for every additional input). When all inputs are used, the execution time is 71.73 µs.

Operation The output (OUT) is the product of the inputs (IN).

O = IN1 × IN2 × … × IN32

The output value is limited to the maximum and minimum values defined by the selected data type range.

Inputs The input data type and the number of inputs (2…32) are selected by the user.

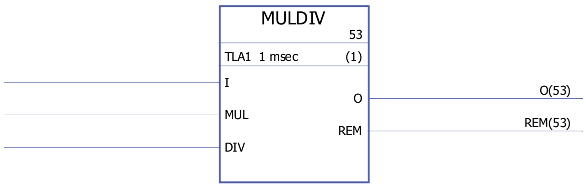
Input (IN1…IN32): INT, DINT, REAL, REAL24

Outputs Output (OUT): INT, DINT, REAL, REAL24

MULDIV

(10007)

Illustration



Execution time 7.10 µs

Operation
The output (O) is the product of input IN and input MUL divided by input DIV.
Output = (I × MUL) / DIV
O = whole value. REM = remainder value.
Example: I = 2, MUL = 16 and DIV = 10:
(2 × 16) / 10 = 3.2, i.e. O = 3 and REM = 2
The output value is limited to the maximum and minimum values defined by the data type range.

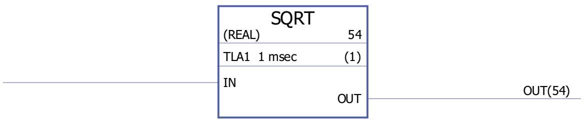
Inputs
Input (I): DINT
Multiplier input (MUL): DINT
Divider input (DIV): DINT

Outputs
Output (O): DINT
Remainder output (REM): DINT

SQRT

(10008)

Illustration



Execution time 2.09 µs

Operation
Output (OUT) is the square root of the input (IN).
OUT = sqrt(IN)
Output is 0 if the input value is negative.

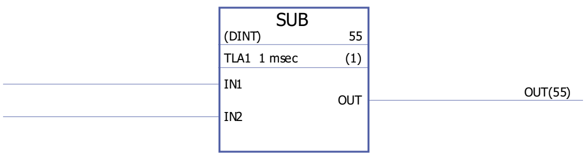
Inputs
The input data type is selected by the user.
Input (IN): REAL, REAL24

Outputs
Output (OUT): REAL, REAL24

SUB -

(10009)

Illustration



Execution time 2.33 µs

Operation
Output (OUT) is the difference between the input signals (IN):
OUT = IN1 - IN2
The output value is limited to the maximum and minimum values defined by the selected data type range.

Inputs
The input data type is selected by the user.
Input (IN1, IN2): INT, DINT, REAL, REAL24

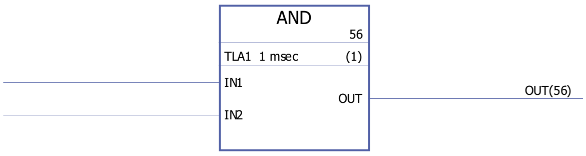
Outputs
Output (OUT): INT, DINT, REAL, REAL24

Bitstring

AND

(10010)

Illustration



Execution time 1.55 µs (when two inputs are used) + 0.60 µs (for every additional input). When all inputs are used, the execution time is 19.55 µs.

Operation The output (OUT) is 1 if all the connected inputs (IN1…IN32) are 1. Otherwise the output is 0.

Truth table:

IN1	IN2	OUT
0	0	0
0	1	0
1	0	0
1	1	1

The inputs can be inverted.

Inputs The number of inputs is selected by the user.

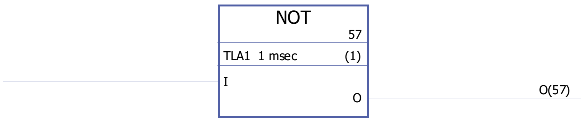
Input (IN1…IN32): Boolean

Outputs Output (OUT): Boolean

NOT

(10011)

Illustration



Execution time 0.32 µs

Operation The output (O) is 1 if the input (I) is 0. The output is 0 if the input is 1.

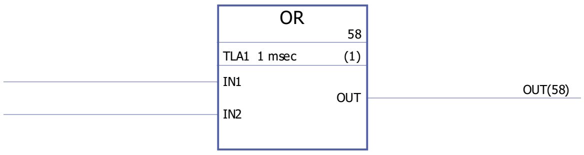
Inputs Input (I): Boolean

Outputs Output (O): Boolean

OR

(10012)

Illustration



Execution time 1.55 µs (when two inputs are used) + 0.60 µs (for every additional input). When all inputs are used, the execution time is 19.55 µs.

Operation The output (OUT) is 0, if all connected inputs (IN) are 0. Otherwise the output is 1.

Truth table:

IN1	IN2	OUT
0	0	0
0	1	1
1	0	1
1	1	1

The inputs can be inverted.

Inputs The number of inputs (2…32) is selected by the user.

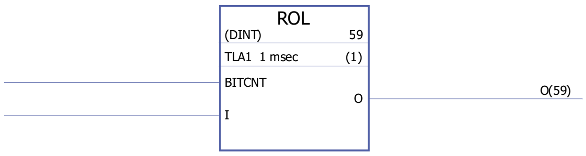
Input (IN1…IN32): Boolean

Outputs Output (OUT): Boolean

ROL

(10013)

Illustration



Execution time 1.28 µs

Operation Input bits (I) are rotated to the left by the number (N) of bits defined by BITCNT. The N most significant bits (MSB) of the input are stored as the N least significant bits (LSB) of the output.

Example: If BITCNT = 3

3 MSB

I	1 1 1 0 0 0 0 0 1 1 1 0 0 1 0 1 1 1 0 1 0 0 1 1 0 0 1 1 0 1 0 1	
O	0 0 0 0 0 1 1 1 0 0 1 0 1 1 1 0 1 0 0 1 1 0 0 1 1 0 1 0 1	1 1 1

3 LSB

Inputs The input data type is selected by the user.

Input (I): INT, DINT

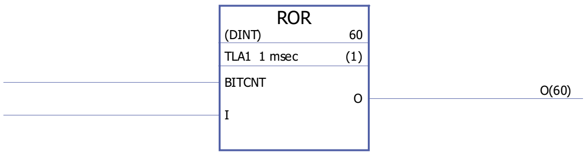
Number of bits input (BITCNT): INT, DINT

Outputs Output (O): INT, DINT

ROR

(10014)

Illustration



Execution time 1.28 µs

Operation Input bits (I) are rotated to the right by the number (N) of bits defined by BITCNT. The N least significant bits (LSB) of the input are stored as the N most significant bits (MSB) of the output.

Example: If BITCNT = 3

3 LSB

I	1	1	1	0	0	0	0	0	1	1	1	0	0	1	0	1	1	1	0	1	0	0	1	1	0	0	1	1	0	1	0	1
O	1	0	1	1	1	1	0	0	0	0	0	1	1	1	0	0	1	0	1	1	1	0	1	0	0	1	1	0	0	1	1	0

3 MSB

Inputs The input data type is selected by the user.

Input (I): INT, DINT

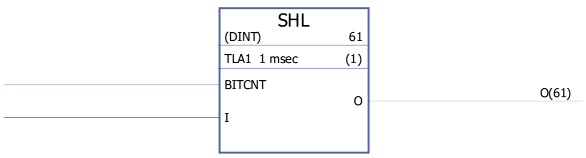
Number of bits input (BITCNT): INT, DINT

Outputs Output (O): INT, DINT

SHL

(10015)

Illustration



Execution time 0.80 µs

Operation Input bits (I) are rotated to the left by the number (N) of bits defined by BITCNT. The N most significant bits (MSB) of the input are lost and the N least significant bits (LSB) of the output are set to 0.

Example: If BITCNT = 3

3 MSB

I	1	1	1	0	0	0	0	0	1	1	1	0	0	1	0	1	1	1	0	1	0	0	1	1	0	0	1	1	0	1	0	1
O	0	0	0	0	0	1	1	1	0	0	1	0	1	1	1	0	1	0	0	1	1	0	0	1	1	0	1	0	1	0	0	0

3 LSB

Inputs The input data type is selected by the user.

Input (I): INT, DINT

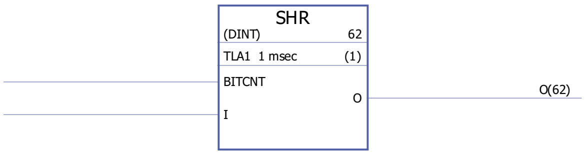
Number of bits (BITCNT): INT; DINT

Outputs Output (O): INT; DINT

SHR

(10016)

Illustration



Execution time 0.80 µs

Operation Input bits (I) are rotated to the right by the number (N) of bits defined by BITCNT. The N least significant bits (LSB) of the input are lost and the N most significant bits (MSB) of the output are set to 0.

Example: If BITCNT = 3

3 LSB

I	1	1	1	0	0	0	0	0	1	1	1	0	0	1	0	1	1	1	0	1	0	0	1	1	0	0	1	1	0	1	0	1
O	0	0	0	1	1	1	0	0	0	0	0	1	1	1	0	0	1	0	1	1	1	0	1	0	0	1	1	0	0	1	1	0

3 MSB

Inputs The input data type is selected by the user.

Input (I): INT, DINT

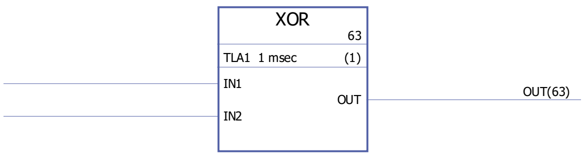
Number of bits (BITCNT): INT; DINT

Outputs Output (O): INT; DINT

XOR

(10017)

Illustration



Execution time 1.24 µs (when two inputs are used) + 0.72 µs (for every additional input). When all inputs are used, the execution time is 22.85 µs.

Operation The output (OUT) is 1 if one of the connected inputs (IN1…IN32) is 1. Output is zero if all the inputs have the same value.

Example:

IN1	IN2	OUT
0	0	0
0	1	1
1	0	1
1	1	0

The inputs can be inverted.

Inputs The number of inputs (2…32) is selected by the user.

Input (IN1…IN32): Boolean

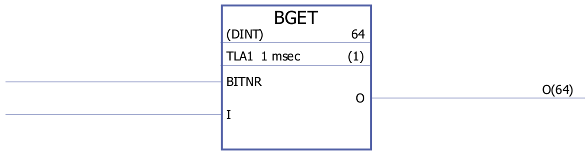
Outputs Output (OUT): Boolean

Bitwise

BGET

(10034)

Illustration



Execution time 0.88 µs

Operation The output (O) is the value of the selected bit (BITNR) of the input (I).

BITNR: Bit number (0 = bit number 0, 31 = bit number 31)

If bit number is not in the range of 0…31 (for DINT) or 0…15 (for INT), the output is 0.

Inputs The input data type is selected by the user.

Number of the bit (BITNR): DINT

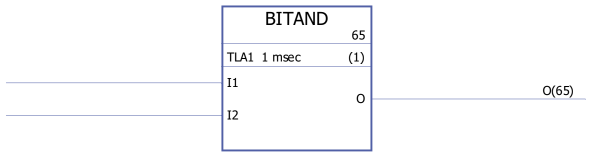
Input (I): DINT, INT

Outputs Output (O): Boolean

BITAND

(10035)

Illustration



Execution time 0.32 µs

Operation The output (O) bit value is 1 if the corresponding bit values of the inputs (I1 and I2) are 1. Otherwise the output bit value is 0.

Example:

I1	1 1 1 0 0 0 0 0 1 1 1 0 0 1 0 1 1 1 0 1 0 0 1 1 0 0 1 1 0 1 0 1
I2	0 0 0 0 0 1 1 1 0 0 1 0 1 1 1 0 1 0 0 1 1 0 0 1 1 0 1 0 1 1 1 1
O	0 0 0 0 0 0 0 0 0 0 1 0 0 1 0 0 1 0 0 1 0 0 0 1 0 0 1 0 0 1 0 1

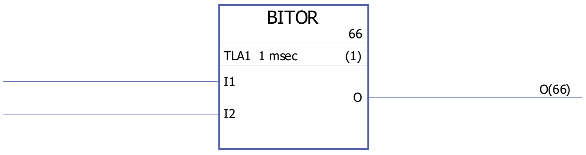
Inputs Input (I1, I2): DINT

Outputs Output (O): DINT

BITOR

(10036)

Illustration



Execution time 0.32 µs

Operation The output (O) bit value is 1 if the corresponding bit value of any of the inputs (I1 or I2) is 1. Otherwise the output bit value is 0.

Example:

I1	1 1 1 0 0 0 0 0 1 1 1 0 0 1 0 1 1 1 0 1 0 0 1 1 0 0 1 1 0 1 0 1
I2	0 0 0 0 0 1 1 1 0 0 1 0 1 1 1 0 1 0 0 1 1 0 0 1 1 0 1 0 1 1 1 1
O	1 1 1 0 0 1 1 1 1 1 1 0 1 1 1 1 1 1 0 1 1 0 1 1 1 0 1 1 1 1 1 1

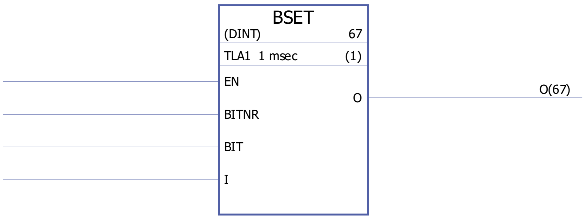
Input Input (I1, I2): DINT

Output Output (O): DINT

BSET

(10037)

Illustration



Execution time 1.36 µs

Operation The value of a selected bit (BITNR) of the input (I) is set as defined by the bit value input (BIT). The function must be enabled by the enable input (EN).

BITNR: Bit number (0 = bit number 0, 31 = bit number 31)

If BITNR is not in the range of 0…31 (for DINT) or 0…15 (for INT) or if EN is reset to zero, the input value is stored to the output as it is (i.e. no bit setting occurs).

Example:

EN = 1, BITNR = 3, BIT = 0

IN = 0000 0000 1111 1111

O = 0000 0000 1111 0111

Inputs The input data type is selected by the user.

Enable input (EN): Boolean

Number of the bit (BITNR): DINT

Bit value input (BIT): Boolean

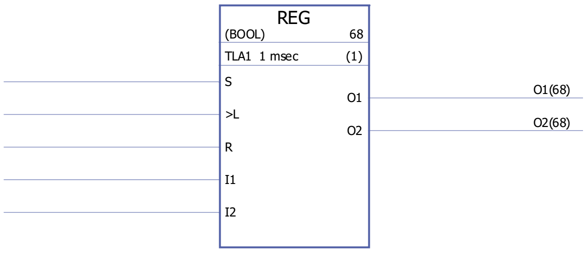
Input (I): INT, DINT

Outputs Output (O): INT, DINT

REG

(10038)

Illustration



Execution time 2.27 µs (when two inputs are used) + 1.02 µs (for every additional input). When all inputs are used, the execution time is 32.87 µs.

Operation The input (I1…I32) value is stored to the corresponding output (O1…O32) if the load input (L) is set to 1 or the set input (S) is 1. When the load input is set to 1, the input value is stored to the output only once. When the set input is 1, the input value is stored to the output every time the block is executed. The set input overrides the load input.

If the reset input (R) is 1, all connected outputs are 0.

Example:

S	R	L	I	$O1_{previous}$	O1
0	0	0	10	15	15
0	0	0->1	20	15	20
0	1	0	30	20	0
0	1	0->1	40	0	0
1	0	0	50	0	50
1	0	0->1	60	50	60
1	1	0	70	60	0
1	1	0->1	80	0	0
$O1_{previous}$ is the previous cycle output value.					

Inputs The input data type and number of inputs (2…32) are selected by the user.

Set input (S): Boolean

Load input (L): Boolean

Reset input (R): Boolean

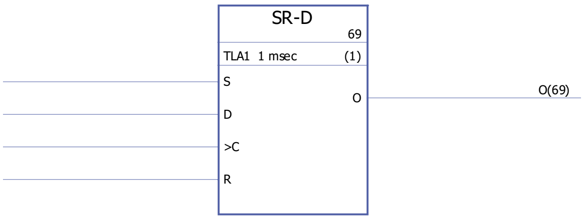
Input (I1…I32): Boolean, INT, DINT, REAL, REAL24

Outputs Output (O1…O32): Boolean, INT, DINT, REAL, REAL24

SR-D

(10039)

Illustration



Execution time 1.04 µs

Operation

When clock input (C) is set to 1, the data input (D) value is stored to the output (O). When reset input (R) is set to 1, the output is set to 0.

If only set (S) and reset (R) inputs are used, SR-D block acts as an SR block: The output is 1 if the set input (S) is 1. The output will retain the previous output state if the set input (S) and reset input (R) are 0. The output is 0 if the set input is 0 and the reset input is 1.

Truth table:

S	R	D	C	$O_{previous}$	O
0	0	0	0	0	0 (= Previous output value)
0	0	0	0 -> 1	0	0 (= Data input value)
0	0	1	0	0	0 (= Previous output value)
0	0	1	0 -> 1	0	1 (= Data input value)
0	1	0	0	1	0 (Reset)
0	1	0	0 -> 1	0	0 (Reset)
0	1	1	0	0	0 (Reset)
0	1	1	0 -> 1	0	0 (Reset)
1	0	0	0	0	1 (= Set value)
1	0	0	0 -> 1	1	0 (= Data input value) for one execution cycle, then changes to 1 according to the set input (S = 1).
1	0	1	0	1	1 (= Set value)
1	0	1	0 -> 1	1	1 (= Data input value)
1	1	0	0	1	0 (Reset)
1	1	0	0 -> 1	0	0 (Reset)
1	1	1	0	0	0 (Reset)
1	1	1	0 -> 1	0	0 (Reset)
$O_{previous}$ is the previous cycle output value.					

Inputs

Set input (S): Boolean

Data input (D): Boolean

Clock input (C): Boolean

Reset input (R): Boolean

Outputs

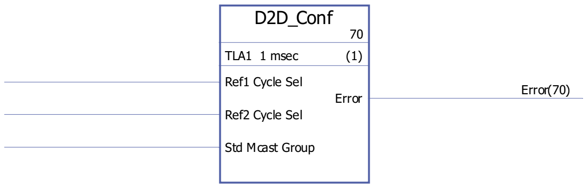
Output (O): Boolean

Communication

D2D_Conf

(10092)

Illustration



Execution time -

Operation

Defines handling interval for drive-to-drive references 1 and 2, and the address (group number) for outgoing standard (non-chained) multicast messages.

The values of the Ref1/2 Cycle Sel inputs correspond to the following intervals:

Value	Handling interval
0	Default (500 µs for reference 1; 2 ms for reference 2)
1	250 µs
2	500 µs
3	2 ms

Note: Negative value of Ref2 Cycle Sel disables the handling of Ref2 (if used, it must be disabled in all follower drives).

Allowable values for the Std Mcast Group input are 0 (= multicasting not used) and 1…62 (multicast group).

An unconnected input, or an input in an error state, is interpreted as having the value 0.

The error codes indicated by the Error output are as follows:

Bit	Description
0	REF1_CYCLE_ERR: Value of input Ref1 Cycle Sel out of range
1	REF2_CYCLE_ERR: Value of input Ref2 Cycle Sel out of range
2	STD_MCAST_ERR: Value of input Std Mcast Group out of range

Inputs

Drive-to-drive reference 1 handling interval (Ref1 Cycle Sel): INT

Drive-to-drive reference 2 handling interval (Ref2 Cycle Sel): INT

Standard multicast address (Std Mcast Group): INT

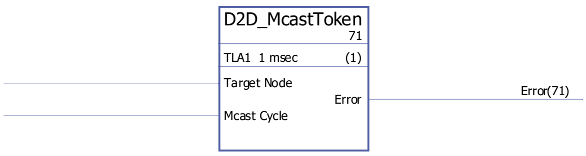
Outputs

Error output (Error): PB

D2D_McastToken

(10096)

Illustration



Execution time -

Operation Configures the transmission of token messages sent to a follower. Each token authorizes the follower to send one message to another follower or group of followers. For the message types, see the block D2D_SendMessage.

Note: This block is only supported in the master.

The Target Node input defines the node address the master sends the tokens to; the range is 1…62.

The Mcast Cycle specifies the interval between token messages in the range of 2…1000 milliseconds. Setting this input to 0 disables the sending of tokens.

The error codes indicated by the Error output are as follows:

Bit	Description
0	D2D_MODE_ERR: Drive is not master
5	TOO_SHORT_CYCLE: Token interval is too short, causing overloading
6	INVALID_INPUT_VAL: An input value is out of range
7	GENERAL_D2D_ERR: Drive-to-drive communication driver failed to initialize message

Inputs Token recipient (Target Node): INT

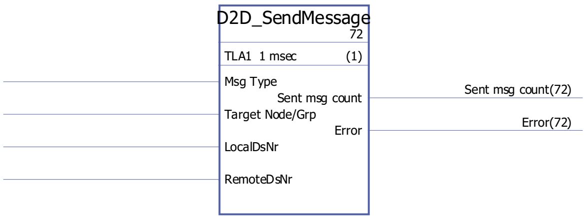
Token interval (Mcast Cycle): INT

Outputs Error output (Error): DINT

D2D_SendMessage

(10095)

Illustration



Execution time -

Operation

Configures the transmission between the dataset tables of drives.

The Msg Type input defines the message type as follows:

Value	Message type
0	Disabled
1	Master P2P: The master sends the contents of a local dataset (specified by LocalDsNr input) to the dataset table (dataset number specified by RemoteDsNr input) of a follower (specified by Target Node/Grp input). The follower replies by sending the next dataset (RemoteDsNr + 1) to the master (LocalDsNr + 1). The node number of a drive is defined by parameter 57.03. **Note:** Only supported in the master drive.
2	Read Remote: The master reads a dataset (specified by RemoteDsNr input) from a follower (specified by Target Node/Grp input) and stores it into local dataset table (dataset number specified by LocalDsNr input). The node number of a drive is defined by parameter 57.03. **Note:** Only supported in the master drive.
3	Follower P2P: The follower sends the contents of a local dataset (specified by LocalDsNr input) to the dataset table (dataset number specified by RemoteDsNr input) of another follower (specified by Target Node/Grp input). The node number of a drive is defined by parameter 57.03. **Note:** Only supported in a follower drive. A token from the master drive is required for the follower to be able to send the message. See the block D2D_McastToken.
4	Standard Multicast: The drive sends the contents of a local dataset (specified by LocalDsNr input) to the dataset table (dataset number specified by RemoteDsNr input) of a group of followers (specified by Target Node/Grp input). Which multicast group a drive belongs to is defined by the Std Mcast Group input of the D2D_Conf block. A token from the master drive is required for a follower to be able to send the message. See the block D2D_McastToken.
5	Broadcast: The drive sends the contents of a local dataset (specified by LocalDsNr input) to the dataset table (dataset number specified by RemoteDsNr input) of all followers. A token from the master drive is required for a follower to be able to send the message. See the block D2D_McastToken.

The Target Node/Grp input specifies the target drive or multicast group of drives depending on message type. See the message type explanations above.

Note: The input must be connected in DriveSPC even if not used.

The LocalDsNr input specifies the number of the local dataset used as the source or the target of the message.

The RemoteDsNr input specifies the number of the remote dataset used as the target or the source of the message.

The Sent msg count output is a wrap-around counter of successfully sent messages.

The error codes indicated by the Error output are as follows:

Bit	Description
0	D2D_MODE_ERR: Drive-to-drive communication not activated, or message type not supported in current drive-to-drive mode (master/follower)
1	LOCAL_DS_ERR: LocalDsNr input out of range (16…199)
2	TARGET_NODE_ERR: Target Node/Grp input out of range (1…62)
3	REMOTE_DS_ERR: Remote dataset number out of range (16…199)
4	MSG_TYPE_ERR: Msg Type input out of range (0…5)
5…6	Reserved
7	GENERAL_D2D_ERR: Unspecified error in D2D driver
8	RESPONSE_ERR: Syntax error in received response
9	TRA_PENDING: Message has not yet been sent
10	REC_PENDING: Response has not yet been received
11	REC_TIMEOUT: No response received
12	REC_ERROR: Frame error in received message
13	REJECTED: Message has been removed from transmit buffer
14	BUFFER_FULL: Transmit buffer full

Inputs

Message type (Msg Type): INT

Target node or multicast group (Target Node/Grp): INT

Local dataset number (LocalDsNr): INT

Remote dataset number (RemoteDsNr): INT

Outputs

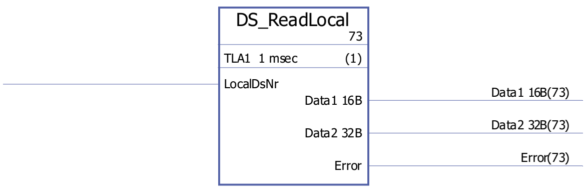
Successfully sent messages counter (Sent msg count): DINT

Error output (Error): PB

DS_ReadLocal

(10094)

Illustration



Execution time -

Operation

Reads the dataset defined by the LocalDsNr input from the local dataset table. One dataset contains one 16-bit and one 32-bit word which are directed to the Data1 16B and Data2 32B outputs respectively.

The LocalDsNr input defines the number of the dataset to be read.

The error codes indicated by the Error output are as follows:

Bit	Description
1	LOCAL_DS_ERR: LocalDsNr out of range (16…199)

Inputs

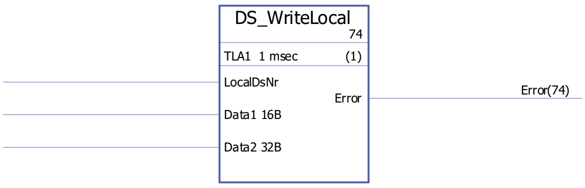
Local dataset number (LocalDsNr): INT

Outputs Contents of dataset (Data1 16B): INT
Contents of dataset (Data2 32B): DINT
Error output (Error): DINT

DS_WriteLocal

(10093)

Illustration



Execution time -

Operation Writes data into the local dataset table. Each dataset contains 48 bits; the data is input through the Data1 16B (16 bits) and Data2 32B (32 bits) inputs. The dataset number is defined by the LocalDsNr input.

The error codes indicated by the Error output are as follows:

Bit	Description
1	LOCAL_DS_ERR: LocalDsNr out of range (16…199)

Inputs Local dataset number (LocalDsNr): INT
Contents of dataset (Data1 16B): INT
Contents of dataset (Data2 32B): DINT

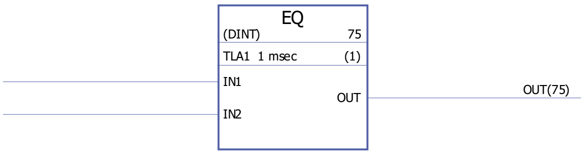
Outputs Error output (Error): DINT

Comparison

EQ

(10040)

Illustration



Execution time 0.89 µs (when two inputs are used) + 0.43 µs (for every additional input). When all inputs are used, the execution time is 13.87 µs.

Operation The output (OUT) is 1 if all the connected input values are equal (IN1 = IN2 = … = IN32). Otherwise the output is 0.

Inputs The input data type and the number of inputs (2…32) are selected by the user.

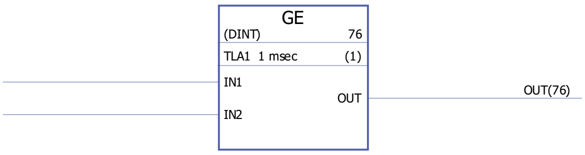
Input (IN1…IN32): INT, DINT, REAL, REAL24

Outputs Output (OUT): Boolean

GE >=

(10041)

Illustration



Execution time 0.89 µs (when two inputs are used) + 0.43 µs (for every additional input). When all inputs are used, the execution time is 13.87 µs.

Operation The output (OUT) is 1 if (IN1 ≥ IN2) & (IN2 ≥ IN3) & … & (IN31 ≥ IN32). Otherwise the output is 0.

Inputs The input data type and the number of inputs (2…32) are selected by the user.

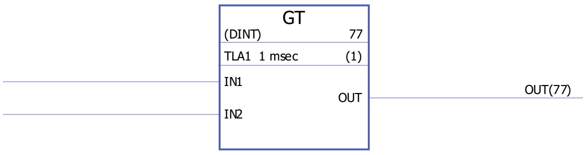
Input (IN1…IN32): INT, DINT, REAL, REAL24

Outputs Output (OUT): Boolean

GT >

(10042)

Illustration



Execution time 0.89 µs (when two inputs are used) + 0.43 µs (for every additional input). When all inputs are used, the execution time is 13.87 µs.

Operation The output (OUT) is 1 if (IN1 > IN2) & (IN2 > IN3) & … & (IN31 > IN32). Otherwise the output is 0.

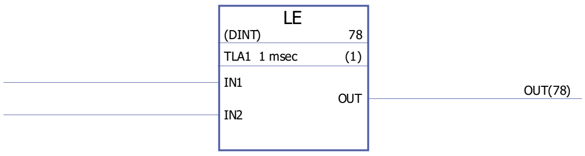
Inputs The input data type and the number of inputs (2…32) are selected by the user.

Input (IN1…IN32): INT, DINT, REAL, REAL24

Outputs Output (OUT): Boolean

LE (10043) <=

Illustration



Execution time 0.89 µs (when two inputs are used) + 0.43 µs (for every additional input). When all inputs are used, the execution time is 13.87 µs.

Operation Output (OUT) is 1 if (IN1 ≤ IN2) & (IN2 ≤ IN3) & … & (IN31 ≤ IN2). Otherwise the output is 0.

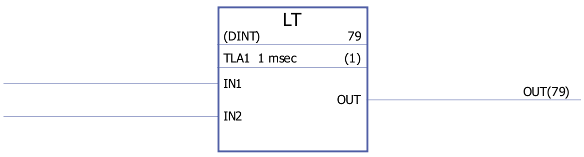
Inputs The input data type and the number of inputs (2…32) are selected by the user.

Input (IN1…IN32): INT, DINT, REAL, REAL24

Outputs Output (OUT): Boolean

LT (10044) <

Illustration



Execution time 0.89 µs (when two inputs are used) + 0.43 µs (for every additional input). When all inputs are used, the execution time is 13.87 µs.

Operation Output (OUT) is 1 if (IN1 < IN2) & (IN2 < IN3) & … & (IN31 < IN32). Otherwise the output is 0.

Inputs The input data type and the number of inputs (2…32) are selected by the user.

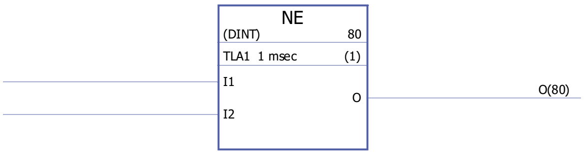
Input (IN1…IN32): INT, DINT, REAL, REAL24

Outputs Output (OUT): Boolean

NE <>

(10045)

Illustration



Execution time 0.44 µs

Operation The output (O) is 1 if I1 <> I2. Otherwise the output is 0.

Inputs The input data type is selected by the user.
Input (I1, I2): INT, DINT, REAL, REAL24

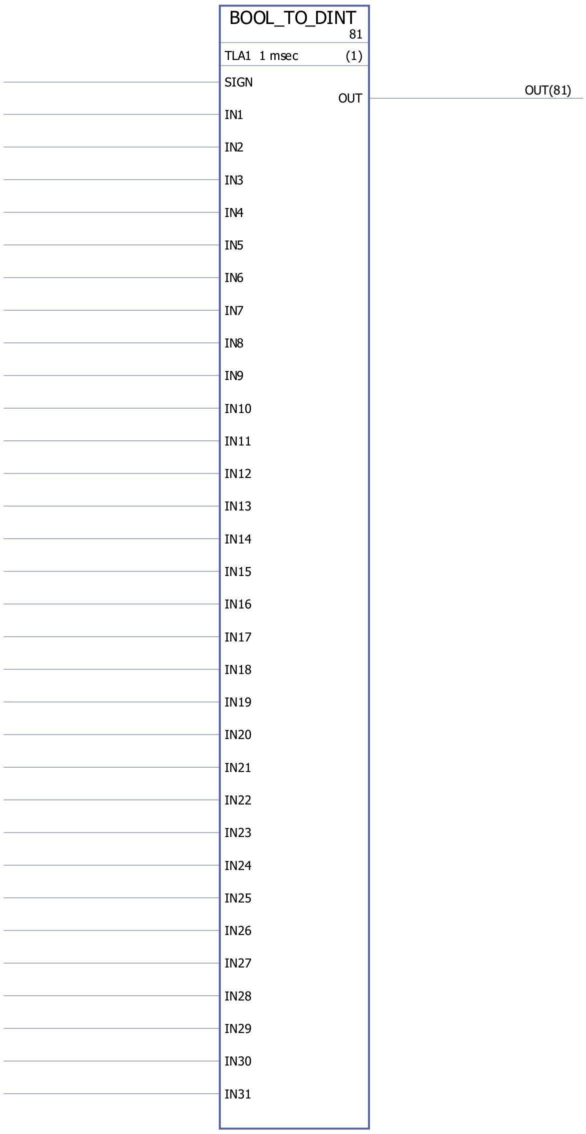
Outputs Output (O): Boolean

Conversion

BOOL_TO_DINT

(10018)

Illustration



Execution time 13.47 µs

Operation The output (OUT) value is a 32-bit integer value formed from the boolean integer input (IN1…IN31 and SIGN) values. IN1 = bit 0 and IN31 = bit 30.

Example:

IN1 = 1, IN2 = 0, IN3…IN31 = 1, SIGN = 1

OUT = 1111 1111 1111 1111 1111 1111 1111 1101

SIGN IN31…IN1

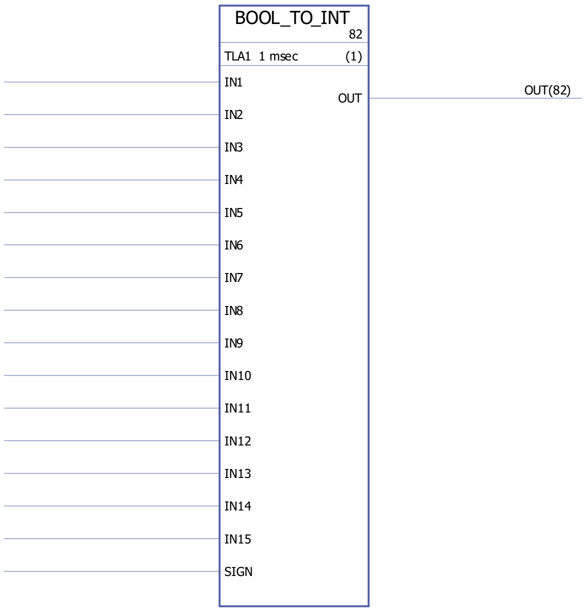
Input Sign input (SIGN): Boolean
Input (IN1…IN31): Boolean

Output Output (OUT): DINT (31 bits + sign)

BOOL_TO_INT

(10019)

Illustration



Execution time 5.00 µs

Operation The output (OUT) value is a 16-bit integer value formed from the boolean integer input (IN1…IN1 and SIGN) values. IN1 = bit 0 and IN15 = bit 14.

Example:

IN1…IN15 = 1, SIGN = 0

OUT = 0111 1111 1111 1111

SIGN IN15…IN1

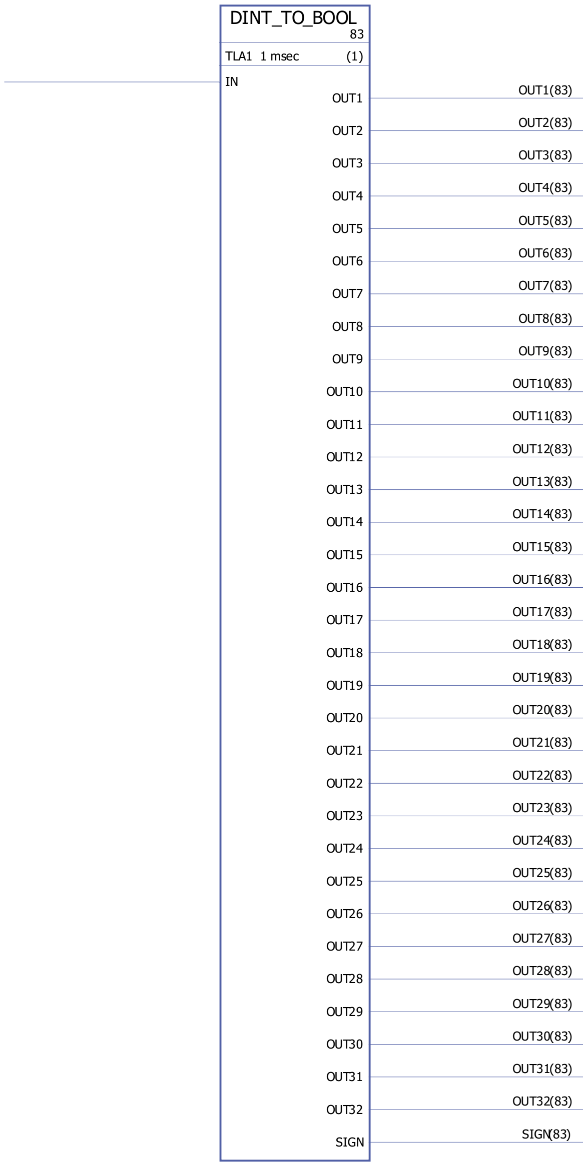
Inputs Input (IN1…IN15): Boolean
Sign input (SIGN): Boolean

Outputs Output (OUT): DINT (15 bits + sign)

DINT_TO_BOOL

(10020)

Illustration



Execution time 11.98 µs

Operation The boolean output (OUT1…32) values are formed from the 32-bit integer input (IN) value.

Example:

IN = 0 111 1111 1111 1111 1111 1111 1111 1100

SIGN OUT32…OUT1

Inputs Input (IN): DINT

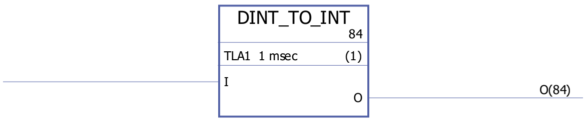
Outputs Output (OUT1…OUT32): Boolean

Sign output (SIGN): Boolean

DINT_TO_INT

(10021)

Illustration



Execution time 0.53 µs

Operation The output (O) value is a 16-bit integer value of the 32-bit integer input (I) value.

Examples:

I (31 bits + sign)	**O** (15 bits + sign)
2147483647	32767
-2147483648	-32767
0	0

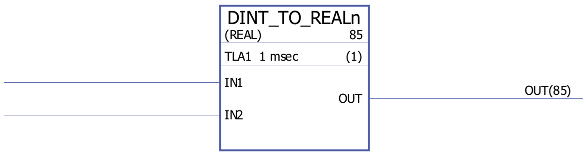
Inputs Input (I): DINT

Outputs Output (O): INT

DINT_TO_REALn

(10023)

Illustration



Execution time 7.25 µs

Operation The output (OUT) is the REAL/REAL24 equivalent of the input (IN). Input IN1 is the integer value and input IN2 is the fractional value.

If one (or both) of the input values is negative, the output value is negative.

Example (from DINT to REAL):

When IN1 = 2 and IN2 = 3276, OUT = 2.04999.

The output value is limited to the maximum value of the selected data type range.

Inputs Input (IN1, IN2): DINT

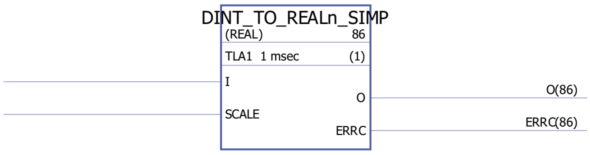
Outputs The output data type is selected by the user.

Output (OUT): REAL, REAL24

DINT_TO_REALn_SIMP

(10022)

Illustration



Execution time 6.53 µs

Operation The output (O) is the REAL/REAL24 equivalent of the input (I) divided by the scale input (SCALE).

Error codes indicated at the error output (ERRC) are as follows:

Error code	Description
0	No error
1001	The calculated REAL/REAL24 value exceeds the minimum value of the selected data type range. The output is set to the minimum value.
1002	The calculated REAL/REAL24 value exceeds the maximum value of the selected data type range. The output is set to the maximum value.
1003	The SCALE input is 0. The output is set to 0.
1004	Incorrect SCALE input, i.e. the scale input is < 0 or is not a factor of 10.

Example (from DINT to REAL24):

When I = 205 and SCALE = 100, I/SCALE = 205 /100 = 2.05 and O = 2.04999.

Inputs Input (I): DINT

Scale input (SCALE): DINT

Outputs The output data type is selected by the user.

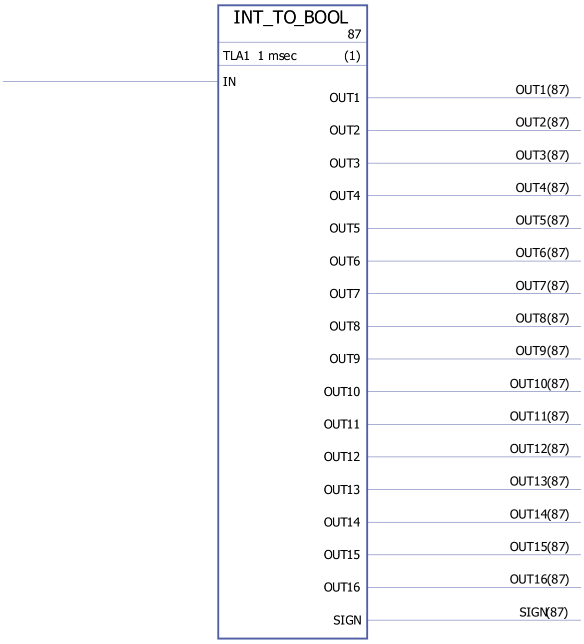
Output (O): REAL, REAL24

Error output (ERRC): DINT

INT_TO_BOOL

(10024)

Illustration



Execution time 4.31 µs

Operation The boolean output (OUT1…16) values are formed from the 16-bit integer input (IN) value.

Example:

IN = 0111 1111 1111 1111

SIGN OUT16…OUT1

Inputs Input (IN): INT

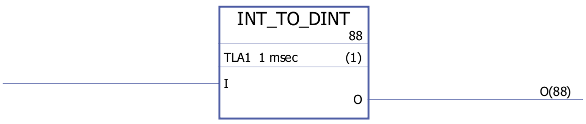
Outputs Output (OUT1…OUT16): Boolean

Sign output (SIGN): Boolean

INT_TO_DINT

(10025)

Illustration



Execution time 0.33 µs

Operation The output (O) value is a 32-bit integer value of the 16-bit integer input (I) value.

I	O
32767	32767
-32767	-32767
0	0

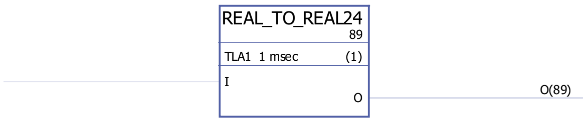
Inputs Input (I): INT

Outputs Output (O): DINT

REAL_TO_REAL24

(10026)

Illustration



Execution time 1.35 µs

Operation Output (O) is the REAL24 equivalent of the REAL input (I).
The output value is limited to the maximum value of the data type.
Example:

I = 0000 0000 0010 0110 1111 1111 1111 1111
(Integer value: 0000 0000 0010 0110; Fractional value: 1111 1111 1111 1111)

O = 0010 0110 1111 1111 1111 1111 0000 0000
(Integer value: 0010 0110; Fractional value: 1111 1111 1111 1111 0000 0000)

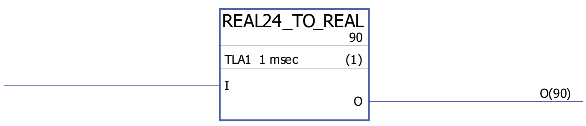
Inputs Input (I): REAL

Outputs Output (O): REAL24

REAL24_TO_REAL

(10027)

Illustration



Execution time 1.20 µs

Operation Output (O) is the REAL equivalent of the REAL24 input (I).
The output value is limited to the maximum value of the data type range.
Example:

I = 0010 0110 1111 1111 1111 1111 0000 0000
(Integer value: 0010 0110; Fractional value: 1111 1111 1111 1111 0000 0000)

O = 0000 0000 0010 0110 1111 1111 1111 1111
(Integer value: 0000 0000 0010 0110; Fractional value: 1111 1111 1111 1111)

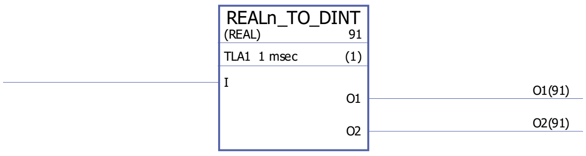
Inputs Input (I): REAL24

Outputs Output (O): REAL

REALn_TO_DINT

(10029)

Illustration



Execution time 6.45 µs

Operation Output (O) is the 32-bit integer equivalent of the REAL/REAL24 input (I). Output O1 is the integer value and output O2 is the fractional value.

The output value is limited to the maximum value of the data type range.

Example (from REAL to DINT):

When I = 2.04998779297, O1 = 2 and O2 = 3276.

Inputs The input data type is selected by the user.

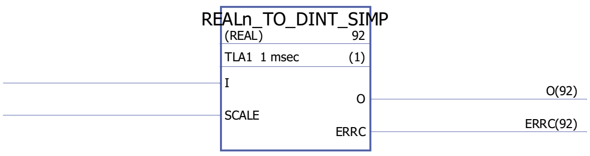
Input (I): REAL, REAL24

Outputs Output (O1, O2): DINT

REALn_TO_DINT_SIMP

(10028)

Illustration



Execution time 5.54 µs

Operation Output (O) is the 32-bit integer equivalent of the REAL/REAL24 input (I) multiplied by the scale input (SCALE).

Error codes are indicated by the error output (ERRC) as follows:

Error code	Description
0	No error
1001	The calculated integer value exceeds the minimum value. The output is set to the minimum value.
1002	The calculated integer value exceeds the maximum value. The output is set to the maximum value.
1003	Scale input is 0. The output is set to 0.
1004	Incorrect scale input, i.e. scale input is < 0 or is not a factor of 10.

Example (from REAL to DINT):

When I = 2.04998779297and SCALE = 100, O = 204.

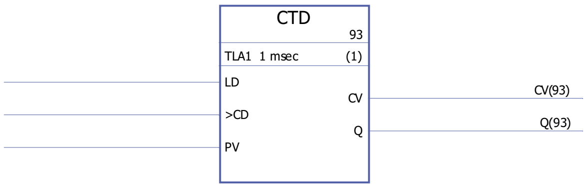
Inputs	The input data type is selected by the user. Input (I): REAL, REAL24 Scale input (SCALE): DINT
Outputs	Output (O): DINT Error output (ERRC): DINT

Counters

CTD

(10047)

Illustration



Execution time 0.92 µs

Operation The counter output (CV) value is decreased by 1 if the counter input (CD) value changes from 0 -> 1 and the load input (LD) value is 0. If the load input value is 1, the preset input (PV) value is stored as the counter output (CV) value. If the counter output has reached its minimum value -32768, the counter output remains unchanged.

The status output (Q) is 1 if the counter output (CV) value ≤ 0.

Example:

LD	CD	PV	Q	CV_{prev}	CV
0	1 -> 0	10	0	5	5
0	0 -> 1	10	0	5	5 - 1 = 4
1	1 -> 0	-2	1	4	-2
1	0 -> 1	1	0	-2	1
0	0 -> 1	5	1	1	1 -1 = 0
1	1 -> 0	-32768	1	0	-32768
0	0 -> 1	10	1	-32768	-32768
CV_{prev} is the previous cycle counter output value.					

Inputs Counter input (CD): Boolean

Load input (LD): Boolean

Preset input (PV): INT

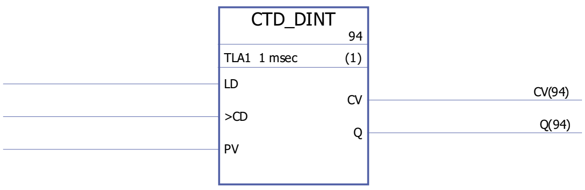
Outputs Status output (Q): Boolean

Counter output (CV): INT

CTD_DINT

(10046)

Illustration



Execution time 0.92 µs

Operation

The counter output (CV) value is decreased by 1 if the counter input (CD) value changes from 0 -> 1 and the load input (LD) value is 0. If the load input (LD) value is 1, the preset input (PV) value is stored as the counter output (CV) value. If the counter output has reached its minimum value -2147483648, the counter output remains unchanged.

The status output (Q) is 1 if the counter output (CV) value ≤ 0.

Example:

LD	CD	PV	Q	CV_{prev}	CV
0	1 -> 0	10	0	5	5
0	0 -> 1	10	0	5	5 - 1 = 4
1	1 -> 0	-2	1	4	-2
1	0 -> 1	1	0	-2	1
0	0 -> 1	5	1	1	1 -1 = 0
1	1 -> 0	-2147483648	1	0	-2147483648
0	0 -> 1	10	1	-2147483648	-2147483648
CV_{prev} is the previous cycle counter output value.					

Inputs

Counter input (CD): Boolean

Load input (LD): Boolean

Preset input (PV): DINT

Outputs

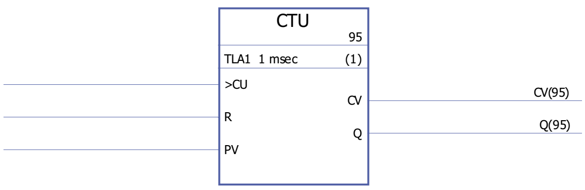
Status output (Q): Boolean

Counter output (CV): DINT

CTU

(10049)

Illustration



Execution time 0.92 µs

Operation

The counter output (CV) value is increased by 1 if the counter input (CU) value changes from 0 -> 1 and the reset input (R) value is 0. If the counter output has reached its maximum value 32767, the counter output remains unchanged.

The counter output (CV) is reset to 0 if the reset input (R) is 1.

The status output (Q) is 1 if the counter output (CV) value $\geq$ preset input (PV) value.

Example:

R	CU	PV	Q	CV_{prev}	CV
0	1 -> 0	20	0	10	10
0	0 -> 1	11	1	10	10 + 1 = 11
1	1 -> 0	20	0	11	0
1	0 -> 1	5	0	0	0
0	0 -> 1	20	0	0	0 + 1 = 1
0	0 -> 1	30	1	32767	32767
CV_{prev} is the previous cycle counter output value.					

Inputs

Counter input (CU): Boolean

Reset input (R): Boolean

Preset input (PV): INT

Outputs

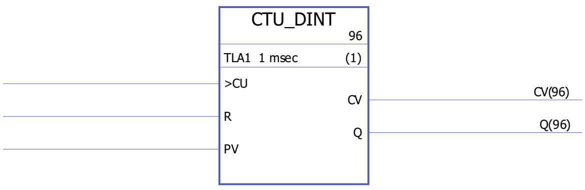
Status output (Q): Boolean

Counter output (CV): INT

CTU_DINT

(10048)

Illustration



Execution time

0.92 µs

Operation

The counter output (CV) value is increased by 1 if the counter input (CU) value changes from 0 -> 1 and the reset input (R) value is 0. If the counter output has reached its maximum value 2147483647, the counter output remains unchanged.

The counter output (CV) is reset to 0 if the reset input (R) is 1.

The status output (Q) is 1 if the counter output (CV) value $\geq$ preset input (PV) value.

Example:

R	CU	PV	Q	CV_{prev}	CV
0	1 -> 0	20	0	10	10
0	0 -> 1	11	1	10	10 + 1 = 11
1	1 -> 0	20	0	11	0
1	0 -> 1	5	0	0	0
0	0 -> 1	20	0	0	0 + 1 = 1
0	0 -> 1	30	1	2147483647	2147483647
CV_{prev} is the previous cycle counter output value.					

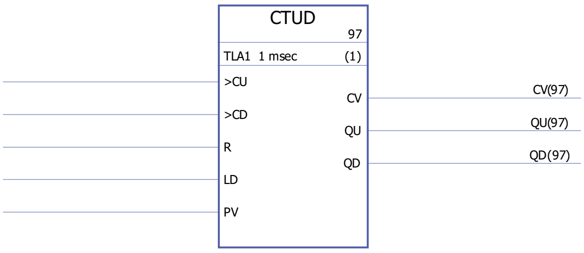
Inputs Counter input (CU): Boolean
Reset input (R): Boolean
Preset input (PV): DINT

Outputs Status output (Q): Boolean
Counter output (CV): DINT

CTUD

(10051)

Illustration



Execution time 1.40 µs

Operation

The counter output (CV) value is increased by 1 if the counter input (CU) value changes from 0 -> 1 and the reset input (R) value is 0.

The counter output (CV) value is decreased by 1 if the counter input (CD) value changes from 0 -> 1 and the load input (LD) value is 0.

If the load input (LD) value is 1, the preset input (PV) value is stored as the counter output (CV) value.

The counter output (CV) is reset to 0 if the reset input (R) is 1.

If the counter output has reached its minimum or maximum value, -32768 or +32767, the counter output remains unchanged until it is reset (R) or until the load input (LD) is set to 1.

The up counter status output (QU) is 1 if the counter output (CV) value ≥ preset input (PV) value.

The down counter status output (QD) is 1 if the counter output (CV) value ≤ 0.

Example:

CU	CD	R	LD	PV	QU	QD	CV_{prev}	CV
0 -> 0	0 -> 0	0	0	2	0	1	0	0
0 -> 0	0 -> 0	0	1	2	1	0	0	2
0 -> 0	0 -> 0	1	0	2	0	1	2	0
0 -> 0	0 -> 0	1	1	2	0	1	0	0
0 -> 0	0 -> 1	0	0	2	0	1	0	0 - 1 = -1
0 -> 0	1 -> 1	0	1	2	1	0	-1	2
0 -> 0	1 -> 1	1	0	2	0	1	2	0
0 -> 0	1 -> 1	1	1	2	0	1	0	0
0 -> 1	1 -> 0	0	0	2	0	0	0	0 + 1 = 1
1 -> 1	0 -> 0	0	1	2	1	0	1	2
1 -> 1	0 -> 0	1	0	2	0	1	2	0
1 -> 1	0 -> 0	1	1	2	0	1	0	0
1 -> 1	0 -> 1	0	0	2	0	1	0	0 - 1 = -1
1 -> 1	1 -> 1	0	1	2	1	0	-1	2
1 -> 1	1 -> 1	1	0	2	0	1	2	0
1 -> 1	1 -> 1	1	1	2	0	1	0	0
CV_{prev} is the previous cycle counter output value.								

Inputs

Down counter input (CD): Boolean

Up counter input (CU): Boolean

Load input (LD): Boolean

Reset input (R): Boolean

Preset input (PV): INT

Outputs

Down counter status output (QD): Boolean

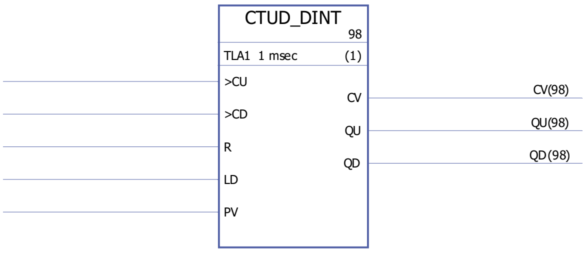
Up counter status output (QU): Boolean

Counter output (CV): INT

CTUD_DINT

(10050)

Illustration



Execution time 1.40 µs

Operation

The counter output (CV) value is increased by 1 if the counter input (CU) value changes from 0 -> 1 and the reset input (R) value is 0.

The counter output (CV) value is decreased by 1 if the counter input (CD) value changes from 0 -> 1 and the load input (LD) value is 0.

If the counter output has reached its minimum or maximum value, -2147483648 or +2147483647, the counter output remains unchanged until it is reset (R) or until the load input (LD) is set.

If the load input (LD) value is 1, the preset input (PV) value is stored as the counter output (CV) value.

The counter output (CV) is reset to 0 if the reset input (R) is 1.

The up counter status output (QU) is 1 if the counter output (CV) value $\geq$ preset input (PV) value.

The down counter status output (QD) is 1 if the counter output (CV) value $\leq$ 0.

Example:

CU	CD	R	LD	PV	QU	QD	CV_{prev}	CV
0 -> 0	0 -> 0	0	0	2	0	1	0	0
0 -> 0	0 -> 0	0	1	2	1	0	0	2
0 -> 0	0 -> 0	1	0	2	0	1	2	0
0 -> 0	0 -> 0	1	1	2	0	1	0	0
0 -> 0	0 -> 1	0	0	2	0	1	0	0 - 1 = -1
0 -> 0	1 -> 1	0	1	2	1	0	-1	2
0 -> 0	1 -> 1	1	0	2	0	1	2	0
0 -> 0	1 -> 1	1	1	2	0	1	0	0
0 -> 1	1 -> 0	0	0	2	0	0	0	0 + 1 = 1
1 -> 1	0 -> 0	0	1	2	1	0	1	2
1 -> 1	0 -> 0	1	0	2	0	1	2	0
1 -> 1	0 -> 0	1	1	2	0	1	0	0
1 -> 1	0 -> 1	0	0	2	0	1	0	0 - 1 = -1
1 -> 1	1 -> 1	0	1	2	1	0	-1	2
1 -> 1	1 -> 1	1	0	2	0	1	2	0
1 -> 1	1 -> 1	1	1	2	0	1	0	0

CV_{prev} is the previous cycle counter output value.

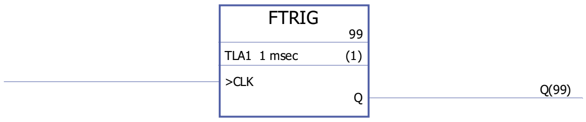
Inputs	Down counter input (CD): Boolean Up counter input (CU): Boolean Load input (LD): Boolean Reset input (R): Boolean Preset input (PV): DINT
Outputs	Down counter status output (QD): Boolean Up counter status output (QU): Boolean Counter output (CV): DINT

Edge & bistable

FTRIG

(10030)

Illustration



Execution time 0.38 µs

Operation The output (Q) is set to 1 when the clock input (CLK) changes from 1 to 0. The output is set back to 0 with the next execution of the block. Otherwise the output is 0.

$CLK_{previous}$	CLK	Q
0	0	0
0	1	0
1	0	1 (for one execution cycle time, returns to 0 at the next execution)
1	1	0
$CLK_{previous}$ is the previous cycle output value.		

Note: The output (Q) is 0 after cold restart and after the first execution of the block. Otherwise the output is 1, when the clock input (CLK) is 1.

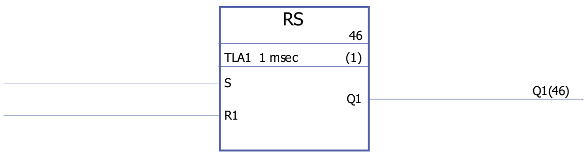
Inputs Clock input (CLK): Boolean

Outputs Output (Q): Boolean

RS

(10032)

Illustration



Execution time 0.38 µs

Operation

The output (Q1) is 0 if the set input (S) is 1 and the reset input (R) value is 0. The output will retain the previous output state if the set input (S) and the reset input (R) are 0. The output is 0 if the set input is 0 and the reset input is 1.

Truth table:

S	R	$Q1_{previous}$	Q1
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	0
$Q_{previous}$ is the previous cycle output value.			

Inputs

Set input (S): Boolean

Reset input (R): Boolean

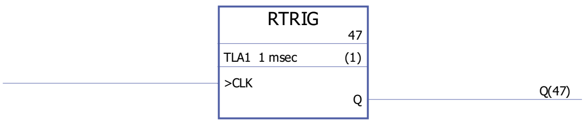
Outputs

Output (Q1): Boolean

RTRIG

(10031)

Illustration



Execution time

0.38 µs

Operation

The output (Q) is set to 1 when the clock input (CLK) changes from 0 to 1. The output is set back to 0 with the next execution of the block. Otherwise the output is 0.

$CLK_{previous}$	CLK	Q
0	0	0
0	1	1
1	0	0
1	1	0
$CLK_{previous}$ is the previous cycle output value.		

Note: The output is 0 after cold restart and after the first execution of the RTRIG block. Otherwise the output is 1, when the clock input is 1.

Inputs

Clock input (CLK): Boolean

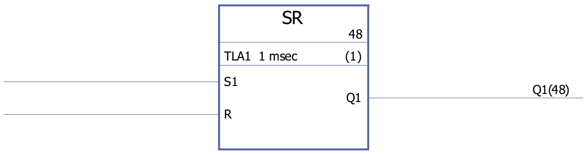
Outputs

Output (Q): Boolean

SR

(10033)

Illustration



Execution time 0.38 µs

Operation The output (Q1) is 1 if the set input (S1) is 1. The output will retain the previous output state if the set input (S1) and the reset input (R) are 0. The output is 0 if the set input is 0 and the reset input is 1.

Truth table:

S1	R	$Q1_{previous}$	Q1
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	1
$Q1_{previous}$ is the previous cycle output value.			

Inputs Set input (S1): Boolean

Reset input (R): Boolean

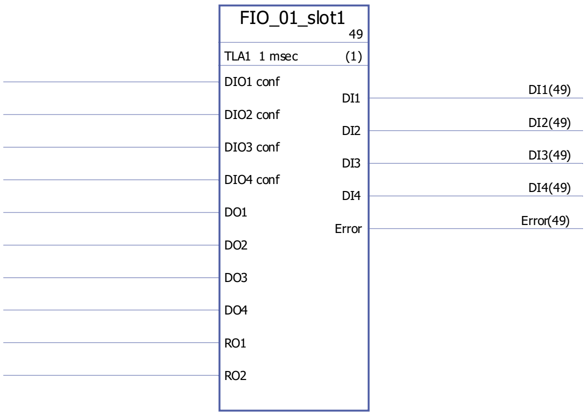
Outputs Output (Q1): Boolean

Extensions

FIO_01_slot1

(10084)

Illustration



Execution time 8.6 μs

Operation

The block controls the four digital inputs/outputs (DIO1…DIO4) and two relay outputs (RO1, RO2) of a FIO-01 Digital I/O Extension mounted on slot 1 of the drive control unit.

The state of a DIOx conf input of the block determines whether the corresponding DIO on the FIO-01 is an input or an output (0 = input, 1 = output). If the DIO is an output, the DOx input of the block defines its state.

The RO1 and RO2 inputs define the state of the relay outputs of the FIO-01 (0 = not energised, 1 = energised).

The DIx outputs show the state of the DIOs.

Inputs

Digital input/output mode selection (DIO1 conf … DIO4 conf): Boolean

Digital output state selection (DO1…DO4): Boolean

Relay output state selection (RO1, RO2): Boolean

Outputs

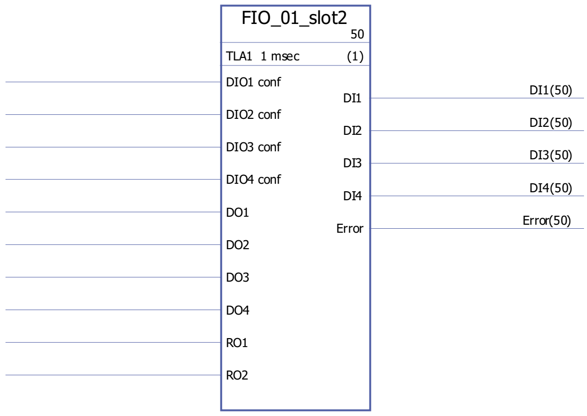
Digital input/output state (DI1…DI4): Boolean

Error output (Error): DINT (0 = No error; 1 = Application program memory full)

FIO_01_slot2

(10085)

Illustration



Execution time 8.6 µs

Operation

The block controls the four digital inputs/outputs (DIO1…DIO4) and two relay outputs (RO1, RO2) of a FIO-01 Digital I/O Extension mounted on slot 2 of the drive control unit.

The state of a DIOx conf input of the block determines whether the corresponding DIO on the FIO-01 is an input or an output (0 = input, 1 = output). If the DIO is an output, the DOx input of the block defines its state.

The RO1 and RO2 inputs define the state of the relay outputs of the FIO-01 (0 = not energised, 1 = energised).

The DIx outputs show the state of the DIOs.

Inputs

Digital input/output mode selection (DIO1 conf … DIO4 conf): Boolean

Digital output state selection (DO1…DO4): Boolean

Relay output state selection (RO1, RO2): Boolean

Outputs

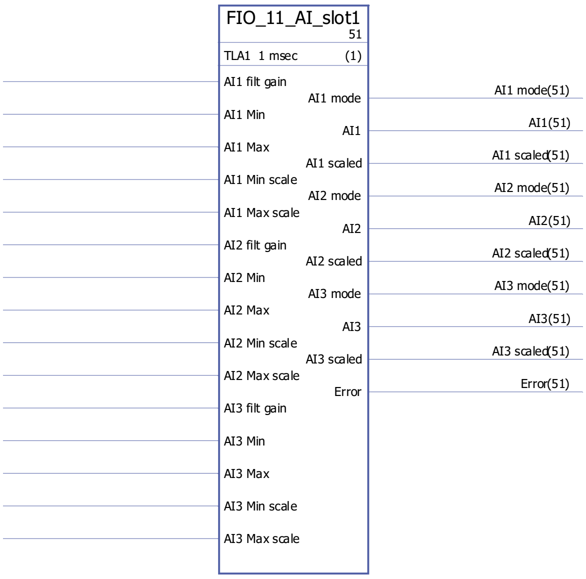
Digital input/output state (DI1…DI4): Boolean

Error output (Error): DINT (0 = No error; 1 = Application program memory full)

FIO_11_AI_slot1

(10088)

Illustration



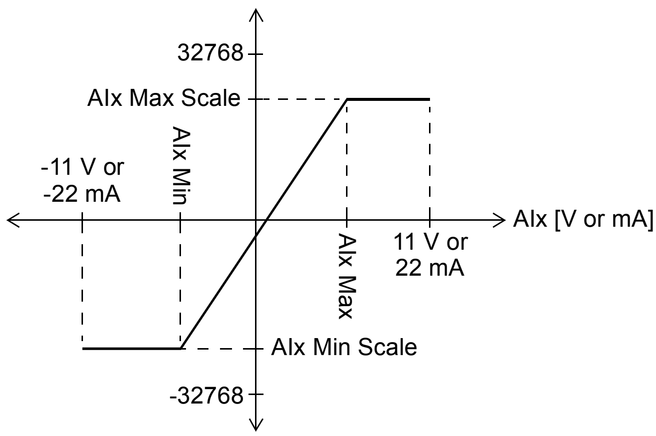
Execution time 11.1 µs

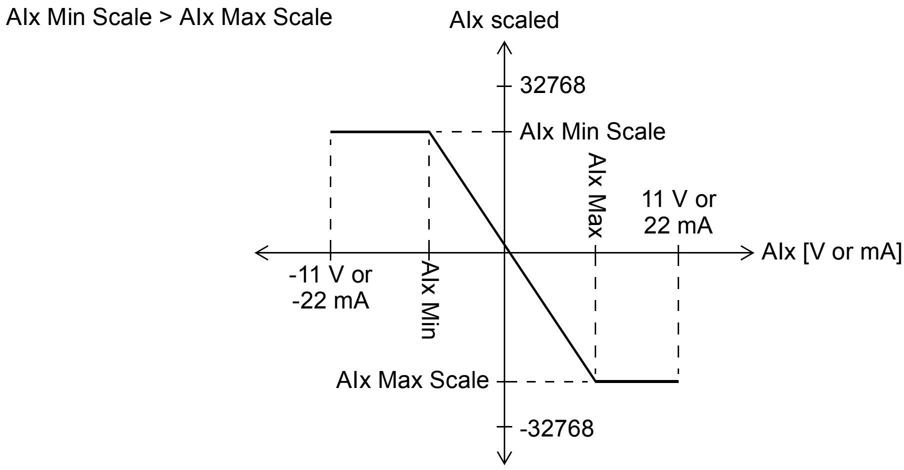
Operation The block controls the three analogue inputs (AI1…AI3) of a FIO-11 Analog I/O Extension mounted on slot 1 of the drive control unit.

The block outputs both the unscaled (AIx) and scaled (AIx scaled) actual values of each analogue input. The scaling is based on the relationship between the ranges AIx min … AIx max and AIx min scale … AIx max scale.

AIx Min must be smaller than AIx Max; AIx Max Scale can be greater or smaller than AIx Min Scale.

AIx Min Scale < AIx Max Scale





The AIx filt gain inputs determine a filtering time for each input as follows:

AIx filt gain	Filtering time	Notes
0	No filtering	
1	125 µs	Recommended setting
2	250 µs	
3	500 µs	
4	1 ms	
5	2 ms	
6	4 ms	
7	7.9375 ms	

The AIx mode outputs show whether the corresponding input is voltage (0) or current (1). The voltage/current selection is made using the hardware switches on the FIO-11.

Inputs

Analogue input filter gain selection (AI1 filt gain … AI3 filt gain): INT

Minimum value of input signal (AI1 Min … AI3 Min): REAL (≥ -11 V or -22 mA)

Maximum value of input signal (AI1 Max … AI3 Max): REAL (≤ 11 V or 22 mA)

Minimum value of scaled output signal (AI1 Min scale … AI3 Min scale): REAL

Maximum value of scaled output signal (AI1 Max scale … AI3 Max scale): REAL

Outputs

Analogue input mode (voltage or current) (AI1 mode … AI3 mode): Boolean

Value of analogue input (AI1 … AI3): REAL

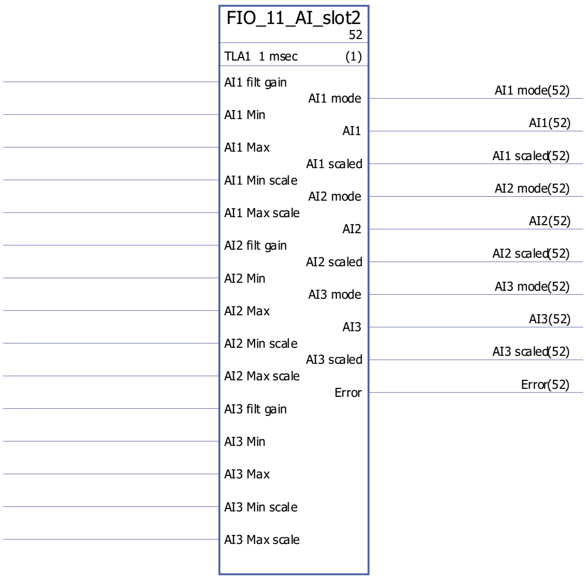
Scaled value of analogue input (AI1 scaled … AI3 scaled): REAL

Error output (Error): DINT (0 = No error; 1 = Application program memory full)

FIO_11_AI_slot2

(10089)

Illustration



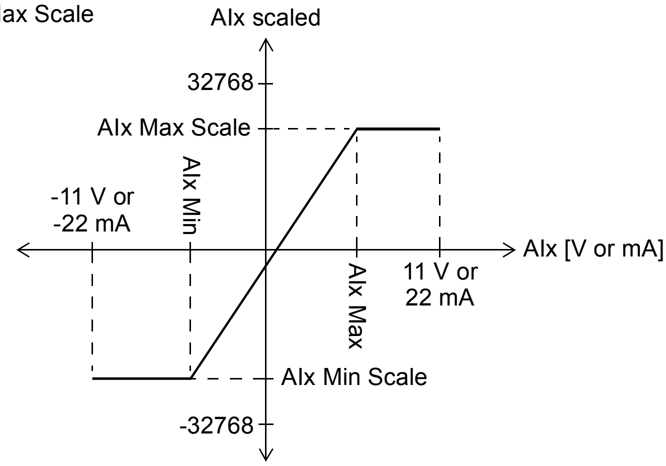
Execution time 11.1 µs

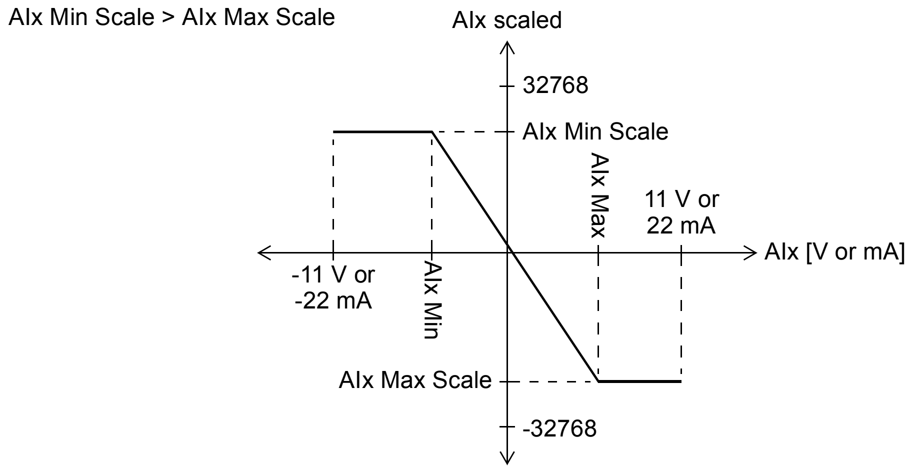
Operation The block controls the three analogue inputs (AI1…AI3) of a FIO-11 Analog I/O Extension mounted on slot 2 of the drive control unit.

The block outputs both the unscaled (AIx) and scaled (AIx scaled) actual values of each analogue input. The scaling is based on the relationship between the ranges AIx min … AIx max and AIx min scale … AIx max scale.

AIx Min must be smaller than AIx Max; AIx Max Scale can be greater or smaller than AIx Min Scale.

AIx Min Scale < AIx Max Scale





The AIx filt gain inputs determine a filtering time for each input as follows:

AIx filt gain	Filtering time	Notes
0	No filtering	
1	125 µs	Recommended setting
2	250 µs	
3	500 µs	
4	1 ms	
5	2 ms	
6	4 ms	
7	7.9375 ms	

The AIx mode outputs show whether the corresponding input is voltage (0) or current (1). The voltage/current selection is made using the hardware switches on the FIO-11.

Inputs

Analogue input filter gain selection (AI1 filt gain … AI3 filt gain): INT

Minimum value of input signal (AI1 Min … AI3 Min): REAL (≥ -11 V or -22 mA)

Maximum value of input signal (AI1 Max … AI3 Max): REAL (≤ 11 V or 22 mA)

Minimum value of scaled output signal (AI1 Min scale … AI3 Min scale): REAL

Maximum value of scaled output signal (AI1 Max scale … AI3 Max scale): REAL

Outputs

Analogue input mode (voltage or current) (AI1 mode … AI3 mode): Boolean

Value of analogue input (AI1 … AI3): REAL

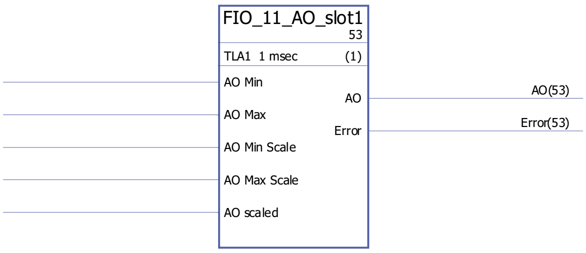
Scaled value of analogue input (AI1 scaled … AI3 scaled): REAL

Error output (Error): DINT (0 = No error; 1 = Application program memory full)

FIO_11_AO_slot1

(10090)

Illustration



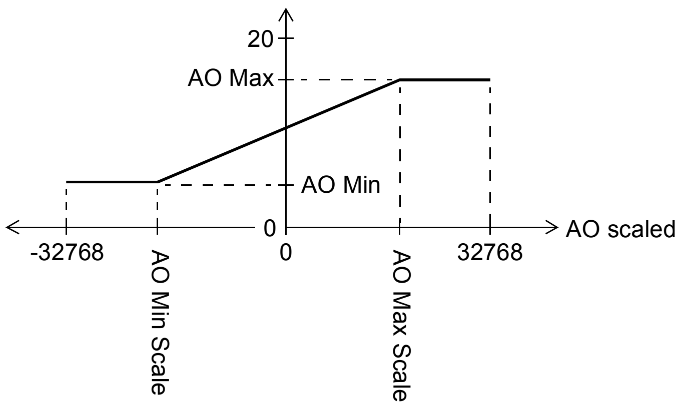
Execution time 4.9 µs

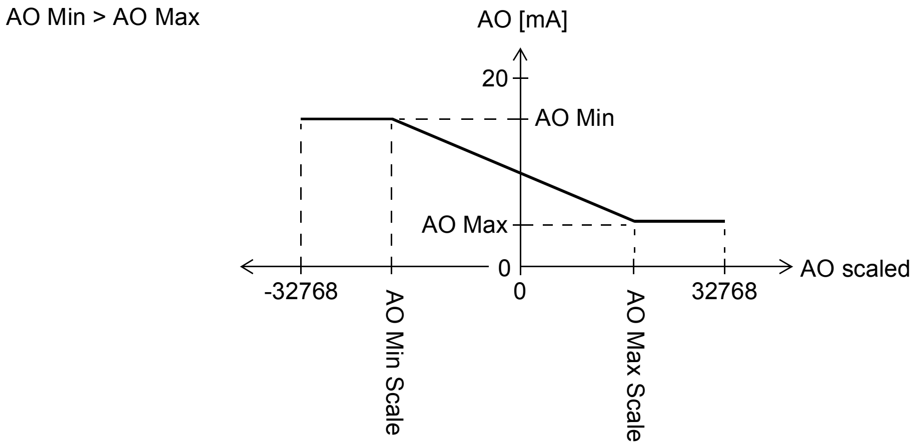
Operation The block controls the analogue output (AO1) of a FIO-11 Analog I/O Extension mounted on slot 1 of the drive control unit.

The block converts the input signal (AO scaled) to a 0…20 mA signal (AO) that drives the analogue output; the input range AO Min Scale … AO Max Scale corresponds to the current signal range of AO Min … AO Max.

AO Min Scale must be smaller than AO Max Scale; AO Max can be greater or smaller than AO Min.

AO Min < AO Max





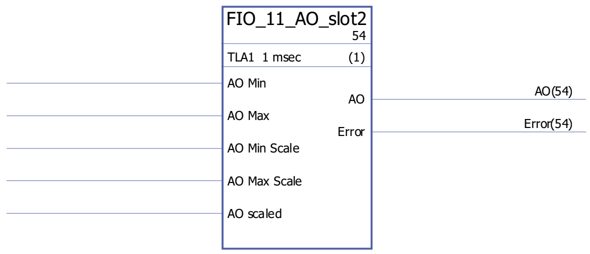
Inputs Minimum current signal (AO Min): REAL (0…20 mA)
Maximum current signal (AO Max): REAL (0…20 mA)
Minimum input signal (AO Min Scale): REAL
Maximum input signal (AO Max Scale): REAL
Input signal (AO scaled): REAL

Outputs Analogue output current value (AO): REAL
Error output (Error): DINT (0 = No error; 1 = Application program memory full)

FIO_11_AO_slot2

(10091)

Illustration



Execution time 4.9 µs

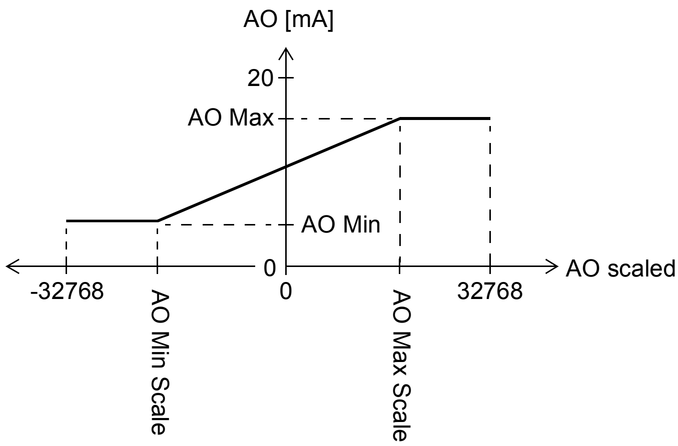
Operation

The block controls the analogue output (AO1) of a FIO-11 Analog I/O Extension mounted on slot 2 of the drive control unit.

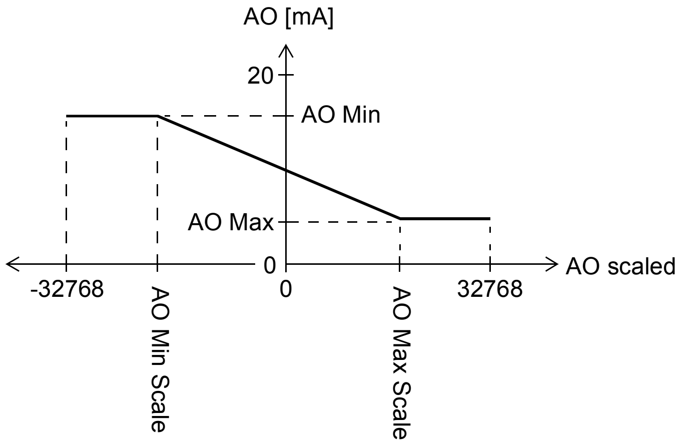
The block converts the input signal (AO scaled) to a 0…20 mA signal (AO) that drives the analogue output; the input range AO Min Scale … AO Max Scale corresponds to the current signal range of AO Min … AO Max.

AO Min Scale must be smaller than AO Max Scale; AO Max can be greater or smaller than AO Min.

AO Min < AO Max



AO Min > AO Max



Inputs

Minimum current signal (AO Min): REAL (0…20 mA)

Maximum current signal (AO Max): REAL (0…20 mA)

Minimum input signal (AO Min Scale): REAL

Maximum input signal (AO Max Scale): REAL

Input signal (AO scaled): REAL

Outputs

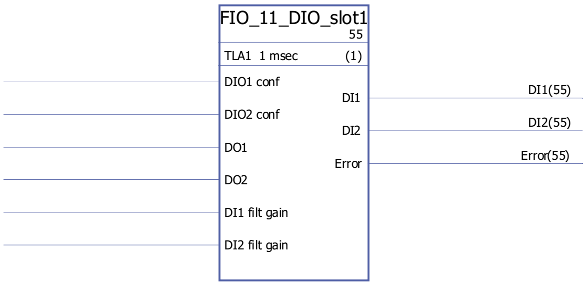
Analogue output current value (AO): REAL

Error output (Error): DINT (0 = No error; 1 = Application program memory full)

FIO_11_DIO_slot1

(10086)

Illustration



Execution time 6.0 µs

Operation The block controls the two digital inputs/outputs (DIO1, DIO2) of a FIO-11 Digital I/O Extension mounted on slot 1 of the drive control unit.

The state of a DIOx conf input of the block determines whether the corresponding DIO on the FIO-11 is an input or an output (0 = input, 1 = output). If the DIO is an output, the DOx input of the block defines its state.

The DIx outputs show the state of the DIOs.

The DIx filt gain inputs determine a filtering time for each input as follows:

DIx filt gain	Filtering time
0	7.5 µs
1	195 µs
2	780 µs
3	4.680 ms

Inputs Digital input/output mode selection (DIO1 conf, DIO2 conf): Boolean

Digital output state selection (DO1, DO2): Boolean

Digital input filter gain selection (DI1 filt gain, DI2 filt gain): INT

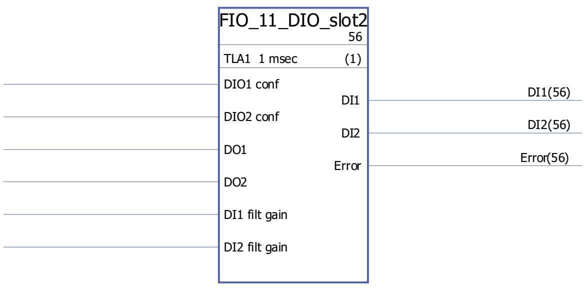
Outputs Digital input/output state (DI1, DI2): Boolean

Error output (Error): DINT (0 = No error; 1 = Application program memory full)

FIO_11_DIO_slot2

(10087)

Illustration



Execution time 6.0 µs

Operation

The block controls the two digital inputs/outputs (DIO1, DIO2) of a FIO-11 Digital I/O Extension mounted on slot 2 of the drive control unit.

The state of a DIOx conf input of the block determines whether the corresponding DIO on the FIO-11 is an input or an output (0 = input, 1 = output). If the DIO is an output, the DOx input of the block defines its state.

The DIx outputs show the state of the DIOs.

The DIx filt gain inputs determine a filtering time for each input as follows:

DIx filt gain	Filtering time
0	7.5 µs
1	195 µs
2	780 µs
3	4.680 ms

Inputs

Digital input/output mode selection (DIO1 conf, DIO2 conf): Boolean

Digital output state selection (DO1, DO2): Boolean

Digital input filter gain selection (DI1 filt gain, DI2 filt gain): INT

Outputs

Digital input/output state (DI1, DI2): Boolean

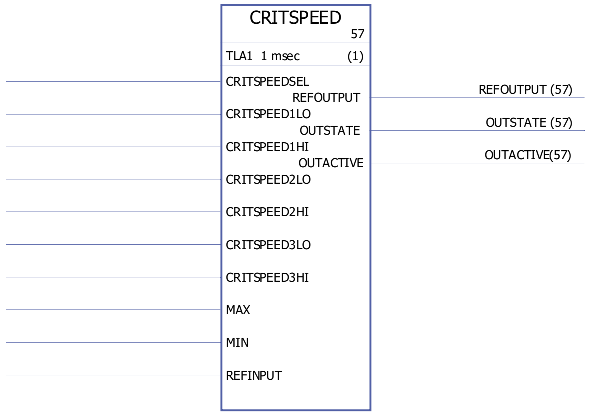
Error output (Error): DINT (0 = No error; 1 = Application program memory full)

Feedback & algorithms

CRITSPEED

(10068)

Illustration



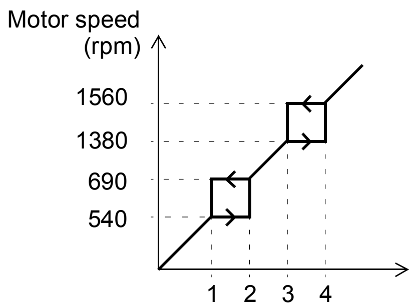
Execution time 4.50 µs

Operation A critical speeds function block is available for applications where it is necessary to avoid certain motor speeds or speed bands because of e.g. mechanical resonance problems. The user can define three critical speeds or speed bands.

Example: An application has vibrations in the range of 540 to 690 rpm and 1380 to 1560 rpm. To make the drive made to jump over the vibration speed ranges:

- activate the critical speeds function (CRITSPEEDSEL = 1),

- set the critical speed ranges as in the figure below.



1	CRITSPEED1LO = 540 rpm
2	CRITSPEED1HI = 690 rpm
3	CRITSPEED2LO = 1380 rpm
4	CRITSPEED2HI = 1560 rpm

Output OUTACTIVE is set to 1 when the output reference (REFOUTPUT) is different from the input reference (REFINPUT).

The output is limited by the defined minimum and maximum limits (MIN and MAX).

Output OUTSTATE indicates in which critical speed range the operation point is.

Inputs Critical speed activation input (CRITSPEEDSEL): Boolean

Reference input (REFINPUT): REAL

Minimum/maximum critical speed range input (CRITSPEEDNLO / CRITSPEEDNHI): REAL

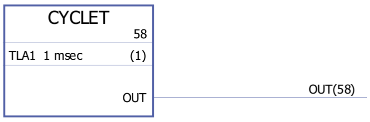
Minimum/maximum input (MIN/MAX): REAL

Outputs Reference output (REFOUTPUT): REAL
Output state (OUTSTATE): REAL
Output active (OUTACTIVE): Boolean

CYCLET

(10074)

Illustration



Execution time 0.00 µs

Operation Output (OUT) is the execution time of the selected function block.

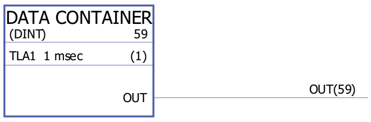
Inputs -

Outputs Output (OUT): DINT. 1 = 1 µs

DATA CONTAINER

(10073)

Illustration



Execution time 0.00 µs

Operation Output (OUT) is the array data used by the XTAB and YTAB tables in block FUNG-1V (page 340). Note that the array is defined with the output pin.

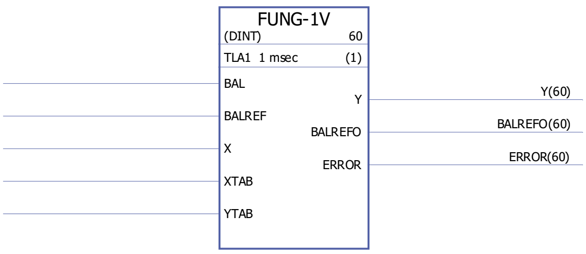
Inputs -

Outputs The output data type and the number of coordinate pairs are selected by the user.
Output (OUT): DINT, INT, REAL or REAL24

FUNG-1V

(10072)

Illustration



Execution time 9.29 µs

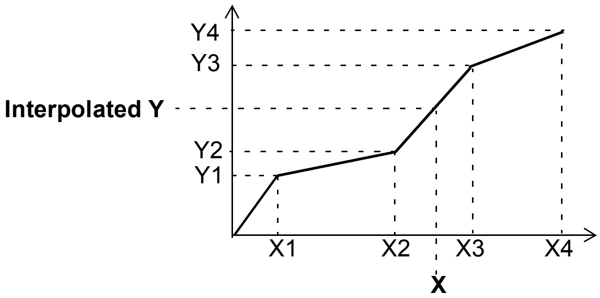
Operation

The output (Y) at the value of the input (X) is calculated with linear interpolation from a piecewise linear function.

$Y = Y_k + (X - X_k)(Y_{k+1} - Y_k) / (X_{k+1} - X_k)$

The piecewise linear function is defined by the X and Y vector tables (XTAB and YTAB). For each X-value in the XTAB table, there is a corresponding Y-value in the YTAB table. The values in XTAB and YTAB must be in ascending order (i.e. from low to high).

XTAB and YTAB values are defined with the DriveSPC tool.



X table (XTAB)	**Y table (YTAB)**
X1	Y1
X2	Y2
X3	Y3
…	…
X9	Y9

The balancing function (BAL) permits the output signal to track an external reference and gives a smooth return to the normal operation. If BAL is set to 1, output Y is set to the value of the balance reference input (BALREF). The X value which corresponds to this Y value is calculated with linear interpolation and it is indicated by the balance reference output (BALREFO).

If the X input is outside the range defined by the XTAB table, the output Y is set to the highest or lowest value in the YTAB table and the ERROR output is set to 1.

If BALREF is outside the range defined by the YTAB table when balancing is activated (BAL: 0 -> 1), the output Y is set to the value of the BALREF input and BALREFO output is set to the highest or lowest value in the XTAB table. (ERROR output is 0).

ERROR output is set to 1 when the number of the XTAB and YTAB inputs are different. When ERROR is 1, the FUNG-1V block will not function. XTAB and YTAB tables are defined in the DATA CONTAINER block (on page 340).

Inputs

The input data type is selected by the user.

X value input (X): DINT, INT, REAL, REAL24

Balance input (BAL): Boolean

Balance reference input (BALREF): DINT, INT, REAL, REAL24.

X table input (XTAB): DINT, INT, REAL, REAL24

Y table input (YTAB): DINT, INT, REAL, REAL24

Outputs

Y value output (Y): DINT, INT, REAL, REAL24

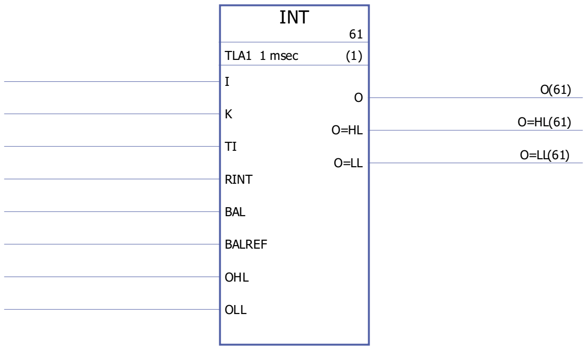
Balance reference output (BALREFO): DINT, INT, REAL, REAL24

Error output (ERROR): Boolean

INT

(10065)

Illustration



Execution time 4.73 µs

Operation The output (O) is the integrated value of the input (I):

O(t) = K/TI (∫ I(t) dt)

Where TI is the integration time constant and K is the integration gain.

The step response for the integration is:

O(t) = K × I(t) × t/TI

The transfer function for the integration is:

G(s) = K 1/sTI

The output value is limited according to the defined minimum and maximum limits (OLL and OHL). If the value is below the minimum value, output O = LL is set to 1. If the value exceeds the maximum value, output O = HL is set to 1. The output (O) retains its value when the input signal I(t) = 0.

The integration time constant is limited to value 2147483 ms. If the time constant is negative, zero time constant is used.

If the ratio between the cycle time and the integration time constant Ts/TI < 1, Ts/TI is set to 1.

The integrator is cleared when the reset input (RINT) is set to 1.

If BAL is set to 1, output O is set to the value of the input BALREF. When BAL is set back to 0, normal integration operation continues.

Inputs Input (I): REAL

Gain input (K): REAL

Integration time constant input (TI): DINT, 0…2147483 ms

Integrator reset input (RINT): Boolean

Balance input (BAL): Boolean

Balance reference input (BALREF): REAL

Output high limit input (OHL): REAL

Output low limit input (OLL): REAL

Outputs Output (O): REAL

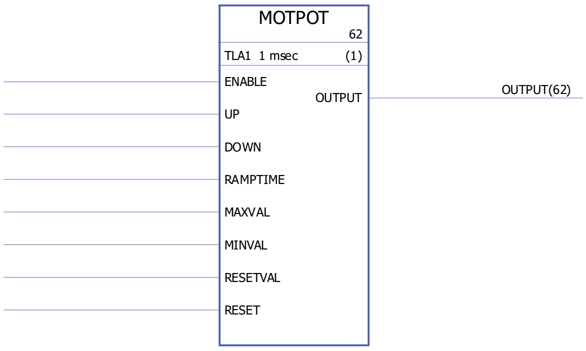
High limit output (O=HL): Boolean

Low limit output (O=LL): Boolean

MOTPOT

(10067)

Illustration



Execution time 2.92 µs

Operation

The motor potentiometer function controls the rate of change of the output from the minimum to the maximum value and vice versa.

The function is enabled by setting the ENABLE input to 1. If the up input (UP) is 1, the output reference (OUTPUT) is increased to the maximum value (MAXVAL) with the defined ramp time (RAMPTIME). If the down input (DOWN) is 1, the output value is decreased to the minimum value (MINVAL) with the defined ramp time. If the up and down inputs are activated/deactivated simultaneously, the output value is not increased/ decreased.

If the RESET input is 1, the output will be reset to the value defined by the reset value input (RESETVAL) or to the value defined by the minimum input (MINVAL), whichever is higher.

If the ENABLE input is 0, the output is zero.

During power recycle the previous values can be stored to the memory (storing must be activated by the user). **Note:** Memory storing is not supported yet.

Digital inputs are normally used as up and down inputs.

Inputs

Function enable input (ENABLE): Boolean

Up input (UP): Boolean

Down input (DOWN): Boolean

Ramp time input (RAMPTIME): REAL (seconds) (i.e. the time required for the output to change from the minimum to the maximum value or from the maximum to the minimum value)

Maximum reference input (MAXVAL): REAL

Minimum reference input (MINVAL): REAL

Reset value input (RESETVAL): REAL

Reset input (RESET): Boolean

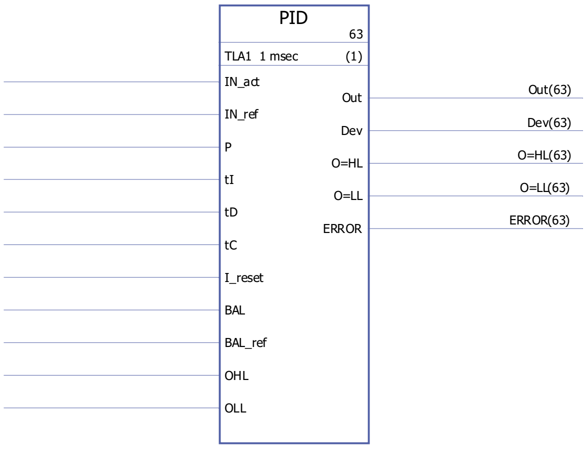
Outputs

Output (OUTPUT) REAL

PID

(10075)

Illustration



Execution time 15.75 µs

Operation

The PID controller can be used for closed-loop control systems. The controller includes anti-windup correction and output limitation.

The PID controller output (Out) before limitation is the sum of the proportional (U_P), integral (U_I) and derivative (U_D) terms:

$Out_{unlimited}(t) = U_P(t) + U_I(t) + U_D(t)$

$U_P(t) = P \times Dev(t)$

$U_I(t) = P/tI \times [\int Dev(\tau)d\tau + tC \times (Out(t) - Out_{unlimited}(t))]$

$U_D(t) = P \times tD \times d(Dev(t))/dt$

Integrator:

The integral term can be cleared by setting I_reset to 1. Note that the anti-windup correction is simultaneously disabled. When I_reset is 1, the controller acts as a PD controller.

If integration time constant tI is 0, the integral term will not be updated.

Smooth return to normal operation is guaranteed after errors or abrupt input value changes. This is achieved by adjusting the integral term so that the output will retain its previous value during these situations.

Limitation:

The output is limited by the defined minimum and maximum values, OLL and OHL:

If the actual value of the output reaches the specified minimum limit, output O=LL is set to 1.

If the actual value of the output reaches the specified maximum limit, output O=HL is set to 1.

Smooth return to normal operation after limitation is requested if and only if the anti-windup correction is not used, i.e. when tI = 0 or tC = 0.

Error codes:

Error codes are indicated by the error output (ERROR) as follows

Error code	Description
1	The minimum limit (OLL) exceeds the maximum limit (OHL).
2	Overflow with Up, Ui, or Ud calculation

Balancing:

The balancing function (BAL) permits the output signal to track an external reference and gives a smooth return to the normal operation. If BAL is set to 1, the output (Out) is set to the value of the balance reference input (BAL_ref). Balance reference is limited by the defined minimum and maximum limits (OLL and OHL).

Anti-windup:

Anti-windup correction time constant is defined by input tC. If tC = 0 or tI = 0, anti-windup correction is disabled.

Inputs

Proportional gain input (P): REAL
Integration time constant input (tI): REAL. 1 = 1 ms
Derivation time constant input (tD): REAL. 1 = 1 ms
Antiwind-up correction time constant input (tC): IQ6. 1 = 1 ms
Output high limit input (OHL): REAL
Output low limit input (OLL): REAL
Actual input (IN_act): REAL
Reference input (IN_ref): REAL
Integrator reset input (I_reset): Boolean
Balance input (BAL): Boolean
Balance reference input (BAL_ref): REAL

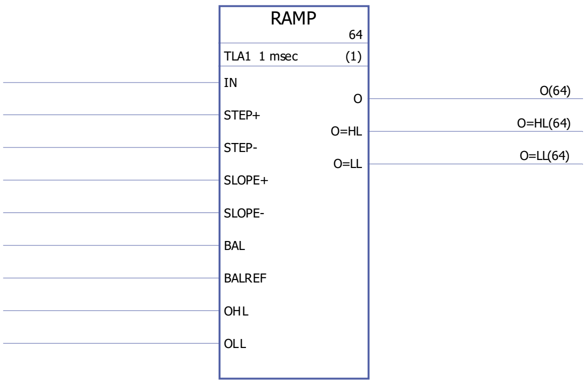
Outputs

Output (Out): REAL
Error code output (ERROR): INT32
Deviation output (Dev): REAL (= actual -reference = IN_act - IN_ref)
High limit output (O=HL): Boolean
Low limit output (O=LL): Boolean

RAMP

(10066)

Illustration



Execution time 4.23 µs

Operation

Limits the rate of the change of the signal.

The input signal (IN) is connected directly to the output (O) if the input signal does not exceed the defined step change limits (STEP+ and STEP-). If the input signal change exceeds these limits, the output signal change is limited by the maximum step change (STEP+/STEP- depending on the direction of rotation). After this the output signal is accelerated/decelerated according to the defined ramp times (SLOPE+/SLOPE-) until the input and output signal values are equal.

The output is limited by the defined minimum and maximum values (OLL and OHL):

If the actual value of the output exceeds the specified minimum limit (OLL), output O=LL is set to 1.

If the actual value of the output exceeds the specified maximum limit (OHL), output O=HL is set to 1.

If balancing input (BAL) is set to 1, the output (O) is set to the value of the balance reference input (BAL_ref). Balancing reference is also limited by the defined minimum and maximum values (OLL and OHL).

Inputs

Input (IN): REAL
Maximum positive step change input (STEP+): REAL
Maximum negative step change input (STEP-): REAL
Positive ramp input (SLOPE+): REAL
Negative ramp input (SLOPE-): REAL
Balance input (BAL): Boolean
Balance reference input (BALREF): REAL
Output high limit input (OHL): REAL
Output low limit input (OLL): REAL

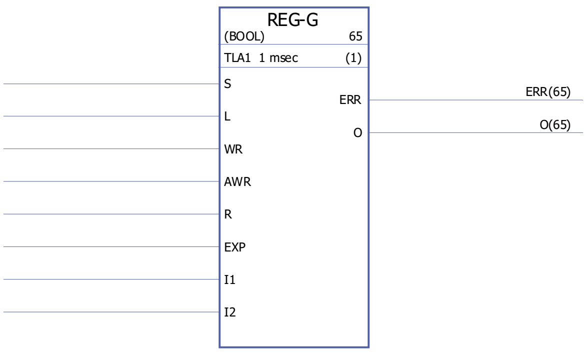
Outputs

Output (O): REAL
High limit output (O=HL): Boolean
Low limit output (O=LL): Boolean

REG-G

(10102)

Illustration



Execution time -

Operation

Assembles individual variables to a single variable of array data type. The data type can be INT, DINT, REAL16, REAL24 or Boolean.

When input S is set, data is continuously assembled at the group variable of the output. The group variable of the output consists of group data from the EXP input and the values of the inputs I1…1n (in this order). The element acts as a latch when input S is reset; the latest data assembled then remains at the output.

If S is reset and L changes state from 0 to 1, an assembly is performed to output O during this program cycle. If S or R is set, L has no effect.

Data can be changed at an optional place by specifying the address (integer 1…C2) through the AWR input. The new data value is entered through the input to the specified address when WR goes from 0 to 1. If AWR is 0 and WR goes to 1, array data is read from the input EXP to their respective places. Places corresponding to the ordinary inputs are not affected.

When input R is set, data at all places in the array register is cleared and all further entry is prevented. R overrides both S and L.

If WR is set, the address at AWR is checked and if its value is greater than the number of inputs, or if it is negative, the error output ERR is set to 1. If the resulting output array (EXP and the inputs combined) is longer than supported, ERR is set to 2. Otherwise ERR is 0.

Whenever an error is detected, ERR is set within one cycle. No place in the register is affected when an error occurs.

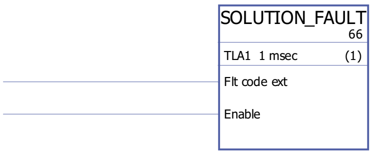
Inputs Set (S): Boolean, INT, DINT, REAL, REAL24
Load (L): Boolean, INT, DINT, REAL, REAL24
Write (WR): Boolean, INT, DINT, REAL, REAL24
Write address (AWR): INT
Reset (R): Boolean
Expander (EXP): IArray
Data input (I1…In): Boolean, INT, DINT, REAL, REAL24

Outputs Error (ERR): INT
Array data output (O): OC1

SOLUTION_FAULT

(10097)

Illustration



Execution time -

Operation When the block is enabled (by setting the Enable input to 1), a fault (F-0317 SOLUTION FAULT) is generated by the drive. The value of the Flt code ext input is recorded by the fault logger.

Inputs Fault code extension (Flt code ext): DINT
Generate fault (Enable): Boolean

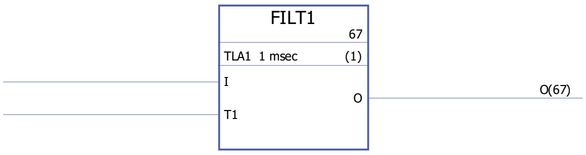
Outputs -

Filters

FILT1

(10069)

Illustration



Execution time 7.59 µs

Operation The output (O) is the filtered value of the input (I) value and the previous output value (O_{prev}). The FILT1 block acts as 1st order low pass filter.

Note: Filter time constant (T1) must be selected so that T1/Ts < 32767. If the ratio exceeds 32767, it is considered as 32767. Ts is the cycle time of the program in ms.

If T1 < Ts, the output value is the input value.

The step response for a single pole low pass filter is:

$O(t) = I(t) \times (1 - e^{-t/T1})$

The transfer function for a single pole low pass filter is:

$G(s) = 1/(1 + sT1)$

Inputs Input (I): REAL

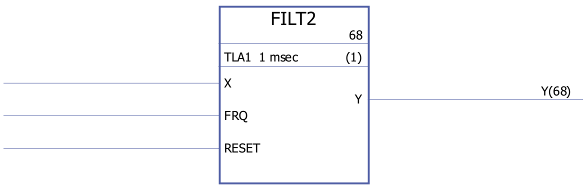
Filter time constant input (T1): DINT, 1 = 1 ms

Outputs Output (O): REAL

FILT2

(10070)

Illustration



Execution time 6.30 µs

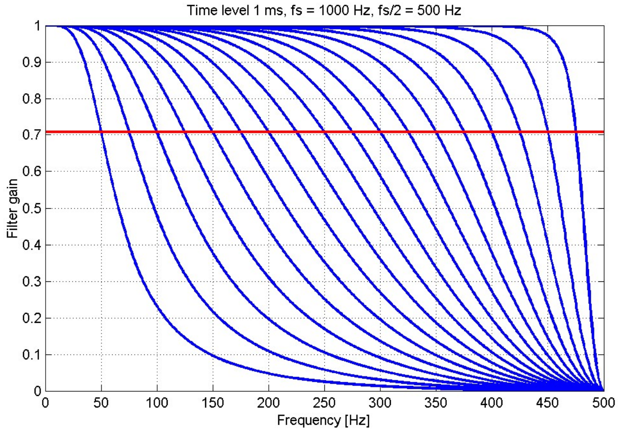
Operation The output (Y) is the filtered value of the input (X). The FILT2 block acts as a 2nd order low pass filter.

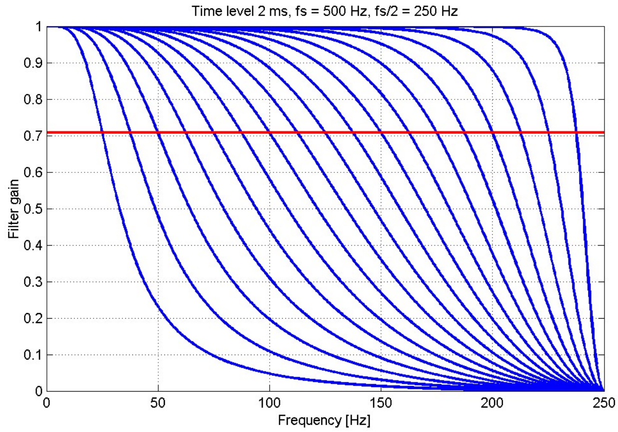
When the RESET input value is set to 1, the input is connected to the output without filtering.

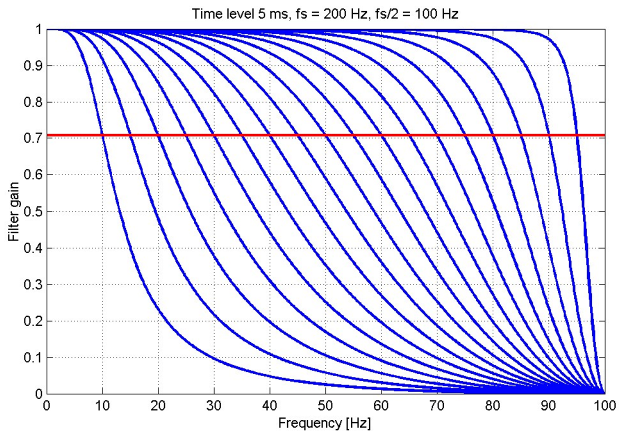
Notes:

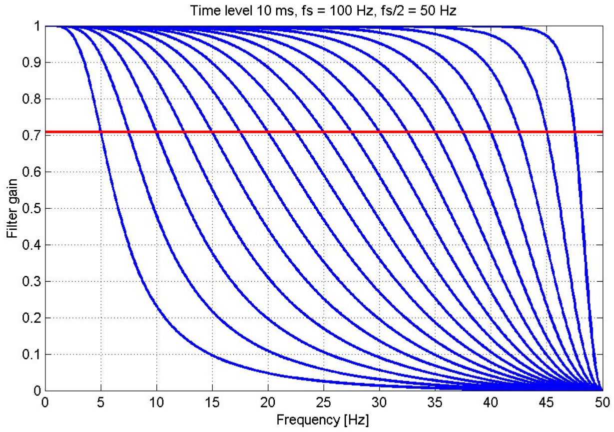
- The -3 dB cutoff frequency (FRQ) is limited to its maximum value (16383 Hz).
- The frequency of the input signal must be less than half of sampling frequency (fs) – any higher frequencies are aliased to the allowable range. The sampling frequency is defined by the time level of the block; for example, 1 ms corresponds to a sampling frequency of 1000 Hz.

The following diagrams show the frequency responses for 1, 2, 5 and 10 ms time levels. The -3 dB cutoff level is represented as the horizontal line at 0.7 gain.


Time level 1 ms, fs = 1000 Hz, fs/2 = 500 Hz
Filter gain
Frequency [Hz]
0
0.1
0.2
0.3
0.4
0.5
0.6
0.7
0.8
0.9
1
0
50
100
150
200
250
300
350
400
450
500


Time level 2 ms, fs = 500 Hz, fs/2 = 250 Hz
Filter gain
Frequency [Hz]
0
0.1
0.2
0.3
0.4
0.5
0.6
0.7
0.8
0.9
1
0
50
100
150
200
250


Time level 5 ms, fs = 200 Hz, fs/2 = 100 Hz
Filter gain
Frequency [Hz]
0
0.1
0.2
0.3
0.4
0.5
0.6
0.7
0.8
0.9
1
0
10
20
30
40
50
60
70
80
90
100



Inputs

Input (X): REAL

-3 dB cutoff frequency input (FRQ): DINT (0…16383 Hz)

Reset input (RESET): Boolean

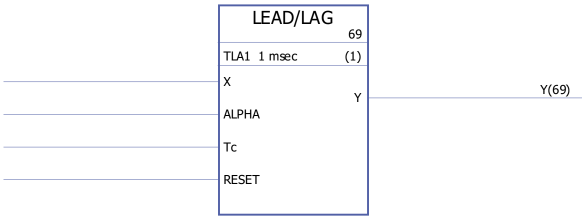
Outputs

Output (Y): REAL

LEAD/LAG

(10071)

Illustration



Execution time 5.55 µs

Operation

The output (Y) is the filtered value of the input (X). When ALPHA > 1, the function block acts as a lead filter. When ALPHA < 1, the function block acts as a lag filter. When ALPHA = 1, no filtering occurs.

The transfer function for a lead/lag filter is:

$(1 + ALPHAT_c s) / (1 + T_c s)$

When RESET input is 1, the input value (X) is connected to the output (Y).

If ALPHA or Tc < 0, the negative input value is set to zero before filtering.

Inputs

Input (X): REAL

Lead/Lag filter type input (ALPHA): REAL

Time constant input (Tc): REAL

Reset input (RESET): Boolean

Outputs

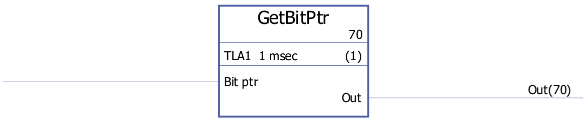
Output (Y): REAL

Parameters

GetBitPtr

(10099)

Illustration



Execution time -

Operation Reads the status of one bit within a parameter value cyclically.
The Bit ptr input specifies the parameter group, index and bit to be read.
The output (Out) provides the value of the bit.

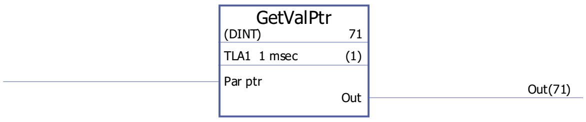
Inputs Parameter group, index and bit (Bit ptr): DINT

Outputs Bit status (Out): DINT

GetValPtr

(10098)

Illustration



Execution time -

Operation Reads the value of a parameter cyclically.
The Par ptr input specifies the parameter group and index to be read.
The output (Out) provides the value of the parameter.

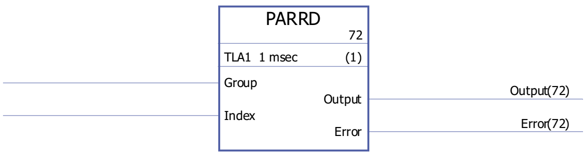
Inputs Parameter group and index (Par ptr): DINT

Outputs Parameter value (Out): DINT

PARRD

(10082)

Illustration



Execution time 6.00 µs

Operation

Reads the value of a parameter (specified by the Group and Index inputs). If the parameter is a pointer parameter, the Output pin provides the number of the source parameter instead of its value.

Error codes are indicated by the error output (Error) as follows:

Error code	Description
0	No error
≠ 0	Error

See also blocks PARRDINTR and PARRDPTR.

Inputs

Parameter group input (Group): DINT

Parameter index input (Index): DINT

Outputs

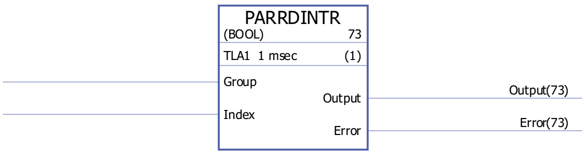
Output (Output): DINT

Error output (Error): DINT

PARRDINTR

(10101)

Illustration



Execution time -

Operation

Reads the internal (non-scaled) value of a parameter (specified by the Group and Index inputs). The value is provided by the Output pin.

Error codes are indicated by the error output (Error) as follows:

Error code	Description
0	No error or busy
≠ 0	Error

Inputs

Parameter group (Group): DINT

Parameter index (Index): DINT

Outputs

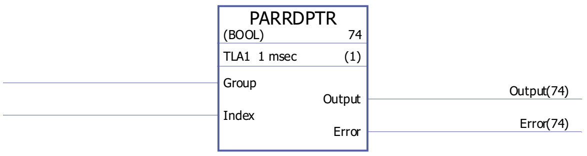
Output (Output): Boolean, INT, DINT, REAL, REAL24

Error output (Error): DINT

PARRDPTR

(10100)

Illustration



Execution time -

Operation

Reads the internal (non-scaled) value of the source of a pointer parameter. The pointer parameter is specified using the Group and Index inputs.

The value of the source selected by the pointer parameter is provided by the Output pin.

Error codes are indicated by the error output (Error) as follows:

Error code	Description
0	No error or busy
≠ 0	Error

Inputs

Parameter group (Group): DINT

Parameter index (Index): DINT

Outputs

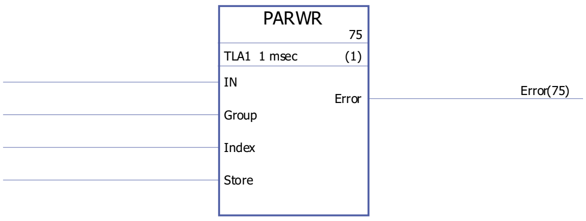
Output (Output): Boolean, INT, DINT, REAL, REAL24

Error output (Error): DINT

PARWR

(10080)

Illustration



Execution time 14.50 µs

Operation

The input value (IN) is written to the defined parameter (Group and Index).

The new parameter value is stored to the flash memory if the store input (Store) is 1.
Note: Cyclic parameter value storing can damage the memory unit. Parameter values should be stored only when necessary.

Error codes are indicated by the error output (Error) as follows:

Error code	Description
0	No error
< > 0	Error

Inputs

Input (IN): DINT

Parameter group input (Group): DINT

Parameter index input (Index): DINT

Store input (Store): Boolean

Outputs

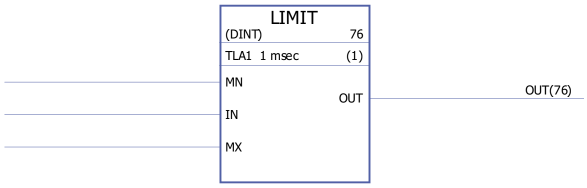
Error output (Error): DINT

Selection

LIMIT

(10052)

Illustration



Execution time 0.53 µs

Operation The output (OUT) is the limited input (IN) value. Input is limited according to the minimum (MN) and maximum (MX) values.

Inputs The input data type is selected by the user.

Maximum input limit (MX): INT, DINT, REAL, REAL24

Minimum input limit (MN): INT, DINT, REAL, REAL24

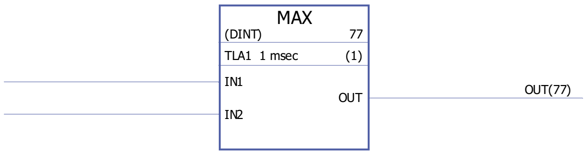
Input (IN): INT, DINT, REAL, REAL24

Outputs Output (OUT): INT, DINT, REAL, REAL24

MAX

(10053)

Illustration



Execution time 0.81 µs (when two inputs are used) + 0.53 µs (for every additional input). When all inputs are used, the execution time is 16.73 µs.

Operation The output (OUT) is the highest input value (IN).

Inputs The input data type and the number of inputs (2…32) are selected by the user.

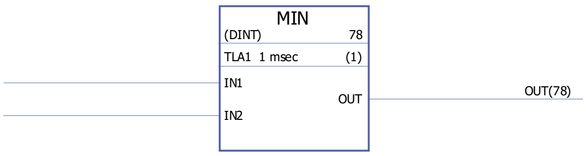
Input (IN1…IN32): INT, DINT, REAL, REAL24

Outputs Output (OUT): INT, DINT, REAL, REAL24

MIN

(10054)

Illustration



Execution time 0.81 µs (when two inputs are used) + 0.52 µs (for every additional input). When all inputs are used, the execution time is 16.50 µs.

Operation The output (OUT) is the lowest input value (IN).

Inputs The input data type and the number of inputs (2…32) are selected by the user.

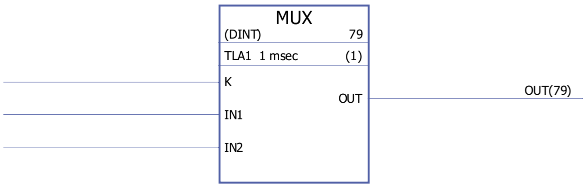
Input (IN1…IN32): INT, DINT, REAL, REAL24

Outputs Output (OUT): INT, DINT, REAL, REAL24

MUX

(10055)

Illustration



Execution time 0.70 µs

Operation The value of an input (IN) selected by the address input (K) is stored to the output (OUT).

If the address input is 0, negative or exceeds the number of the outputs, the output is 0.

Inputs The input data type and number of inputs (2…32) are selected by the user.

Address input (K): DINT

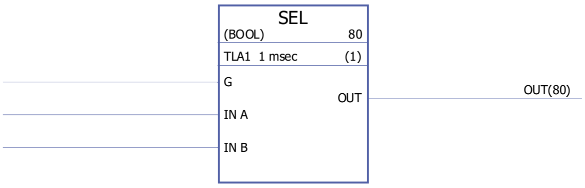
Input (IN1…IN32): INT, DINT, REAL, REAL24

Outputs Output (OUT): INT, DINT, REAL, REAL24

SEL

(10056)

Illustration



Execution time 1.53 µs

Operation The output (OUT) is the value of the input (IN) selected by the selection input (G).

If G = 0: OUT = IN A.

If G = 1: OUT = IN B.

Inputs The input data type is selected by the user.

Selection input (G): Boolean

Input (IN A, IN B): Boolean, INT, DINT, REAL, REAL24

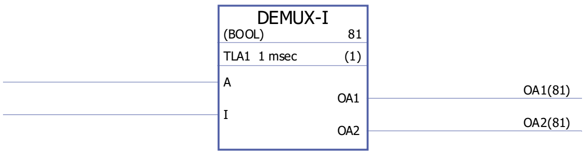
Outputs Output (OUT): Boolean, INT, DINT, REAL, REAL24

Switch & Demux

DEMUX-I

(10061)

Illustration



Execution time 1.38 µs (when two inputs are used) + 0.30 µs (for every additional input). When all inputs are used, the execution time is 10.38 µs.

Operation Input (I) value is stored to the output (OA1…OA32) selected by the address input (A). All other outputs are 0.

If the address input is 0, negative or exceeds the number of the outputs, all outputs are 0.

Inputs The input data type is selected by the user.

Address input (A): DINT

Input (I): INT, DINT, Boolean, REAL, REAL24

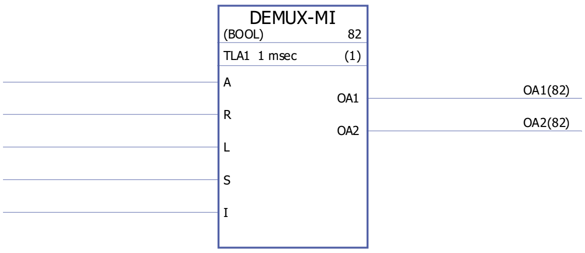
Outputs The number of the output channels (2…32) is selected by the user.

Output (OA1…OA32): INT, DINT, REAL, REAL24, Boolean

DEMUX-MI

(10062)

Illustration



Execution time 0.99 µs (when two inputs are used) + 0.25 µs (for every additional input). When all inputs are used, the execution time is 8.4 µs.

Operation

The input (I) value is stored to the output (OA1…OA32) selected by the address input (A) if the load input (L) or the set input (S) is 1. When the load input is set to 1, the input (I) value is stored to the output only once. When the set input is set to 1, the input (I) value is stored to the output every time the block is executed. The set input overrides the load input.

If the reset input (R) is 1, all connected outputs are 0.

If the address input is 0, negative or exceeds the number of the outputs, all outputs are 0.

Example:

S	L	R	A	I	OA1	OA2	OA3	OA4
1	0	0	2	150	0	150	0	0
0	0	0	2	120	0	150	0	0
0	1	0	3	100	0	150	100	0
1	0	0	1	200	200	150	100	0
1	1	0	4	250	200	150	100	250
1	1	1	2	300	0	0	0	0

Inputs

The input data type is selected by the user.

Set input (S): Boolean

Load input (L): Boolean

Reset input (R): Boolean

Address input (A): DINT

Input (I): DINT, INT, REAL, REAL24, Boolean

Outputs

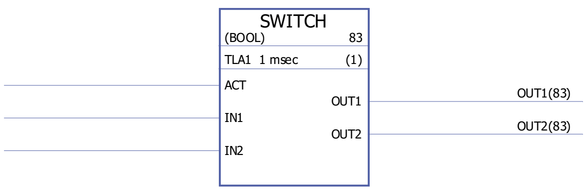
The number of the output channels (2…32) is selected by the user.

Output (OA1…OA32): DINT, INT, REAL, REAL24, Boolean

SWITCH

(10063)

Illustration



Execution time

0.68 µs (when two inputs are used) + 0.50 µs (for every additional input). When all inputs are used, the execution time is 15.80 µs.

Operation

The output (OUT) is equal to the corresponding input (IN) if the activate input (ACT) is 1. Otherwise the output is 0.

Inputs

The input data type and the number of inputs (2…32) are selected by the user.

Activate input (ACT): Boolean

Input (IN1…IN32): INT, DINT, REAL, REAL24, Boolean

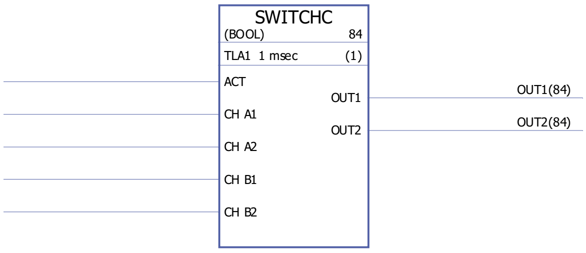
Outputs

Output (OUT1…OUT32): INT, DINT, REAL, REAL24, Boolean

SWITCHC

(10064)

Illustration



Execution time 1.53 µs (when two inputs are used) + 0.73 µs (for every additional input). When all inputs are used, the execution time is 23.31 µs.

Operation The output (OUT) is equal to the corresponding channel A input (CH A1…32) if the activate input (ACT) is 0. The output is equal to the corresponding channel B input (CH B1…32) if the activate input (ACT) is 1.

Inputs The input data type and the number of inputs (2…32) are selected by the user.

Activate input (ACT): Boolean

Input (CH A1…CH A32, CH B1…CH B32): INT, DINT, REAL, REAL24, Boolean

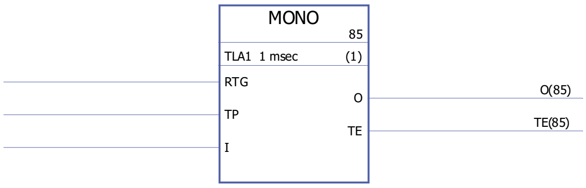
Outputs Output (OUT1…OUT32): INT, DINT, REAL, REAL24, Boolean

Timers

MONO

(10057)

Illustration



Execution time 1.46 µs

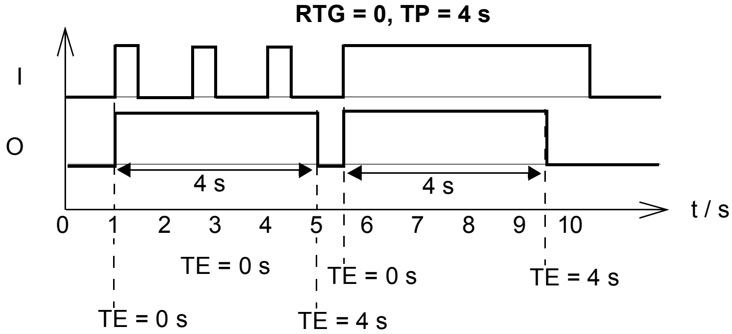
Operation The output (O) is set to 1 and the timer is started, if the input (I) is set to 1. The output is reset to 0 when the time defined by the time pulse input (TP) has elapsed. Elapsed time (TE) count starts when the output is set to 1 and stops when the output is set to 0.

If RTG is 0, a new input pulse during the time defined by TP has no effect on the function. The function can be restarted only after the time defined by TP has elapsed.

If RTG is 1, a new input pulse during the time defined by TP restarts the timer and sets the elapsed time (TE) to 0.

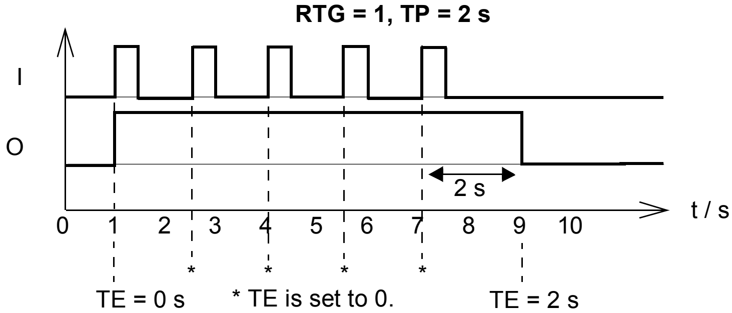
Example 1: MONO is not re-triggable, i.e. RTG = 0.

RTG = 0, TP = 4 s



Example 2: MONO is re-triggable, i.e. RTG = 1.

RTG = 1, TP = 2 s



Inputs Re-trigger input (RTG): Boolean

Input (I): Boolean

Time pulse input (TP): DINT (1 = µs)

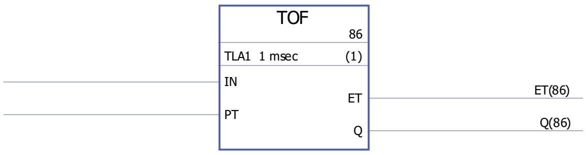
Outputs Output (O): Boolean

Time elapsed output (TE): DINT (1 = 1 µs)

TOF

(10058)

Illustration



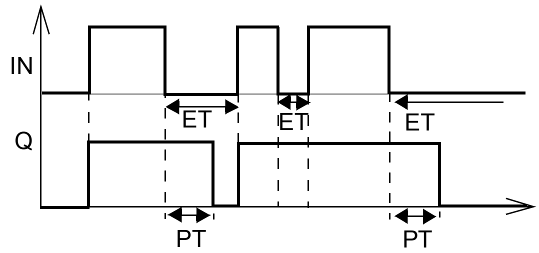
Execution time 1.10 µs

Operation

The output (Q) is set to 1, when the input (IN) is set to 1. The output is reset to zero when the input has been 0 for a time defined by the pulse time input (PT).

Elapsed time count (TE) starts when the input is set to 0 and stops when the input is set to 1.

Example:



Inputs

Input (IN): Boolean

Pulse time input (PT): DINT (1 = 1 µs)

Outputs

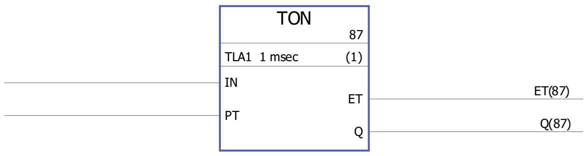
Output (Q): Boolean

Elapsed time output (ET): DINT (1 = 1 µs)

TON

(10059)

Illustration



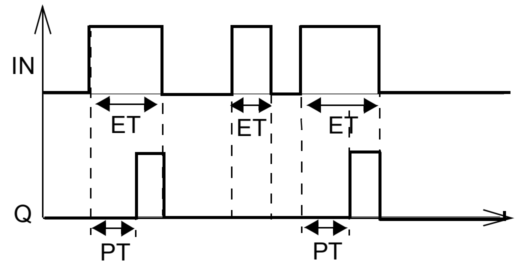
Execution time 1.22 µs

Operation

The output (Q) is set to 1 when the input (IN) has been 1 for a time defined by the pulse time input (PT). The output is set to 0, when the input is set to 0.

Elapsed time count (TE) starts when the input is set to 1 and stops when the input is set to 0.

Example:



Inputs

Input (IN): Boolean

Pulse time input (PT): DINT (1 = 1 µs)

Outputs

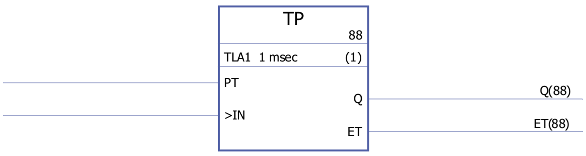
Output (Q): Boolean

Elapsed time output (ET): DINT (1 = 1 µs)

TP

(10060)

Illustration



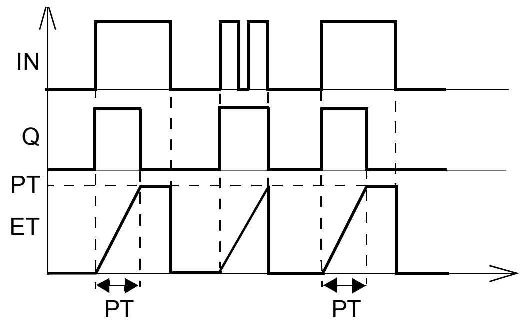
Execution time

1.46 µs

Operation

The output (Q) is set to 1 when the input (IN) is set to 1. The output is set to 0, when it has been 1 for a time defined by the pulse time input (PT).

Elapsed time count (TE) starts when the input is set to 1 and stops when the input is set to 0.



Inputs

Input (IN): Boolean

Pulse time input (PT): DINT (1 = 1 µs)

Outputs

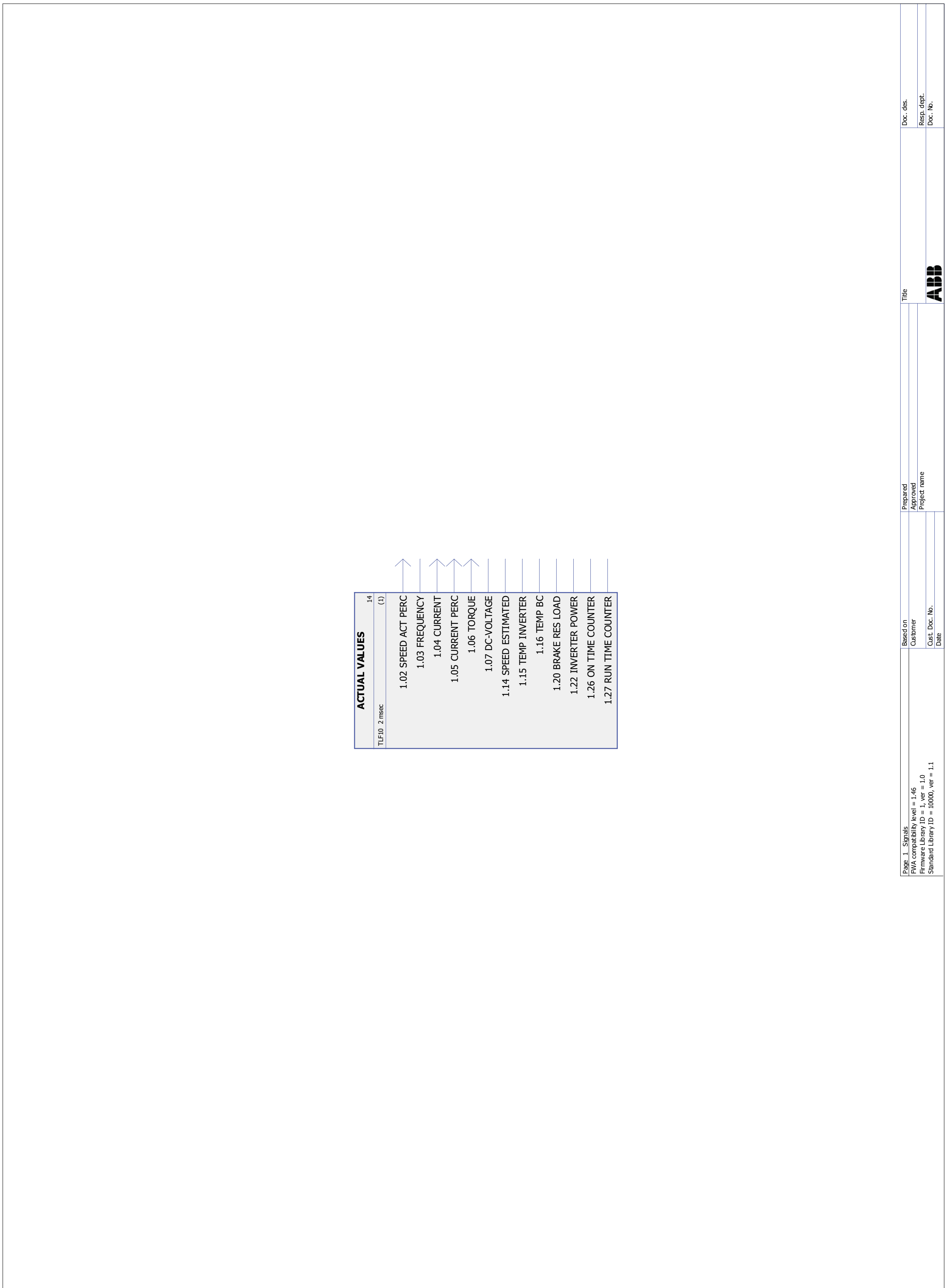
Output (Q): Boolean

Elapsed time output (ET): DINT (1 = 1 µs)

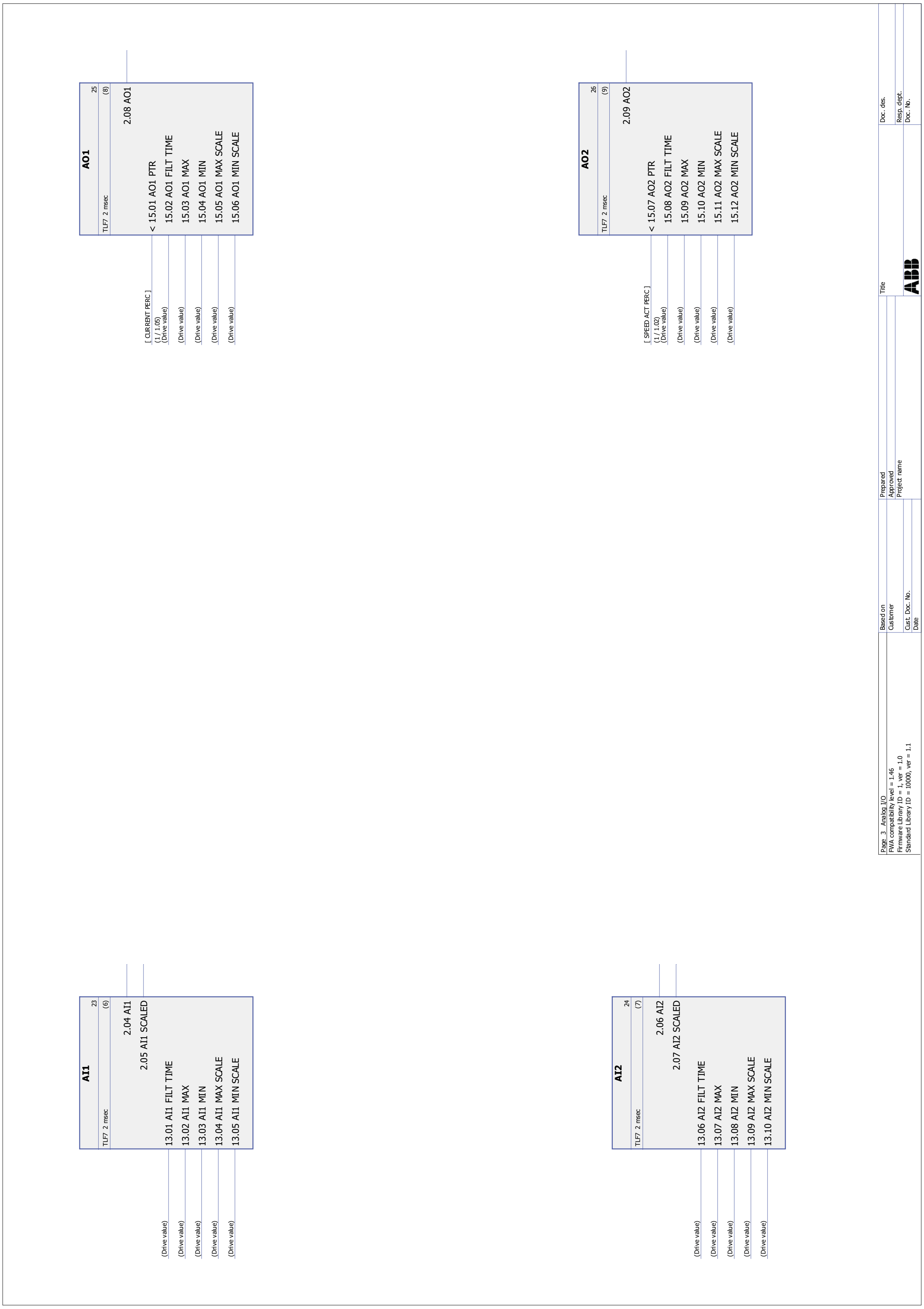
Application program template

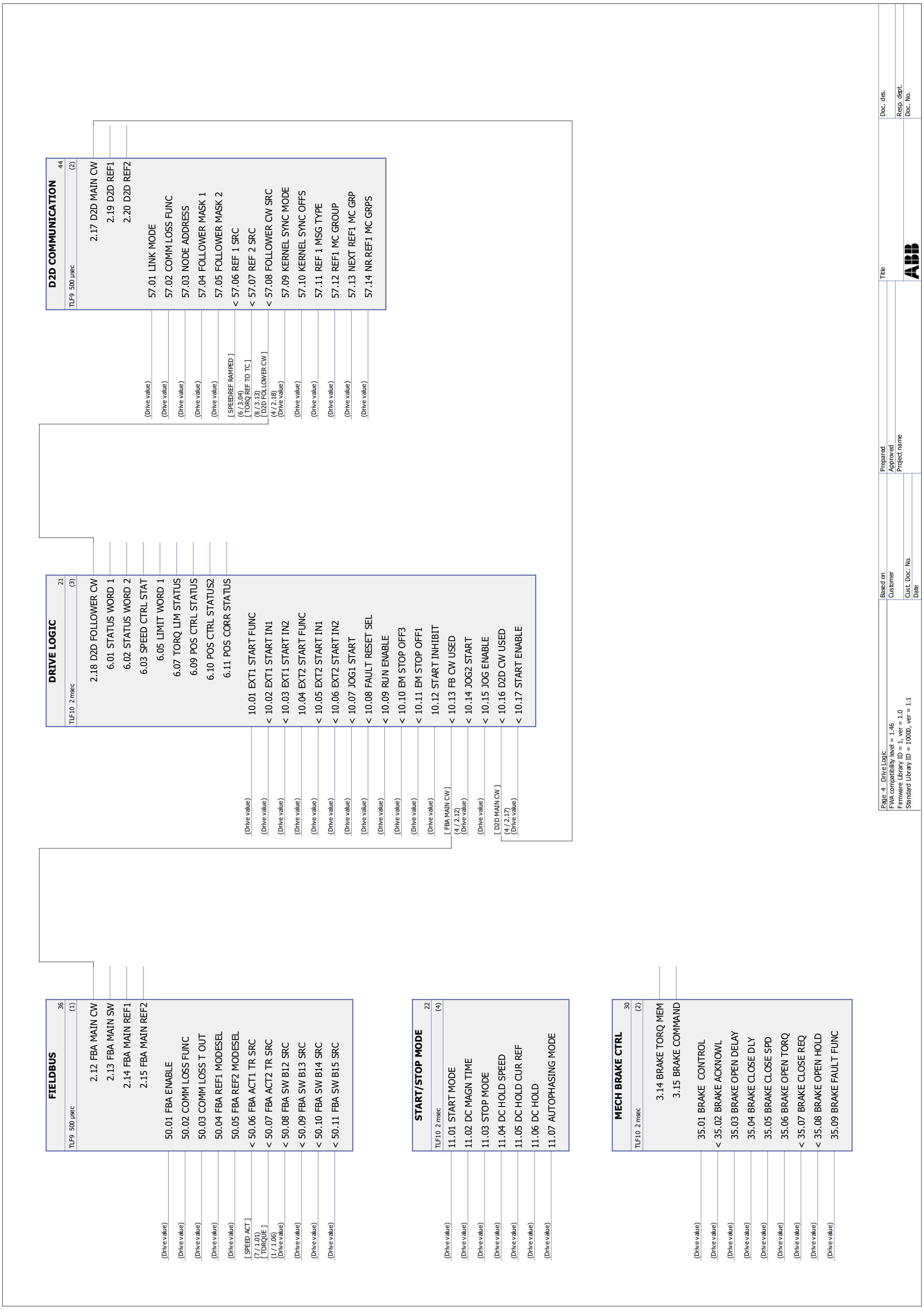
What this chapter contains

This chapter presents the application program template as displayed by the DriveSPC tool.

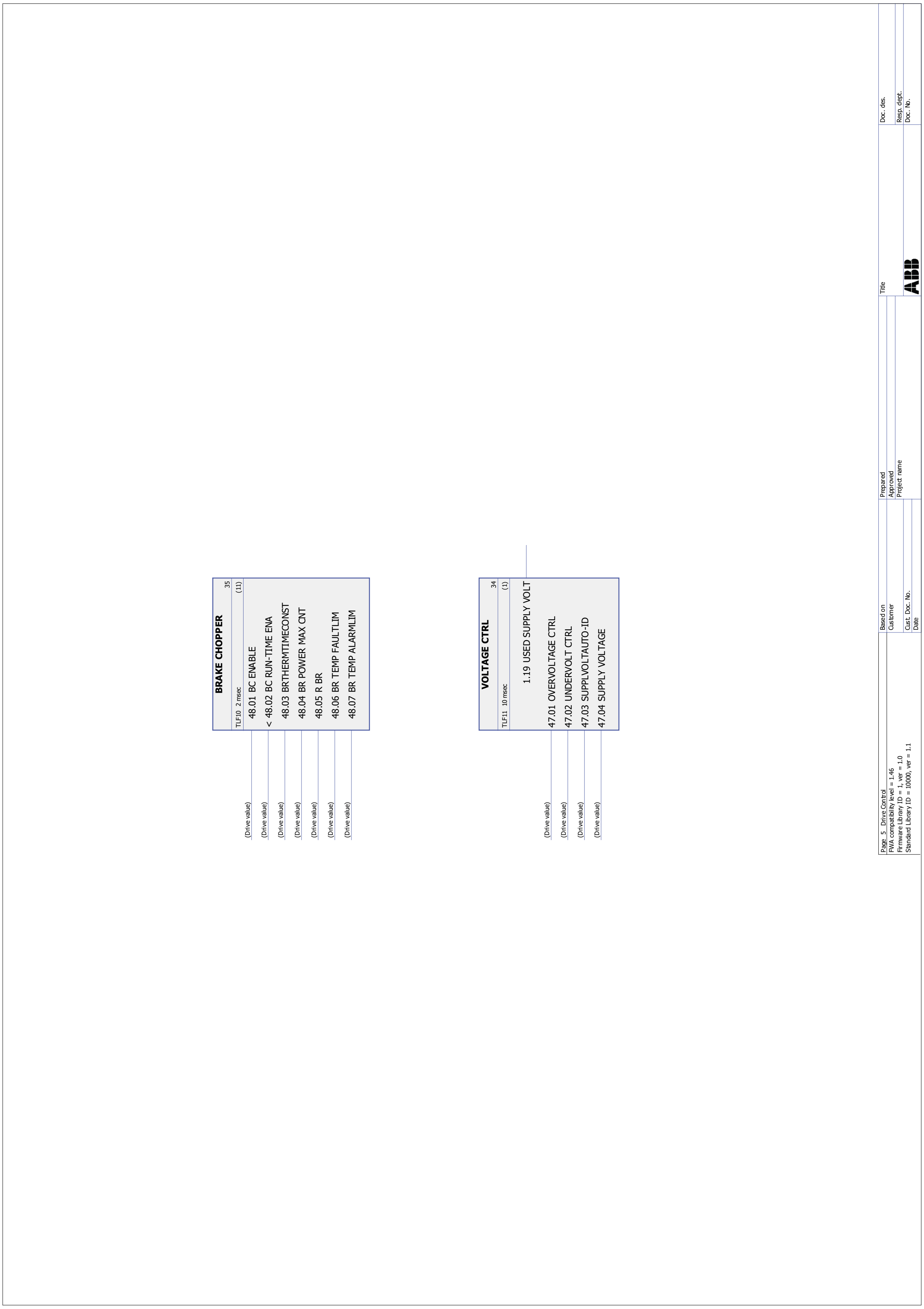

ACTUAL VALUES
14
TLF10 2 msec
(1)
1.02 SPEED ACT PERC
1.03 FREQUENCY
1.04 CURRENT
1.05 CURRENT PERC
1.06 TORQUE
1.07 DC-VOLTAGE
1.14 SPEED ESTIMATED
1.15 TEMP INVERTER
1.16 TEMP BC
1.20 BRAKE RES LOAD
1.22 INVERTER POWER
1.26 ON TIME COUNTER
1.27 RUN TIME COUNTER
Page 1 Signals
FWA compatibility level = 1.46
Firmware Library ID = 1, ver = 1.0
Standard Library ID = 10000, ver = 1.1
Based on
Customer
Cust. Doc. No.
Date
Prepared
Approved
Project name
Title
ABB
Doc. des.
Resp. dept.
Doc. No.


DIO1
TLF7 2 msec
18
(3)
2.03 DIO STATUS
2.03 Bit 0
12.01 DIO1 CONF
< 12.04 DIO1 OUT PTR
(Drive value)
(Drive value)
DIO2
TLF7 2 msec
19
(4)
2.03 DIO STATUS
2.10 DIO2 FREQ IN
2.03 Bit 1
12.02 DIO2 CONF
< 12.05 DIO2 OUT PTR
12.14 DIO2 F MAX
12.15 DIO2 F MIN
12.16 DIO2 F MAX SCALE
12.17 DIO2 F MIN SCALE
DIO3
TLF7 2 msec
20
(5)
2.03 DIO STATUS
2.11 DIO3 FREQ OUT
2.03 Bit 2
12.03 DIO3 CONF
< 12.06 DIO3 OUT PTR
< 12.07 DIO3 F OUT PTR
12.08 DIO3 F MAX
12.09 DIO3 F MIN
12.10 DIO3 F MAX SCALE
12.11 DIO3 F MIN SCALE
[SPEED ACT]
(7 / 1.01)
RO
TLF7 2 msec
17
(2)
2.02 RO STATUS
< 12.12 RO1 OUT PTR
DI
TLF7 2 msec
16
(1)
2.01 DI STATUS
12.13 DI INVERT MASK
Page 2 Digital I/O
FWA compatibility level = 1.46
Firmware Library ID = 1, ver = 1.0
Standard Library ID = 10000, ver = 1.1
Based on
Customer
Cust. Doc. No.
Date
Prepared
Approved
Project name
Title
Doc. des.
Resp. dept.
Doc. No.


AO1
TLF7 2 msec
25
(8)
2.08 AO1
[CURRENT PERC]
(1 / 1.05)
(Drive value)
< 15.01 AO1 PTR
15.02 AO1 FILT TIME
15.03 AO1 MAX
15.04 AO1 MIN
15.05 AO1 MAX SCALE
15.06 AO1 MIN SCALE
(Drive value)
AO2
TLF7 2 msec
26
(9)
2.09 AO2
[SPEED ACT PERC]
(1 / 1.02)
(Drive value)
< 15.07 AO2 PTR
15.08 AO2 FILT TIME
15.09 AO2 MAX
15.10 AO2 MIN
15.11 AO2 MAX SCALE
15.12 AO2 MIN SCALE
AI1
TLF7 2 msec
23
(6)
2.04 AI1
2.05 AI1 SCALED
13.01 AI1 FILT TIME
13.02 AI1 MAX
13.03 AI1 MIN
13.04 AI1 MAX SCALE
13.05 AI1 MIN SCALE
AI2
TLF7 2 msec
24
(7)
2.06 AI2
2.07 AI2 SCALED
13.06 AI2 FILT TIME
13.07 AI2 MAX
13.08 AI2 MIN
13.09 AI2 MAX SCALE
13.10 AI2 MIN SCALE
Page 3 Analog I/O
FWA compatibility level = 1.46
Firmware Library ID = 1, ver = 1.0
Standard Library ID = 10000, ver = 1.1
Based on
Customer
Cust. Doc. No.
Date
Prepared
Approved
Project name
Title
Doc. des.
Resp. dept.
Doc. No.



D2D COMMUNICATION
TLF9 500 µsec 44 (2)
2.17 D2D MAIN CW
2.19 D2D REF1
2.20 D2D REF2
57.01 LINK MODE
57.02 COMM LOSS FUNC
57.03 NODE ADDRESS
57.04 FOLLOWER MASK 1
57.05 FOLLOWER MASK 2
< 57.06 REF 1 SRC
< 57.07 REF 2 SRC
< 57.08 FOLLOWER CW SRC
57.09 KERNEL SYNC MODE
57.10 KERNEL SYNC OFFS
57.11 REF 1 MSG TYPE
57.12 REF1 MC GROUP
57.13 NEXT REF1 MC GRP
57.14 NR REF1 MC GRPS
[SPEEDREF RAMPED] (6 / 3.04)
[TORQ REF TO TC] (8 / 3.13)
[D2D FOLLOWER CW] (4 / 2.18)
(Drive value)
DRIVE LOGIC
TLF10 2 msec 21 (3)
2.18 D2D FOLLOWER CW
6.01 STATUS WORD 1
6.02 STATUS WORD 2
6.03 SPEED CTRL STAT
6.05 LIMIT WORD 1
6.07 TORQ LIM STATUS
6.09 POS CTRL STATUS
6.10 POS CTRL STATUS2
6.11 POS CORR STATUS
10.01 EXT1 START FUNC
< 10.02 EXT1 START IN1
< 10.03 EXT1 START IN2
10.04 EXT2 START FUNC
< 10.05 EXT2 START IN1
< 10.06 EXT2 START IN2
< 10.07 JOG1 START
< 10.08 FAULT RESET SEL
< 10.09 RUN ENABLE
< 10.10 EM STOP OFF3
< 10.11 EM STOP OFF1
10.12 START INHIBIT
< 10.13 FB CW USED
< 10.14 JOG2 START
< 10.15 JOG ENABLE
< 10.16 D2D CW USED
< 10.17 START ENABLE
[FBA MAIN CW] (4 / 2.12)
[D2D MAIN CW] (4 / 2.17)
FIELDBUS
TLF9 500 µsec 36 (1)
2.12 FBA MAIN CW
2.13 FBA MAIN SW
2.14 FBA MAIN REF1
2.15 FBA MAIN REF2
50.01 FBA ENABLE
50.02 COMM LOSS FUNC
50.03 COMM LOSS T OUT
50.04 FBA REF1 MODESEL
50.05 FBA REF2 MODESEL
< 50.06 FBA ACT1 TR SRC
< 50.07 FBA ACT2 TR SRC
< 50.08 FBA SW B12 SRC
< 50.09 FBA SW B13 SRC
< 50.10 FBA SW B14 SRC
< 50.11 FBA SW B15 SRC
[SPEED ACT] (7 / 1.01)
[TORQUE] (1 / 1.06)
START/STOP MODE
TLF10 2 msec 22 (4)
11.01 START MODE
11.02 DC MAGN TIME
11.03 STOP MODE
11.04 DC HOLD SPEED
11.05 DC HOLD CUR REF
11.06 DC HOLD
11.07 AUTOPHASING MODE
MECH BRAKE CTRL
TLF10 2 msec 30 (2)
3.14 BRAKE TORQ MEM
3.15 BRAKE COMMAND
35.01 BRAKE CONTROL
< 35.02 BRAKE ACKNOWL
35.03 BRAKE OPEN DELAY
35.04 BRAKE CLOSE DLY
35.05 BRAKE CLOSE SPD
35.06 BRAKE OPEN TORQ
< 35.07 BRAKE CLOSE REQ
< 35.08 BRAKE OPEN HOLD
35.09 BRAKE FAULT FUNC
Page 4 Drive Logic
FWA compatibility level = 1.46
Firmware Library ID = 1, ver = 1.0
Standard Library ID = 10000, ver = 1.1
Based on
Customer
Cust. Doc. No.
Date
Prepared
Approved
Project name
Title
Doc. des.
Resp. dept.
Doc. No.
ABB


BRAKE CHOPPER
TLF10 2 msec
35
(11)
48.01 BC ENABLE
< 48.02 BC RUN-TIME ENA
48.03 BRTHERMTIMECONST
48.04 BR POWER MAX CNT
48.05 R BR
48.06 BR TEMP FAULTLIM
48.07 BR TEMP ALARMLIM
(Drive value)
VOLTAGE CTRL
TLF11 10 msec
34
(1)
1.19 USED SUPPLY VOLT
47.01 OVERVOLTAGE CTRL
47.02 UNDERVOLT CTRL
47.03 SUPPLVOLTAUTO-ID
47.04 SUPPLY VOLTAGE
Page 5 Drive Control
FWA compatibility level = 1.46
Firmware Library ID = 1, ver = 1.0
Standard Library ID = 10000, ver = 1.1
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Customer
Cust. Doc. No.
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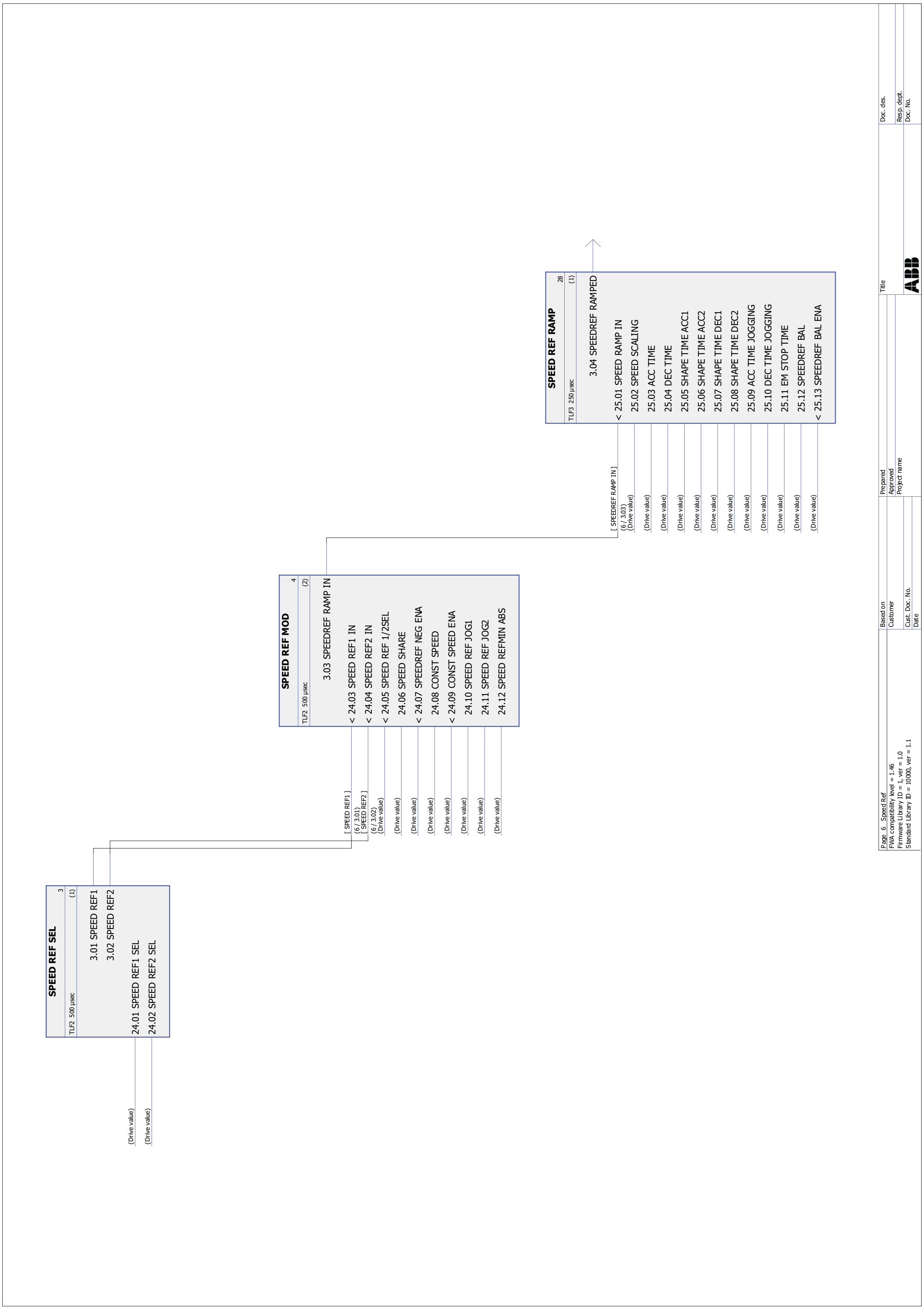
Doc. No.



SPEED REF SEL
TLF2 500 µsec
3
(1)
3.01 SPEED REF1
3.02 SPEED REF2
24.01 SPEED REF1 SEL
24.02 SPEED REF2 SEL
(Drive value)
(Drive value)
SPEED REF MOD
TLF2 500 µsec
4
(2)
3.03 SPEEDREF RAMP IN
< 24.03 SPEED REF1 IN
< 24.04 SPEED REF2 IN
< 24.05 SPEED REF 1/2SEL
24.06 SPEED SHARE
< 24.07 SPEEDREF NEG ENA
24.08 CONST SPEED
< 24.09 CONST SPEED ENA
24.10 SPEED REF JOG1
24.11 SPEED REF JOG2
24.12 SPEED REFMIN ABS
[SPEED REF1]
(6 / 3.01)
[SPEED REF2]
(6 / 3.02)
SPEED REF RAMP
TLF3 250 µsec
28
(1)
3.04 SPEEDREF RAMPED
< 25.01 SPEED RAMP IN
25.02 SPEED SCALING
25.03 ACC TIME
25.04 DEC TIME
25.05 SHAPE TIME ACC1
25.06 SHAPE TIME ACC2
25.07 SHAPE TIME DEC1
25.08 SHAPE TIME DEC2
25.09 ACC TIME JOGGING
25.10 DEC TIME JOGGING
25.11 EM STOP TIME
25.12 SPEEDREF BAL
< 25.13 SPEEDREF BAL ENA
[SPEEDREF RAMP IN]
(6 / 3.03)
Page 6 Speed Ref
FWA compatibility level = 1.46
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Standard Library ID = 10000, ver = 1.1
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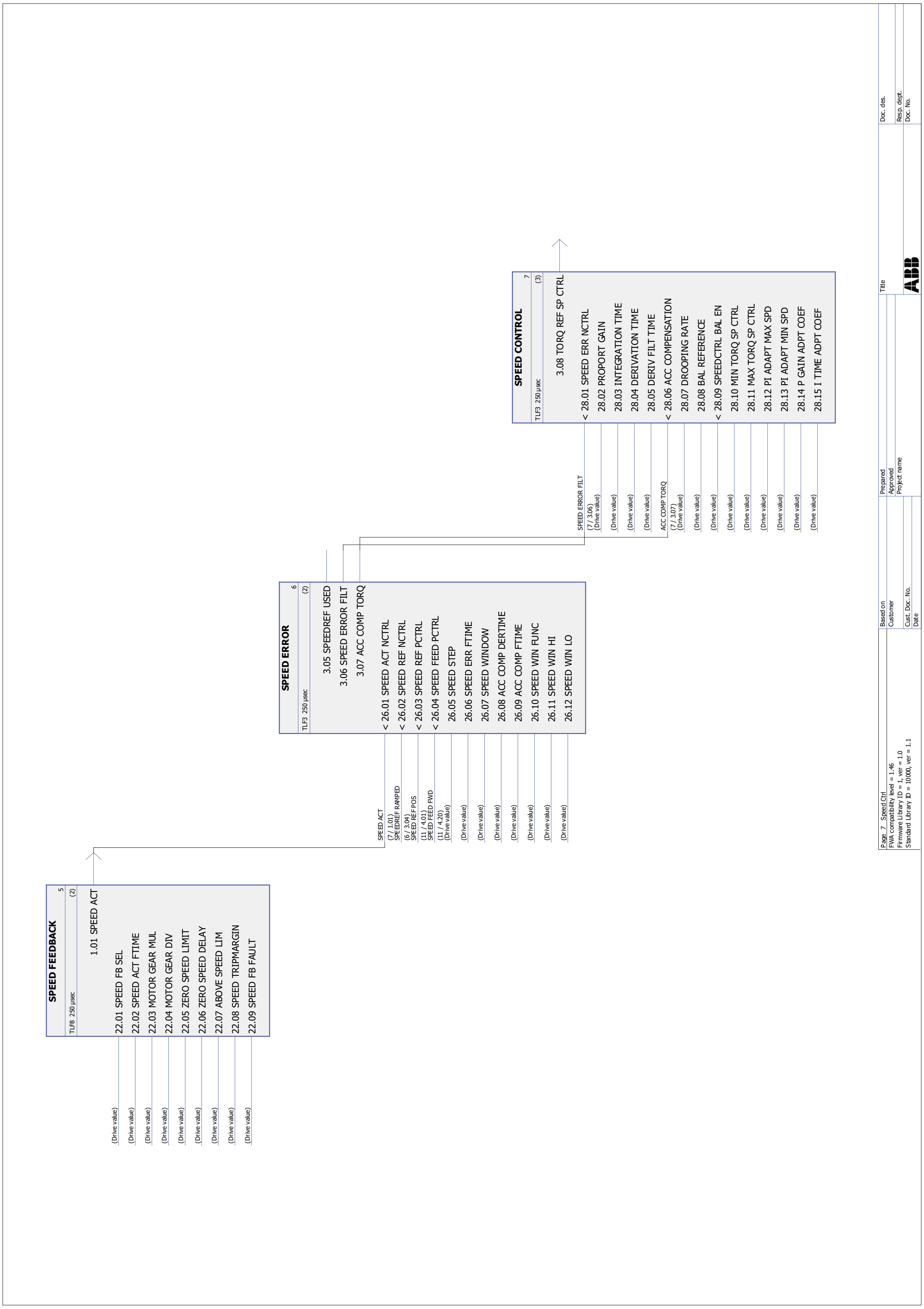
SPEED FEEDBACK
TLF8 250 µsec
5
(2)
1.01 SPEED ACT
22.01 SPEED FB SEL
22.02 SPEED ACT FTIME
22.03 MOTOR GEAR MUL
22.04 MOTOR GEAR DIV
22.05 ZERO SPEED LIMIT
22.06 ZERO SPEED DELAY
22.07 ABOVE SPEED LIM
22.08 SPEED TRIPMARGIN
22.09 SPEED FB FAULT
(Drive value)
SPEED ERROR
TLF3 250 µsec
6
(2)
3.05 SPEEDREF USED
3.06 SPEED ERROR FILT
3.07 ACC COMP TORQ
< 26.01 SPEED ACT NCTRL
< 26.02 SPEED REF NCTRL
< 26.03 SPEED REF PCTRL
< 26.04 SPEED FEED PCTRL
26.05 SPEED STEP
26.06 SPEED ERR FTIME
26.07 SPEED WINDOW
26.08 ACC COMP DERTIME
26.09 ACC COMP FTIME
26.10 SPEED WIN FUNC
26.11 SPEED WIN HI
26.12 SPEED WIN LO
SPEED ACT
(7 / 1.01)
SPEEDREF RAMPED
(6 / 3.04)
SPEED REF POS
(11 / 4.01)
SPEED FEED FWD
(11 / 4.20)
SPEED CONTROL
TLF3 250 µsec
7
(3)
3.08 TORQ REF SP CTRL
< 28.01 SPEED ERR NCTRL
28.02 PROPORT GAIN
28.03 INTEGRATION TIME
28.04 DERIVATION TIME
28.05 DERIV FILT TIME
< 28.06 ACC COMPENSATION
28.07 DROOPING RATE
28.08 BAL REFERENCE
< 28.09 SPEEDCTRL BAL EN
28.10 MIN TORQ SP CTRL
28.11 MAX TORQ SP CTRL
28.12 PI ADAPT MAX SPD
28.13 PI ADAPT MIN SPD
28.14 P GAIN ADPT COEF
28.15 I TIME ADPT COEF
SPEED ERROR FILT
(7 / 3.06)
ACC COMP TORQ
(7 / 3.07)
Page 7 Speed Ctrl
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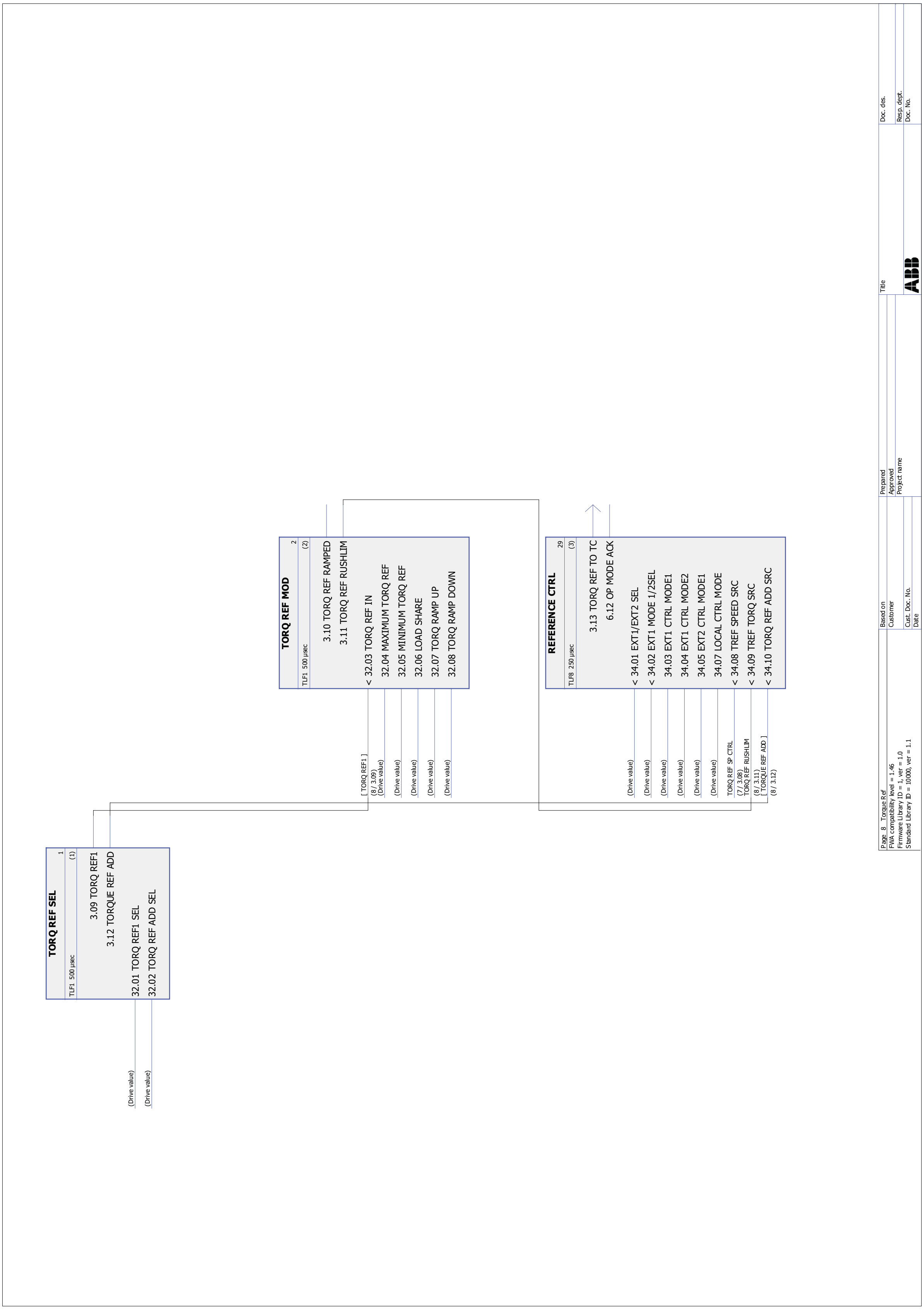

TORQ REF SEL
TLF1 500 µsec
1
(1)
3.09 TORQ REF1
3.12 TORQUE REF ADD
32.01 TORQ REF1 SEL
32.02 TORQ REF ADD SEL
(Drive value)
(Drive value)
TORQ REF MOD
TLF1 500 µsec
2
(2)
3.10 TORQ REF RAMPED
3.11 TORQ REF RUSHLIM
< 32.03 TORQ REF IN
32.04 MAXIMUM TORQ REF
32.05 MINIMUM TORQ REF
32.06 LOAD SHARE
32.07 TORQ RAMP UP
32.08 TORQ RAMP DOWN
[TORQ REF1]
(8 / 3.09)
REFERENCE CTRL
TLF8 250 µsec
29
(3)
3.13 TORQ REF TO TC
6.12 OP MODE ACK
< 34.01 EXT1/EXT2 SEL
< 34.02 EXT1 MODE 1/2SEL
34.03 EXT1 CTRL MODE1
34.04 EXT1 CTRL MODE2
34.05 EXT2 CTRL MODE1
34.07 LOCAL CTRL MODE
< 34.08 TREF SPEED SRC
< 34.09 TREF TORQ SRC
< 34.10 TORQ REF ADD SRC
TORQ REF SP CTRL
(7 / 3.08)
TORQ REF RUSHLIM
(8 / 3.11)
[TORQUE REF ADD]
(8 / 3.12)
Page 8 Torque Ref
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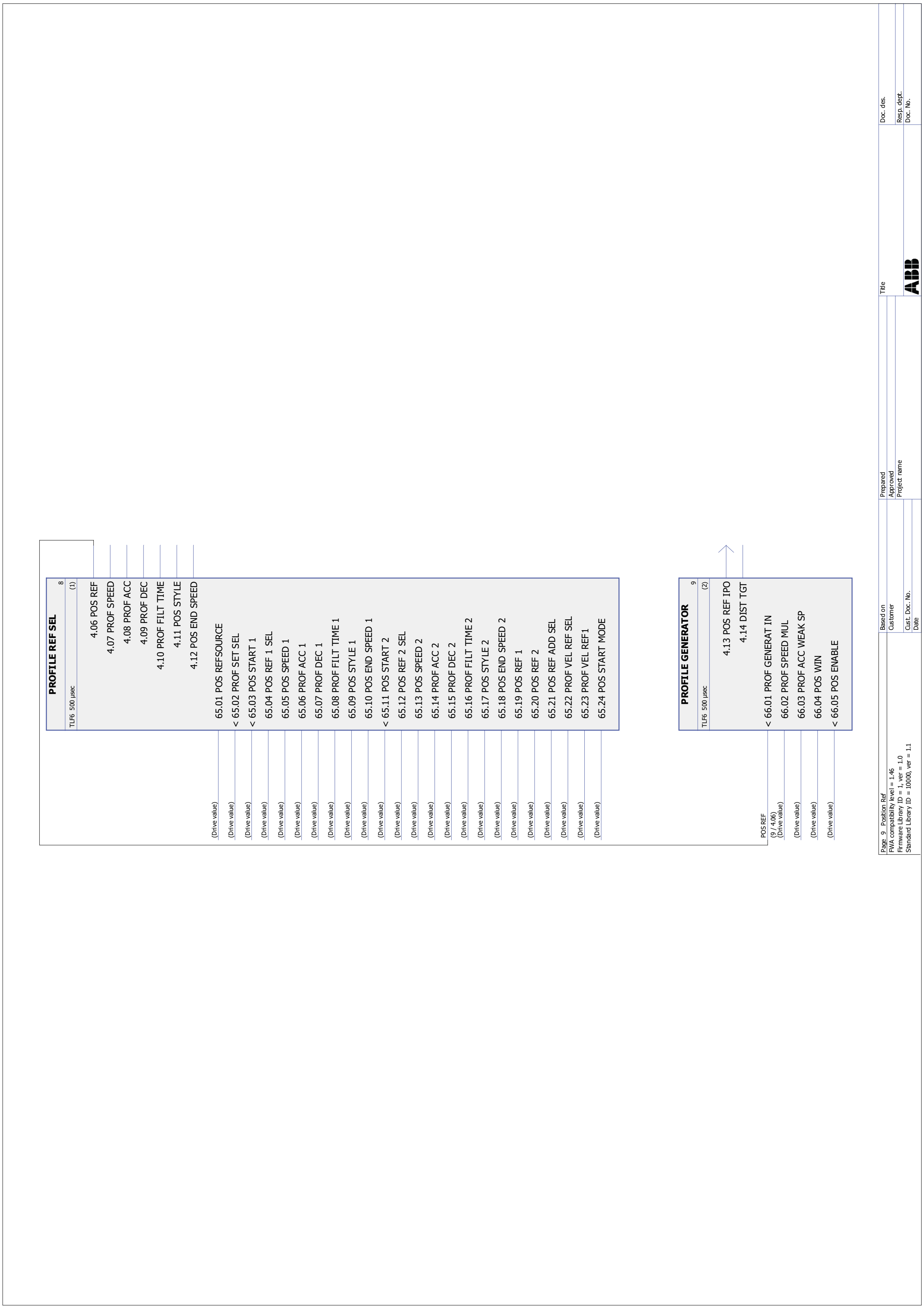

PROFILE REF SEL
TLF6 500 µsec
8
(1)
4.06 POS REF
4.07 PROF SPEED
4.08 PROF ACC
4.09 PROF DEC
4.10 PROF FILT TIME
4.11 POS STYLE
4.12 POS END SPEED
65.01 POS REFSOURCE
< 65.02 PROF SET SEL
< 65.03 POS START 1
65.04 POS REF 1 SEL
65.05 POS SPEED 1
65.06 PROF ACC 1
65.07 PROF DEC 1
65.08 PROF FILT TIME 1
65.09 POS STYLE 1
65.10 POS END SPEED 1
< 65.11 POS START 2
65.12 POS REF 2 SEL
65.13 POS SPEED 2
65.14 PROF ACC 2
65.15 PROF DEC 2
65.16 PROF FILT TIME 2
65.17 POS STYLE 2
65.18 POS END SPEED 2
65.19 POS REF 1
65.20 POS REF 2
65.21 POS REF ADD SEL
65.22 PROF VEL REF SEL
65.23 PROF VEL REF1
65.24 POS START MODE
(Drive value)
PROFILE GENERATOR
TLF6 500 µsec
9
(2)
4.13 POS REF IPO
4.14 DIST TGT
< 66.01 PROF GENERAT IN
66.02 PROF SPEED MUL
66.03 PROF ACC WEAK SP
66.04 POS WIN
< 66.05 POS ENABLE
POS REF
(9 / 4.06)
Page 9 Position Ref
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Firmware Library ID = 1, ver = 1.0
Standard Library ID = 10000, ver = 1.1
Based on
Customer
Cust. Doc. No.
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Title
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Doc. No.


SYNC REF SEL
TLF5 500 µsec
41
(1)
4.15 SYNC REF UNGEAR
67.01 SYNC REF SEL
67.02 SPEED REF VIRT M
67.03 INTERPOLAT MODE
67.04 INTERPOLAT CYCLE
(Drive value)
SYNC REF MOD
TLF5 500 µsec
13
(2)
4.16 SYNC REF GEARED
< 68.01 SYNC GEAR IN
68.02 SYNC GEAR MUL
68.03 SYNC GEAR DIV
68.04 SYNC GEAR ADD
68.05 SYNC REF FTIME
68.06 SYNCFILT DLY LIM
68.07 SYNCHRON MODE
[SYNC REF UNGEAR]
(10 / 4.15)
Page 10 Syncronisation
FWA compatibility level = 1.46
Firmware Library ID = 1, ver = 1.0
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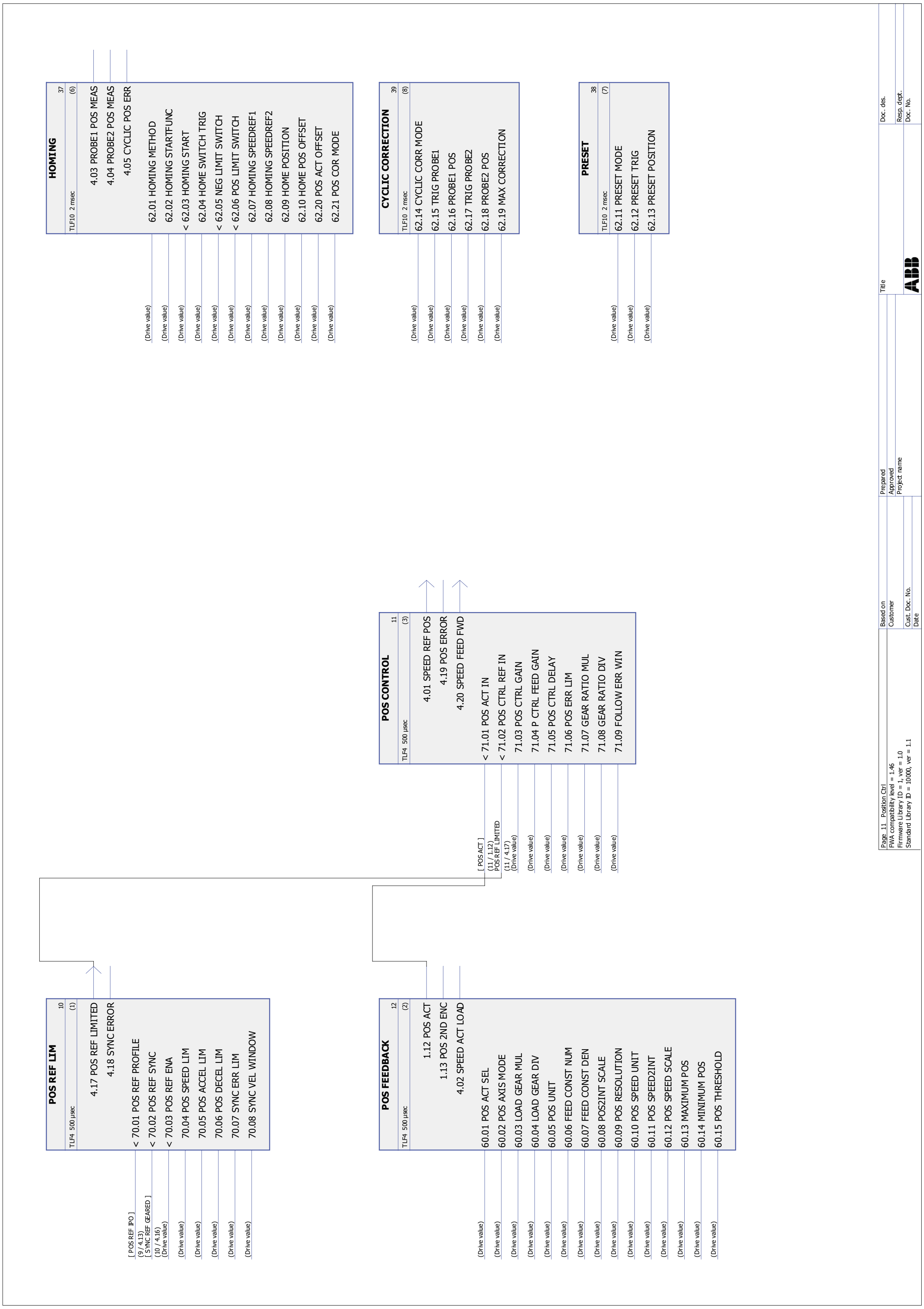
HOMING
TLF10 2 msec
37
(6)
4.03 PROBE1 POS MEAS
4.04 PROBE2 POS MEAS
4.05 CYCLIC POS ERR
62.01 HOMING METHOD
62.02 HOMING STARTFUNC
< 62.03 HOMING START
62.04 HOME SWITCH TRIG
< 62.05 NEG LIMIT SWITCH
< 62.06 POS LIMIT SWITCH
62.07 HOMING SPEEDREF1
62.08 HOMING SPEEDREF2
62.09 HOME POSITION
62.10 HOME POS OFFSET
62.20 POS ACT OFFSET
62.21 POS COR MODE
CYCLIC CORRECTION
TLF10 2 msec
39
(8)
62.14 CYCLIC CORR MODE
62.15 TRIG PROBE1
62.16 PROBE1 POS
62.17 TRIG PROBE2
62.18 PROBE2 POS
62.19 MAX CORRECTION
PRESET
TLF10 2 msec
38
(7)
62.11 PRESET MODE
62.12 PRESET TRIG
62.13 PRESET POSITION
POS CONTROL
TLF4 500 µsec
11
(3)
4.01 SPEED REF POS
4.19 POS ERROR
4.20 SPEED FEED FWD
< 71.01 POS ACT IN
< 71.02 POS CTRL REF IN
71.03 POS CTRL GAIN
71.04 P CTRL FEED GAIN
71.05 POS CTRL DELAY
71.06 POS ERR LIM
71.07 GEAR RATIO MUL
71.08 GEAR RATIO DIV
71.09 FOLLOW ERR WIN
[POS ACT]
(11 / 1.12)
POS REF LIMITED
(11 / 4.17)
POS REF LIM
TLF4 500 µsec
10
(1)
4.17 POS REF LIMITED
4.18 SYNC ERROR
< 70.01 POS REF PROFILE
< 70.02 POS REF SYNC
< 70.03 POS REF ENA
70.04 POS SPEED LIM
70.05 POS ACCEL LIM
70.06 POS DECEL LIM
70.07 SYNC ERR LIM
70.08 SYNC VEL WINDOW
[POS REF IPO]
(9 / 4.13)
[SYNC REF GEARED]
(10 / 4.16)
POS FEEDBACK
TLF4 500 µsec
12
(2)
1.12 POS ACT
1.13 POS 2ND ENC
4.02 SPEED ACT LOAD
60.01 POS ACT SEL
60.02 POS AXIS MODE
60.03 LOAD GEAR MUL
60.04 LOAD GEAR DIV
60.05 POS UNIT
60.06 FEED CONST NUM
60.07 FEED CONST DEN
60.08 POS2INT SCALE
60.09 POS RESOLUTION
60.10 POS SPEED UNIT
60.11 POS SPEED2INT
60.12 POS SPEED SCALE
60.13 MAXIMUM POS
60.14 MINIMUM POS
60.15 POS THRESHOLD
(Drive value)
Page 11 Position Ctrl
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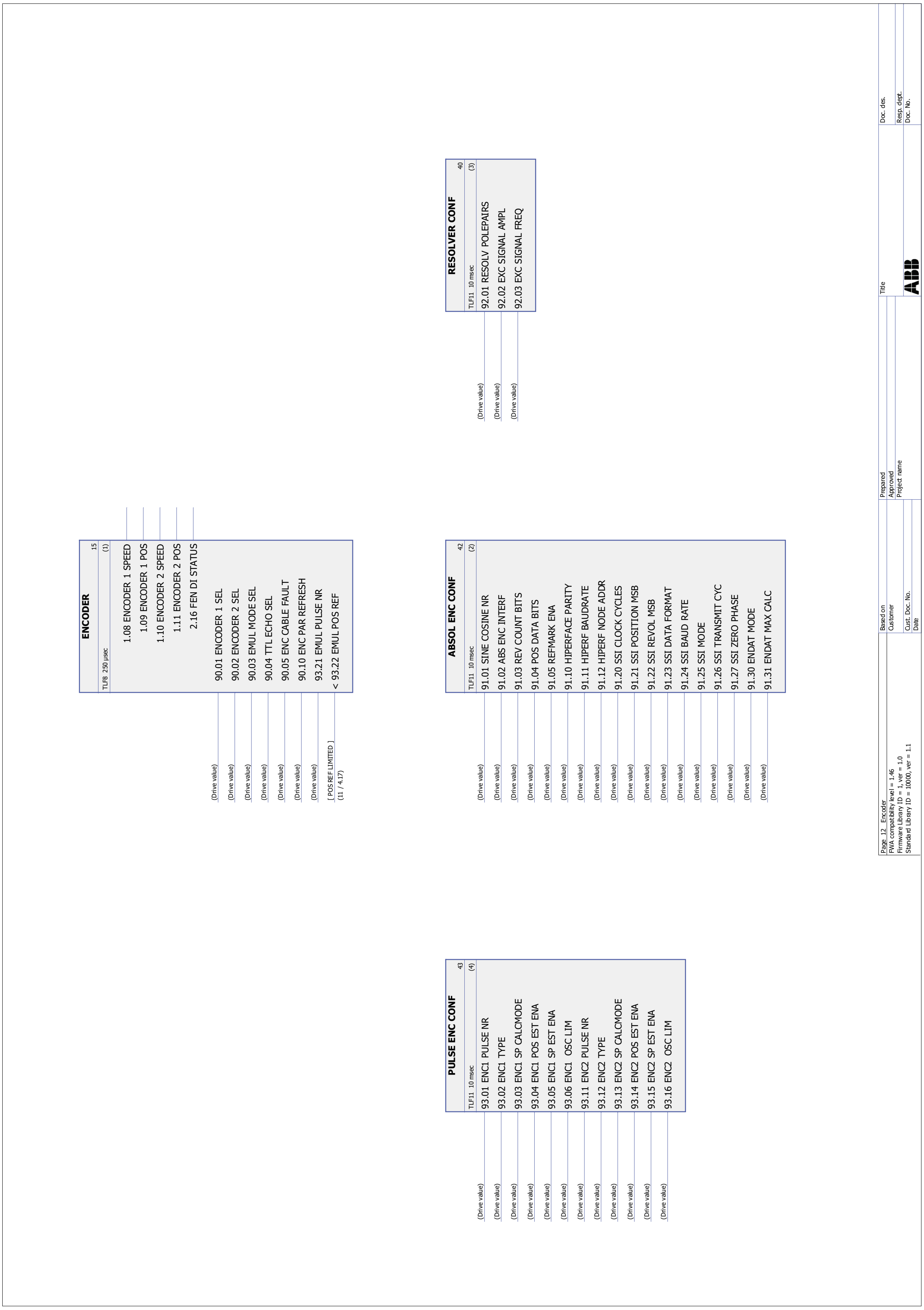
Doc. No.


ENCODER
TLF8 250 µsec
15
(1)
1.08 ENCODER 1 SPEED
1.09 ENCODER 1 POS
1.10 ENCODER 2 SPEED
1.11 ENCODER 2 POS
2.16 FEN DI STATUS
90.01 ENCODER 1 SEL
90.02 ENCODER 2 SEL
90.03 EMUL MODE SEL
90.04 TTL ECHO SEL
90.05 ENC CABLE FAULT
90.10 ENC PAR REFRESH
93.21 EMUL PULSE NR
< 93.22 EMUL POS REF
(Drive value)
[POS REF LIMITED]
(11 / 4.17)
RESOLVER CONF
TLF11 10 msec
40
(3)
92.01 RESOLV POLEPAIRS
92.02 EXC SIGNAL AMPL
92.03 EXC SIGNAL FREQ
ABSOL ENC CONF
TLF11 10 msec
42
(2)
91.01 SINE COSINE NR
91.02 ABS ENC INTERF
91.03 REV COUNT BITS
91.04 POS DATA BITS
91.05 REFMARK ENA
91.10 HIPERFACE PARITY
91.11 HIPERF BAUDRATE
91.12 HIPERF NODE ADDR
91.20 SSI CLOCK CYCLES
91.21 SSI POSITION MSB
91.22 SSI REVOL MSB
91.23 SSI DATA FORMAT
91.24 SSI BAUD RATE
91.25 SSI MODE
91.26 SSI TRANSMIT CYC
91.27 SSI ZERO PHASE
91.30 ENDAT MODE
91.31 ENDAT MAX CALC
PULSE ENC CONF
TLF11 10 msec
43
(4)
93.01 ENC1 PULSE NR
93.02 ENC1 TYPE
93.03 ENC1 SP CALCMODE
93.04 ENC1 POS EST ENA
93.05 ENC1 SP EST ENA
93.06 ENC1 OSC LIM
93.11 ENC2 PULSE NR
93.12 ENC2 TYPE
93.13 ENC2 SP CALCMODE
93.14 ENC2 POS EST ENA
93.15 ENC2 SP EST ENA
93.16 ENC2 OSC LIM
Page 12 Encoder
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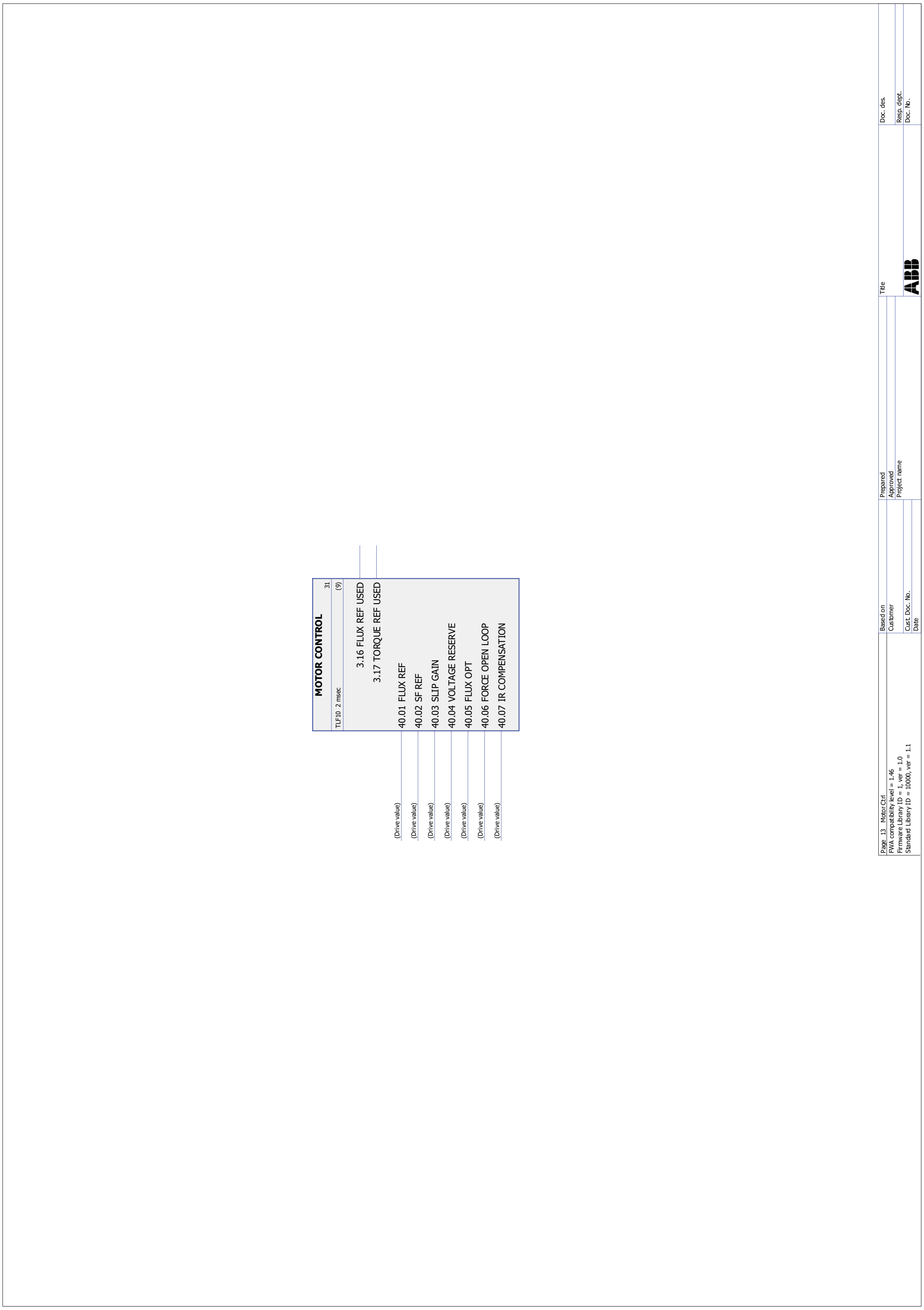
MOTOR CONTROL
TLF10 2 msec
31
(9)
3.16 FLUX REF USED
3.17 TORQUE REF USED
40.01 FLUX REF
40.02 SF REF
40.03 SLIP GAIN
40.04 VOLTAGE RESERVE
40.05 FLUX OPT
40.06 FORCE OPEN LOOP
40.07 IR COMPENSATION
(Drive value)
(Drive value)
(Drive value)
(Drive value)
(Drive value)
(Drive value)
(Drive value)
Page 13 Motor Ctrl
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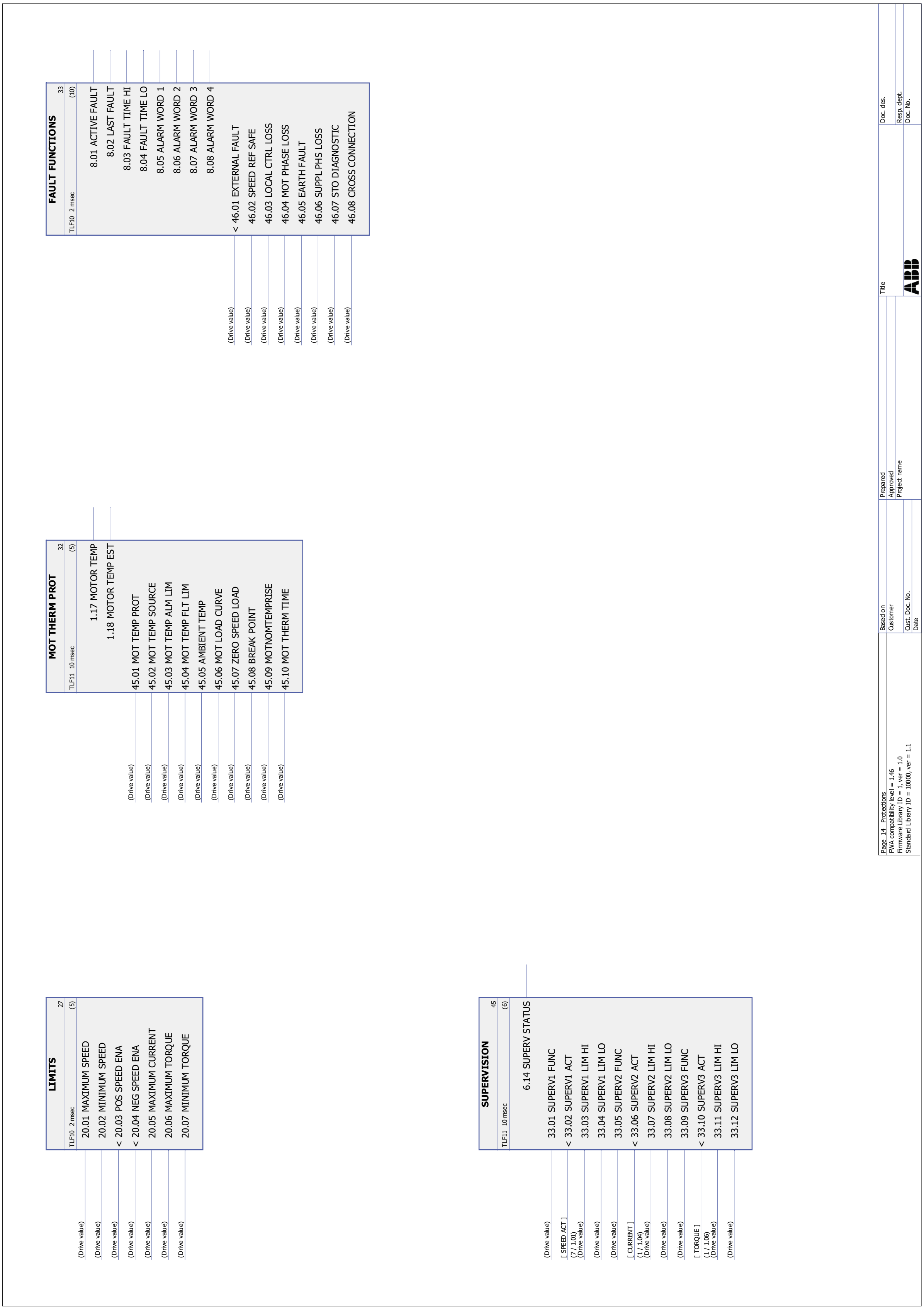
LIMITS
TLF10 2 msec 27 (5)
20.01 MAXIMUM SPEED
20.02 MINIMUM SPEED
< 20.03 POS SPEED ENA
< 20.04 NEG SPEED ENA
20.05 MAXIMUM CURRENT
20.06 MAXIMUM TORQUE
20.07 MINIMUM TORQUE
MOT THERM PROT
TLF11 10 msec 32 (5)
1.17 MOTOR TEMP
1.18 MOTOR TEMP EST
45.01 MOT TEMP PROT
45.02 MOT TEMP SOURCE
45.03 MOT TEMP ALM LIM
45.04 MOT TEMP FLT LIM
45.05 AMBIENT TEMP
45.06 MOT LOAD CURVE
45.07 ZERO SPEED LOAD
45.08 BREAK POINT
45.09 MOTNOMTEMPRISE
45.10 MOT THERM TIME
FAULT FUNCTIONS
TLF10 2 msec 33 (10)
8.01 ACTIVE FAULT
8.02 LAST FAULT
8.03 FAULT TIME HI
8.04 FAULT TIME LO
8.05 ALARM WORD 1
8.06 ALARM WORD 2
8.07 ALARM WORD 3
8.08 ALARM WORD 4
< 46.01 EXTERNAL FAULT
46.02 SPEED REF SAFE
46.03 LOCAL CTRL LOSS
46.04 MOT PHASE LOSS
46.05 EARTH FAULT
46.06 SUPPL PHS LOSS
46.07 STO DIAGNOSTIC
46.08 CROSS CONNECTION
SUPERVISION
TLF11 10 msec 45 (6)
6.14 SUPERV STATUS
33.01 SUPERV1 FUNC
< 33.02 SUPERV1 ACT
33.03 SUPERV1 LIM HI
33.04 SUPERV1 LIM LO
33.05 SUPERV2 FUNC
< 33.06 SUPERV2 ACT
33.07 SUPERV2 LIM HI
33.08 SUPERV2 LIM LO
33.09 SUPERV3 FUNC
< 33.10 SUPERV3 ACT
33.11 SUPERV3 LIM HI
33.12 SUPERV3 LIM LO
(Drive value)
[SPEED ACT]
(7 / 1.01)
[CURRENT]
(1 / 1.04)
[TORQUE]
(1 / 1.06)
Page 14 Protections
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Customer
Cust. Doc. No.
Date
Prepared
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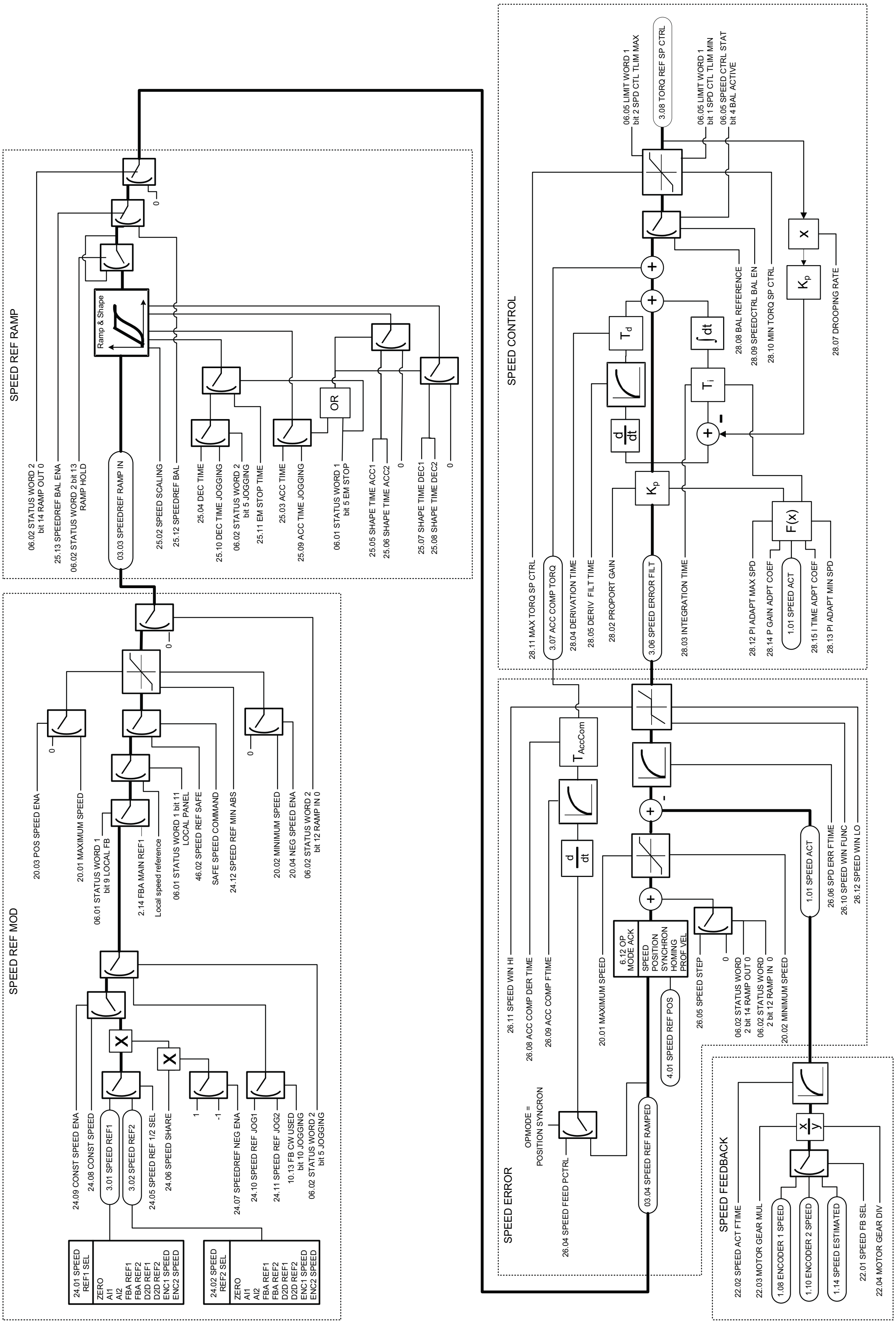
Control chain block diagrams

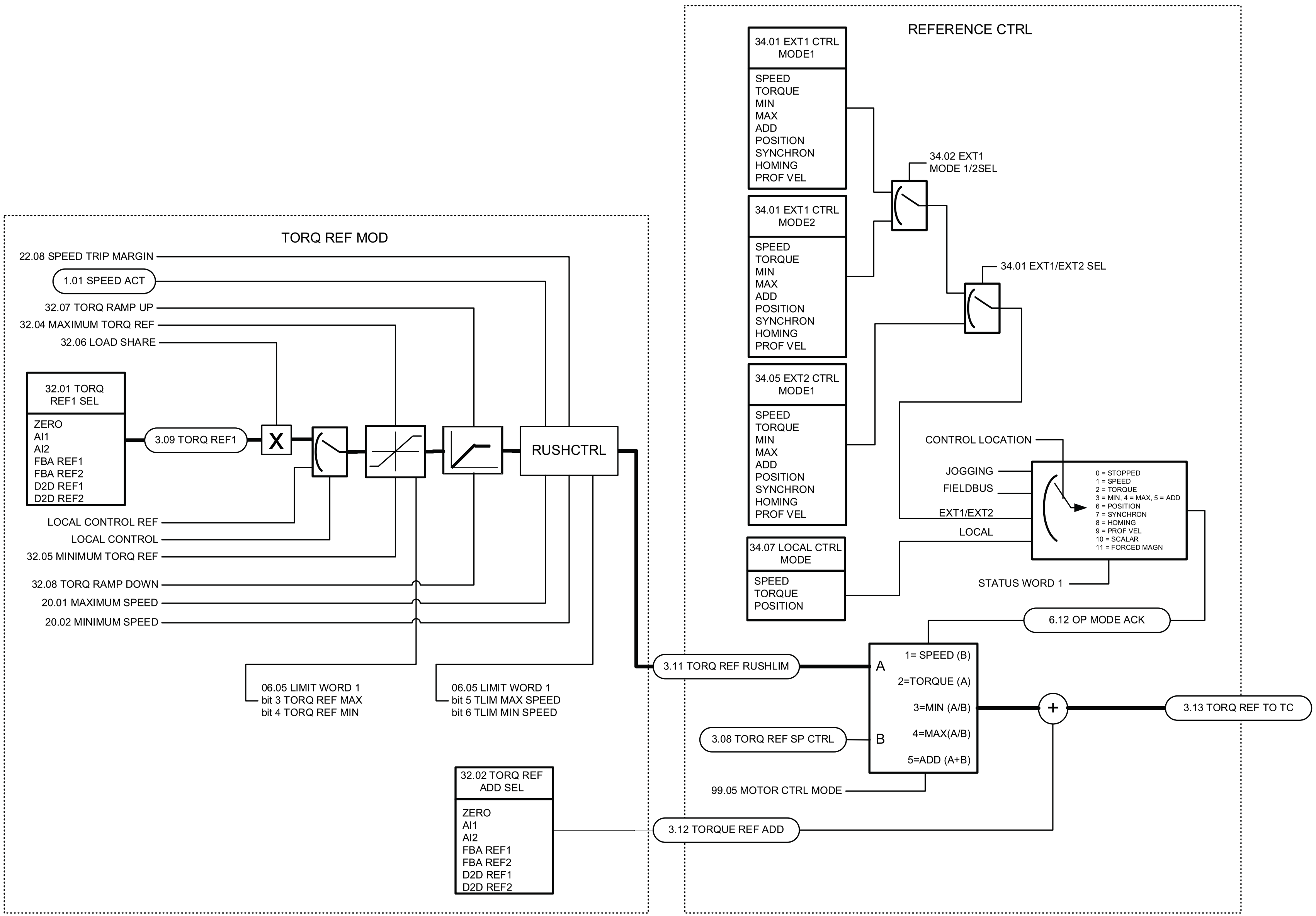
What this chapter contains

This chapter presents the drive control chain in different control modes.

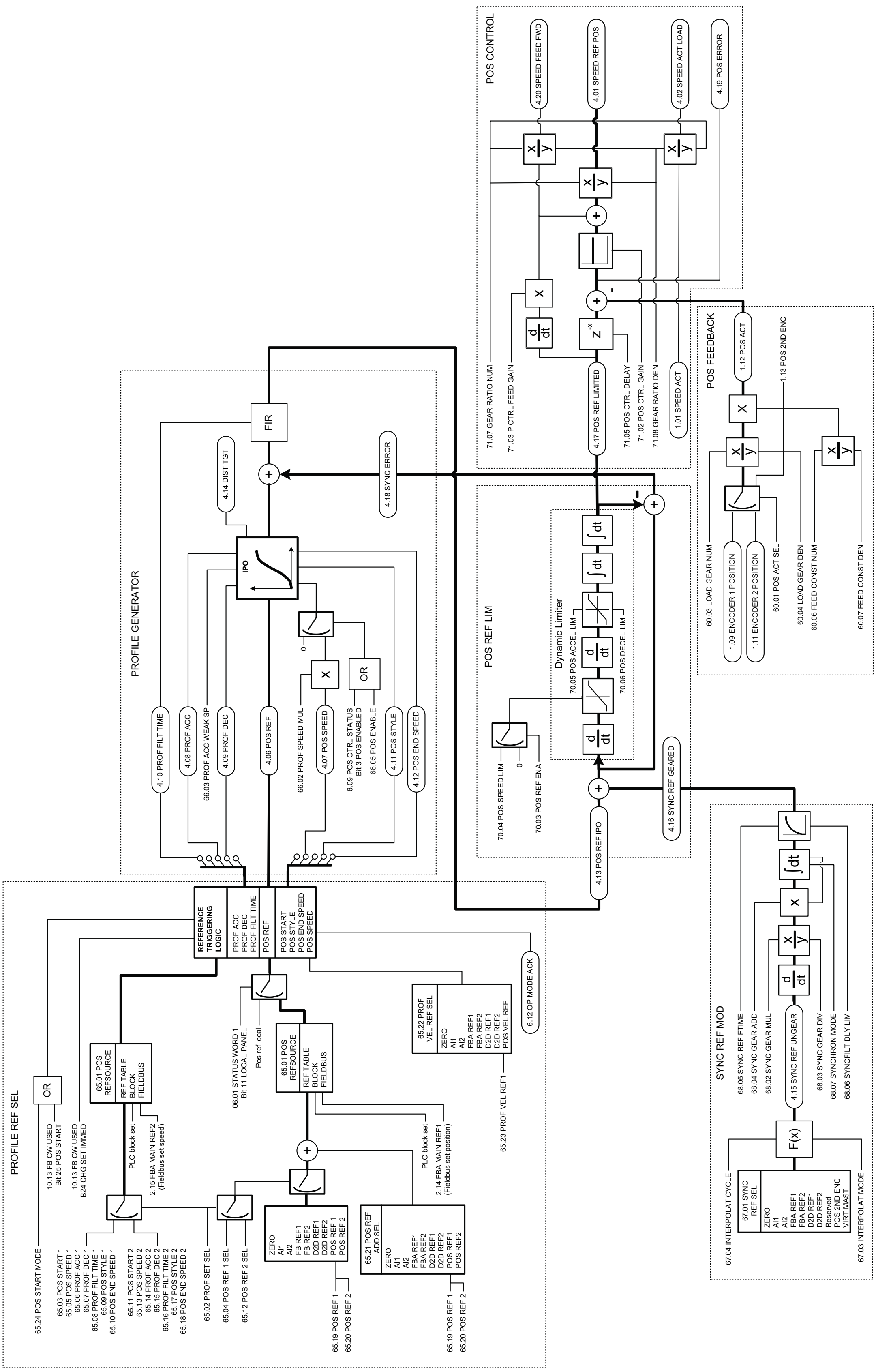
Speed control chain



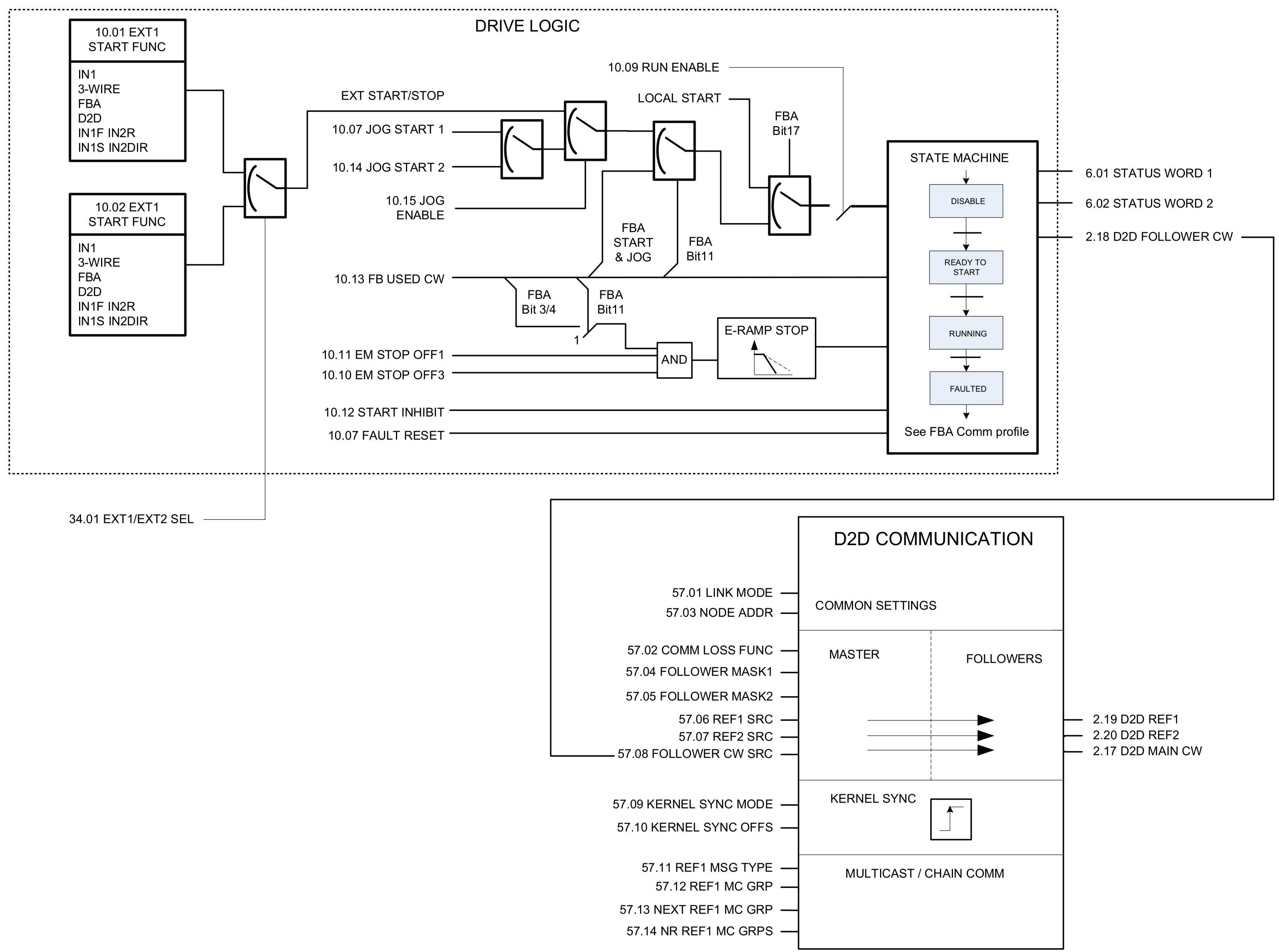
Torque control chain



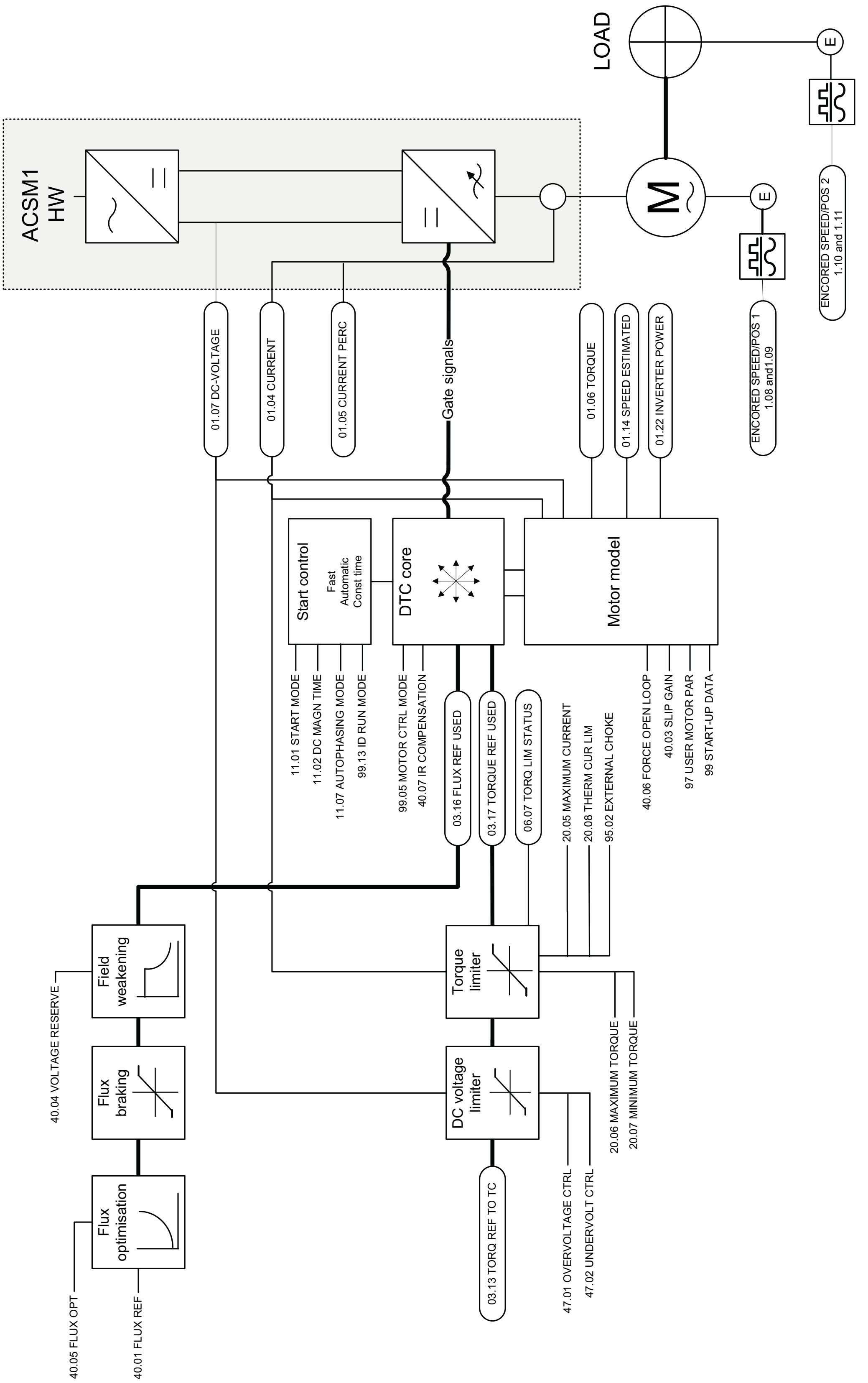
Position control chain



Drive logic


DRIVE LOGIC
10.01 EXT1 START FUNC
IN1
3-WIRE
FBA
D2D
IN1F IN2R
IN1S IN2DIR
10.02 EXT1 START FUNC
IN1
3-WIRE
FBA
D2D
IN1F IN2R
IN1S IN2DIR
EXT START/STOP
10.07 JOG START 1
10.14 JOG START 2
10.15 JOG ENABLE
10.09 RUN ENABLE
LOCAL START
FBA Bit17
FBA START & JOG
FBA Bit11
10.13 FB USED CW
FBA Bit 3/4
FBA Bit11
1
10.11 EM STOP OFF1
10.10 EM STOP OFF3
AND
E-RAMP STOP
10.12 START INHIBIT
10.07 FAULT RESET
STATE MACHINE
DISABLE
READY TO START
RUNNING
FAULTED
See FBA Comm profile
6.01 STATUS WORD 1
6.02 STATUS WORD 2
2.18 D2D FOLLOWER CW
34.01 EXT1/EXT2 SEL
D2D COMMUNICATION
57.01 LINK MODE
57.03 NODE ADDR
COMMON SETTINGS
57.02 COMM LOSS FUNC
57.04 FOLLOWER MASK1
57.05 FOLLOWER MASK2
57.06 REF1 SRC
57.07 REF2 SRC
57.08 FOLLOWER CW SRC
MASTER
FOLLOWERS
2.19 D2D REF1
2.20 D2D REF2
2.17 D2D MAIN CW
57.09 KERNEL SYNC MODE
57.10 KERNEL SYNC OFFS
KERNEL SYNC
57.11 REF1 MSG TYPE
57.12 REF1 MC GRP
57.13 NEXT REF1 MC GRP
57.14 NR REF1 MC GRPS
MULTICAST / CHAIN COMM

DTC motor control



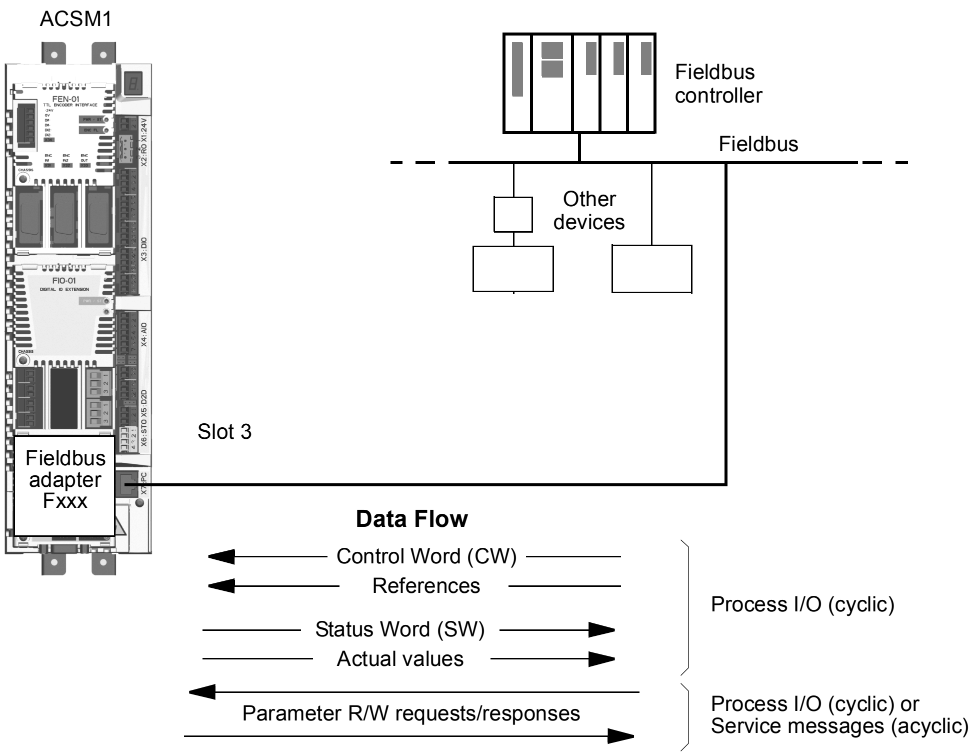
Appendix A – Fieldbus control

What this chapter contains

The chapter describes how the drive can be controlled by external devices over a communication network.

System overview

The drive can be connected to a fieldbus controller via a fieldbus adapter module. The adapter module is connected to drive Slot 3.



The drive can be set to receive all of its control information through the fieldbus interface, or the control can be distributed between the fieldbus interface and other available sources, for example digital and analogue inputs.

The drive can communicate with fieldbus controller via fieldbus adapter using one of the following serial communication protocols:

- PROFIBUS-DP® (FPBA-01 adapter)
- CANopen® (FCAN-01 adapter)
- DeviceNet® (FDNA-01 adapter).

Setting up communication through a fieldbus adapter module

Before configuring the drive for fieldbus control, the adapter module must be mechanically and electrically installed according to the instructions given in the User's Manual of the appropriate fieldbus adapter module.

The communication between the drive and the fieldbus adapter module is activated by setting parameter 50.01 FBA ENABLE to (1) ENABLE. The adapter-specific parameters must also be set. See the table below.

Parameter	Setting for fieldbus control	Function/Information
COMMUNICATION INITIALISATION AND SUPERVISION		
50.01 FBA ENABLE	(1) ENABLE	Initialises communication between drive and fieldbus adapter module.
50.02 COMM LOSS FUNC	(0) NO (1) FAULT (2) SPD REF SAFE (3) LAST SPEED	Selects how the drive reacts in a fieldbus communication break.
50.03 COMM LOSS T OUT	0.3…6553.5 s	Defines the time between communication break detection and the action selected with parameter 50.02 COMM LOSS FUNC.
50.04 FBA REF1 MODESEL and 50.05 FBA REF2 MODESEL	(0) RAW DATA (1) TORQUE (2) SPEED (3) POSITION (4) VELOCITY (5) AUTO	Defines the fieldbus reference scaling. When (0) RAW DATA is selected, see also parameters 50.06…50.11.
ADAPTER MODULE CONFIGURATION		
51.01 FBA TYPE	–	Displays the type of the fieldbus adapter module.
51.02 FBA PAR2 • • • 51.26 FBA PAR26	These parameters are adapter module-specific. For more information, see the User's Manual of the fieldbus adapter module. Note that not all of these parameters are necessarily used.	
51.27 FBA PAR REFRESH	(0) DONE (1) REFRESH	Validates any changed adapter module configuration parameter settings.
51.28 PAR TABLE VER	–	Displays the parameter table revision of the fieldbus adapter module mapping file stored in the memory of the drive.
51.29 DRIVE TYPE CODE	–	Displays the drive type code of the fieldbus adapter module mapping file stored in the memory of the drive.
51.30 MAPPING FILE VER	–	Displays the fieldbus adapter module mapping file revision stored in the memory of the drive.
51.31 D2FBA COMM STA	–	Displays the status of the fieldbus adapter module communication.
51.32 FBA COMM SW VER	–	Displays the common program revision of the adapter module.
51.33 FBA APPL SW VER	–	Displays the application program revision of the adapter module.
Note: In the User's Manual of the fieldbus adapter module, the parameter group number is 1 or A for parameters 51.01…51.26.		

Parameter	Setting for fieldbus control	Function/Information
TRANSMITTED DATA SELECTION		
52.01 FBA DATA IN1 … 52.12 FBA DATA IN12	0 4…6 14…16 101…9999	Defines the data transmitted from drive to fieldbus controller. **Note:** If the selected data is 32 bits long, two parameters are reserved for the transmission.
53.01 FBA DATA OUT1 … 53.12 FBA DATA OUT12	0 1…3 11…13 1001…9999	Defines the data transmitted from fieldbus controller to drive. **Note:** If the selected data is 32 bits long, two parameters are reserved for the transmission.
Note: In the User's Manual of the fieldbus adapter module, the parameter group number is 3 or C for parameters 52.01…52.12 and 2 or B for parameters 53.01…53.12.		

After the module configuration parameters have been set, the drive control parameters (see section Drive control parameters) must be checked and adjusted when necessary.

The new settings will take effect when the drive is powered up the next time (before powering off the drive, wait at least 1 minute), or when parameter 51.27 FBA PAR REFRESH is activated.

Drive control parameters

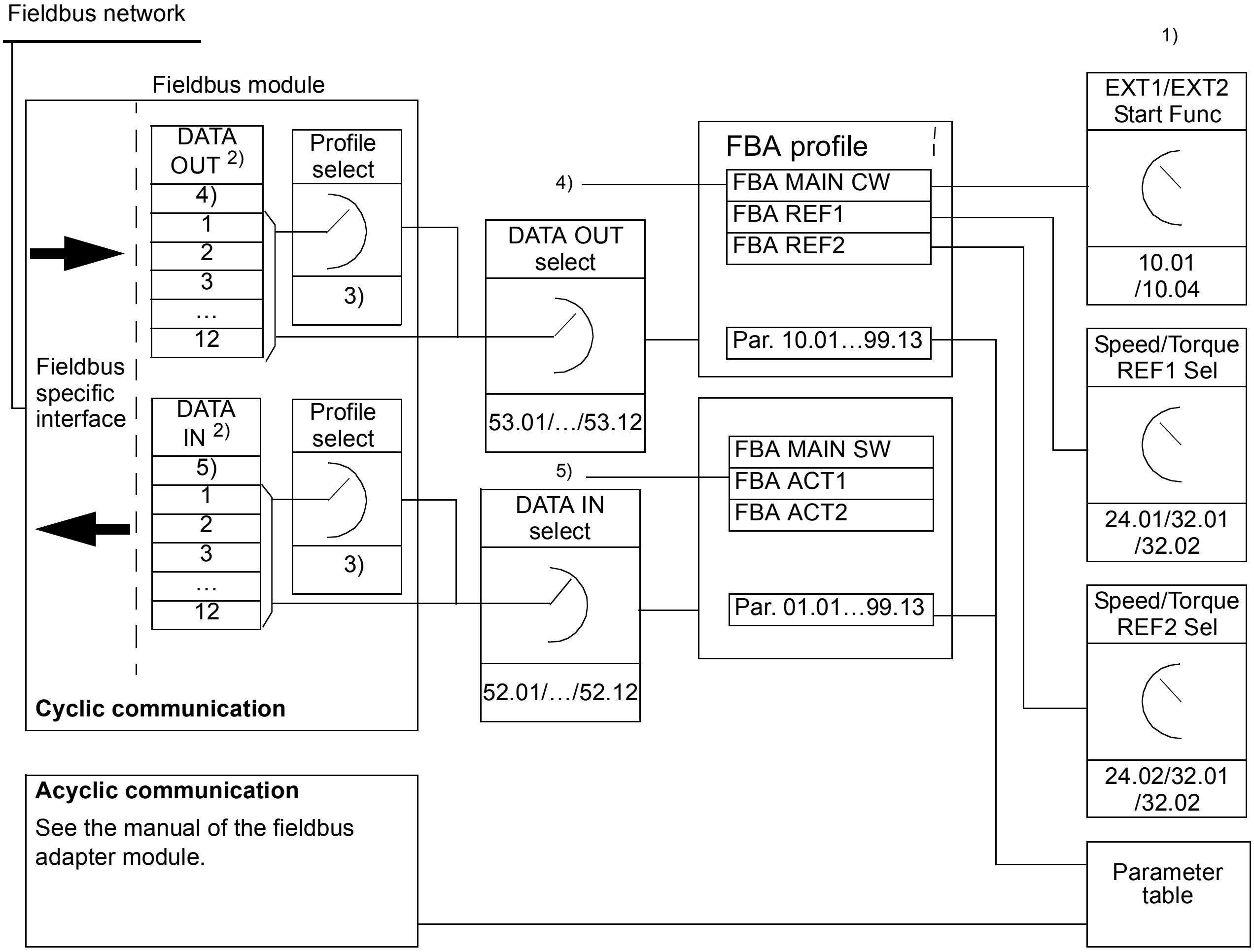
The Setting for fieldbus control column gives the value to use when the fieldbus interface is the desired source or destination for that particular signal. The Function/ Information column gives a description of the parameter.

Parameter	Setting for fieldbus control	Function/Information
CONTROL COMMAND SOURCE SELECTION		
10.01 EXT1 START FUNC	(3) FBA	Selects fieldbus as the source for the start and stop commands when EXT1 is selected as the active control location.
10.04 EXT2 START FUNC	(3) FBA	Selects fieldbus as the source for the start and stop commands when EXT2 is selected as the active control location.
24.01 SPEED REF1 SEL	(3) FBA REF1 (4) FBA REF2	Fieldbus reference REF1 or REF2 is used as speed reference 1.
24.02 SPEED REF2 SEL	(3) FBA REF1 (4) FBA REF2	Fieldbus reference REF1 or REF2 is used as speed reference 2.
32.01 TORQ REF1 SEL	(3) FBA REF1 (4) FBA REF2	Fieldbus reference REF1 or REF2 is used as torque reference 1.
32.02 TORQ REF ADD SEL	(3) FBA REF1 (4) FBA REF2	Fieldbus reference REF1 or REF2 is used for torque reference addition.
SYSTEM CONTROL INPUTS		
16.07 PARAM SAVE	(0) DONE (1) SAVE	Saves parameter value changes (including those made through fieldbus control) to permanent memory.

The fieldbus control interface

The cyclic communication between a fieldbus system and the drive consists of 16/32-bit input and output data words. The drive supports at the maximum the use of 12 data words (16-bit) in each direction.

Data transmitted from the drive to the fieldbus controller is defined by parameters 52.01…52.12 (FBA DATA IN) and data transmitted from the fieldbus controller to the drive is defined by parameters 53.01…53.12 (FBA DATA OUT).



1) See also other parameters which can be controlled by the fieldbus.
2) The maximum number of used data words is protocol-dependent.
3) Profile/instance selection parameters. Fieldbus module specific parameters. For more information, see the User's Manual of the appropriate fieldbus adapter module.
4) With DeviceNet the control part is transmitted directly.
5) With DeviceNet the actual value part is transmitted directly.

The Control Word and the Status Word

The Control Word (CW) is the principal means of controlling the drive from a fieldbus system. The Control Word is sent by the fieldbus controller to the drive. The drive switches between its states according to the bit-coded instructions of the Control Word.

The Status Word (SW) is a word containing status information, sent by the drive to the fieldbus controller.

Actual values

Actual values (ACT) are 16/32-bit words containing information on selected operations of the drive.

FBA communication profile

The FBA communication profile is a state machine model which describes the general states and state transitions of the drive. The State diagram on page 390 presents the most important states (including the FBA profile state names). The FBA Control Word (2.12 FBA MAIN CW, page 81) commands the transitions between these states and the FBA Status Word (2.13 FBA MAIN SW, page 84) indicates the status of the drive.

Fieldbus adapter module profile (selected by adapter module parameter) defines how the control word and status word are transmitted in a system which consists of fieldbus controller, fieldbus adapter module and drive. With transparent modes, control word and status word are transmitted without any conversion between the fieldbus controller and the drive. With other profiles (e.g. PROFIdrive for FPBA-01, AC/DC drive for FDNA-01, DS-402 for FCAN-01 and ABB Drives profile for all fieldbus adapter modules) fieldbus adapter module converts the fieldbus-specific control word to the FBA communication profile and status word from FBA communication profile to the fieldbus-specific status word.

For descriptions of other profiles, see the *User’s Manual* of the appropriate fieldbus adapter module.

Fieldbus references

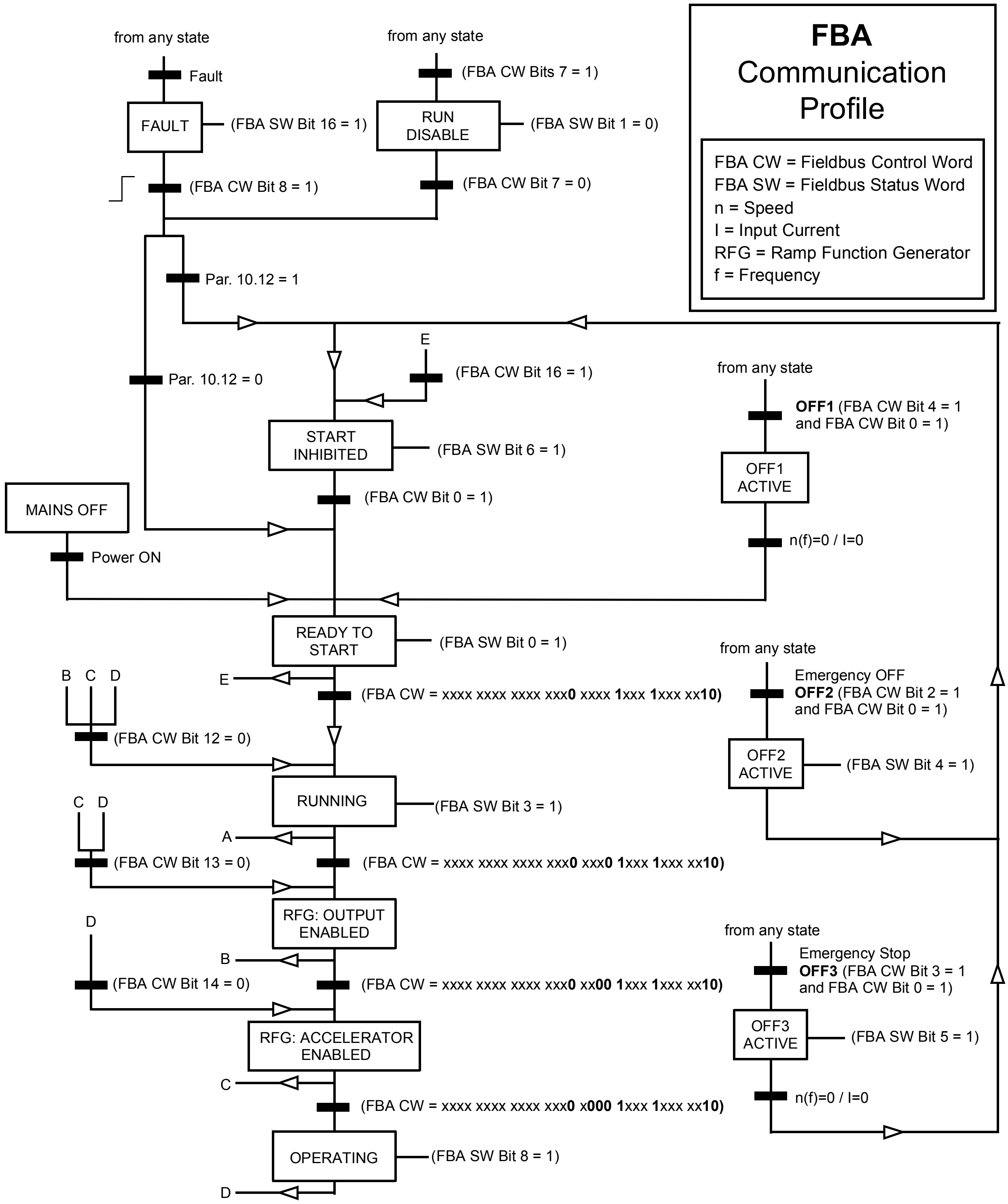
References (FBA REF) are 16/32-bit signed integers. A negative reference (indicating reversed direction of rotation) is formed by calculating the two’s complement from the corresponding positive reference value. The contents of each reference word can be used as torque or speed reference.

When torque or speed reference scaling is selected (by parameter 50.04 FBA REF1 MODESEL / 50.05 FBA REF2 MODESEL), the fieldbus references are 32-bit integers. The value consists of a 16-bit integer value and a 16-bit fractional value. The speed/torque reference scaling is as follows:

Reference	Scaling	Notes
Torque reference	FBA REF / 65536 (value in %)	Final reference is limited by parameters 20.06 MAXIMUM TORQUE and 20.07 MINIMUM TORQUE.
Speed reference	FBA REF / 65536 (value in rpm)	Final reference is limited by parameters 20.01 MAXIMUM SPEED, 20.02 MINIMUM SPEED and 24.12 SPEED REFMIN ABS.
Position reference	See parameter group 60 POS FEEDBACK.	
Velocity reference		

State diagram

The following presents the state diagram for the FBA communication profile. For other profiles, see the *User's Manual* of the appropriate fieldbus adapter module.



Appendix B – Drive-to-drive link

What this chapter contains

This chapter describes the wiring of, and available communication methods on the drive-to-drive link. Examples of using standard firmware blocks in the communication are also given starting on page 399.

General

The drive-to-drive link is a daisy-chained RS-485 transmission line, constructed by connecting the X5 terminal blocks of the JCU Control Units of several drives. It is also possible to use an FMBA Modbus extension module installed into an option slot on the JCU. The firmware supports up to 63 nodes on the link.

The link has one master drive; the rest of the drives are followers. By default, the master broadcasts control commands as well as speed and torque references for all followers. The master can also be configured to send a position reference as either target position or synchronization reference. The master can send 8 messages per millisecond at 100/150-microsecond intervals. Sending one message takes approximately 15 microseconds, which results in a theoretical link capacity of roughly 6 messages per 100 microseconds.

Multicasting the control data and reference 1 to a pre-defined group of drives is possible, as is chained multicast messaging. Reference 2 is always broadcast by the master to all followers. See parameters 57.11…57.14.

Wiring

Shielded twisted-pair cable (~100 ohm, e.g. PROFIBUS-compatible cable) must be used for the wiring. The maximum length of the link is 50 metres (164 ft).

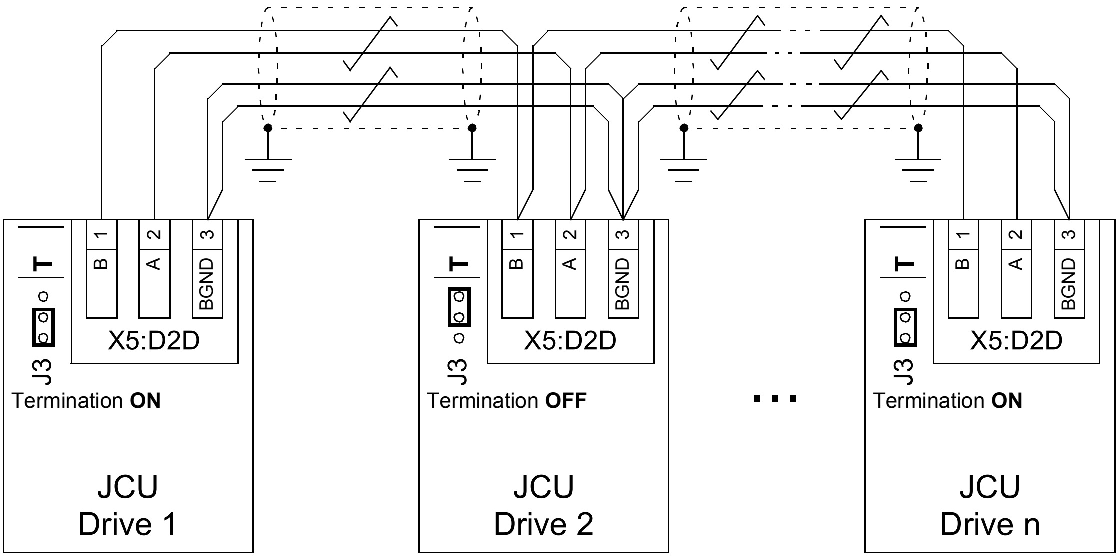
The JCU Control Unit has a jumper (J3, “T”) next to the X5 terminal block for bus termination. Termination must be ON on the drives at the ends of the drive-to-drive link; on intermediate drives, termination must be OFF.

Instead of the X5 connector, an FMBA Modbus extension module can be used.

For best immunity, high quality cable is recommended. The cable should be kept as short as possible. Unnecessary loops and running the cable near power cables (such as motor cables) must be avoided.

Note: The cable shields are to be grounded to the control cable clamp plate on the drive. Follow the instructions given in the *Hardware Manual* of the drive.

The following diagram shows the wiring of the drive-to-drive link.



Datasets

Drive-to-drive communication uses DDCS (Distributed Drives Communication System) messages and dataset tables for data transfer. Each drive has a dataset table of 256 datasets, numbered 0…255. Each dataset contains 48 data bits.

By default, datasets 0…15 and 200…255 are reserved for the drive firmware; datasets 16…199 are available for the user application program.

The contents of the standard communication datasets (16-bit control word and two 32-bit references) can be configured freely with pointer parameters and/or application programming with the DriveSPC tool. Depending on the drive control mode, the followers can be configured to use the drive-to-drive commands and references with the following parameters:

Control data	**Parameter**	**Setting for drive-to-drive communication**
Start/Stop commands	10.01 EXT1 START FUNC 10.04 EXT2 START FUNC	(4) D2D
Speed reference	24.01 SPEED REF1 SEL 24.02 SPEED REF2 SEL	(5) D2D REF1 or (6) D2D REF2
Torque reference	32.01 TORQ REF1 SEL 32.02 TORQ REF ADD SEL	(5) D2D REF1 or (6) D2D REF2
Position reference	65.04 POS REF 1 SEL 65.12 POS REF 2 SEL	(5) D2D REF1 or (6) D2D REF2
Position reference in synchron control operating mode	67.01 SYNC REF SEL 67.02 SPEED REF VIRT M	(5) D2D REF1 or (6) D2D REF2

The communication status of the followers can be supervised by a periodic supervision message from the master to the individual followers (see parameters 57.04 FOLLOWER MASK 1 and 57.05 FOLLOWER MASK 2).

Drive-to-drive function blocks can be used in the DriveSPC tool to enable additional communication methods (such as follower-to-follower messaging) and to modify the use of datasets between the drives. See the function blocks under *Communication* (page 301).

Types of messaging

Each drive on the link has a unique node address allowing point-to-point communication between two drives. The node address 0 is automatically assigned to the master drive; on other drives, the node address is defined by parameter 57.03 NODE ADDRESS.

Multicast addressing is supported, allowing the composition of groups of drives. Data sent to a multicast address is received by all drives that have that address. A multicast group can consist of 1…62 drives.

In broadcast messaging, data can be sent to all drives (actually, all followers) on the link.

Both master-to-follower(s) and follower-to-follower(s) communication is supported. A follower can send one message to another follower (or a group of followers) after receiving a token message from the master.

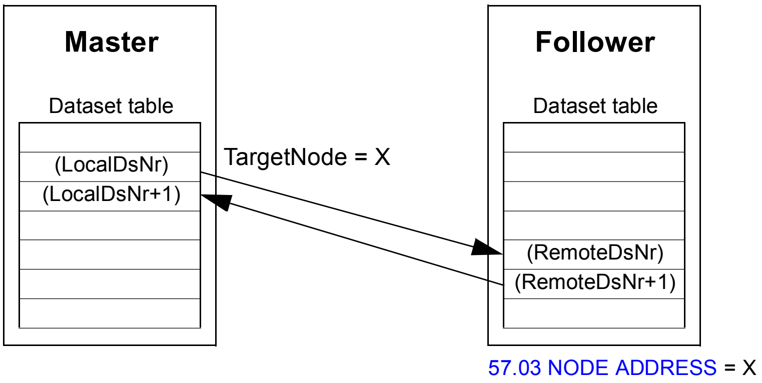
Type of messaging		**Note**
Point-to-point	Master point-to-point	Supported only at master
	Read remote	Supported only at master
	Follower multicast	Supported only at followers
Standard multicast		For both master and followers
Broadcast		For both master and followers
Token message for follower-to-follower communication		–
Chained multicast (Reference 1 only)		Supported only for drive-to-drive reference 1

Master point-to-point messaging

In this type of messaging, the master sends one dataset (LocalDsNr) from its own dataset table to the follower's. TargetNode stands for the node address of the follower; RemoteDsNr specifies the target dataset number.

The follower responds by returning the contents of the next dataset. The response is stored into dataset LocalDsNr+1 in the master.

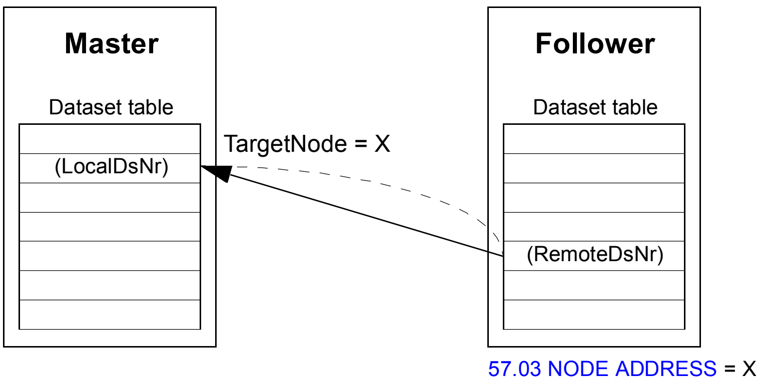
Note: Master point-to-point messaging is only supported at the master because the response is always sent to node address 0 (the master).



Read remote messaging

The master can read a dataset (RemoteDsNr) from a follower specified by TargetNode. The follower returns the contents of the requested dataset to the master. The response is stored at dataset LocalDsNr in the master.

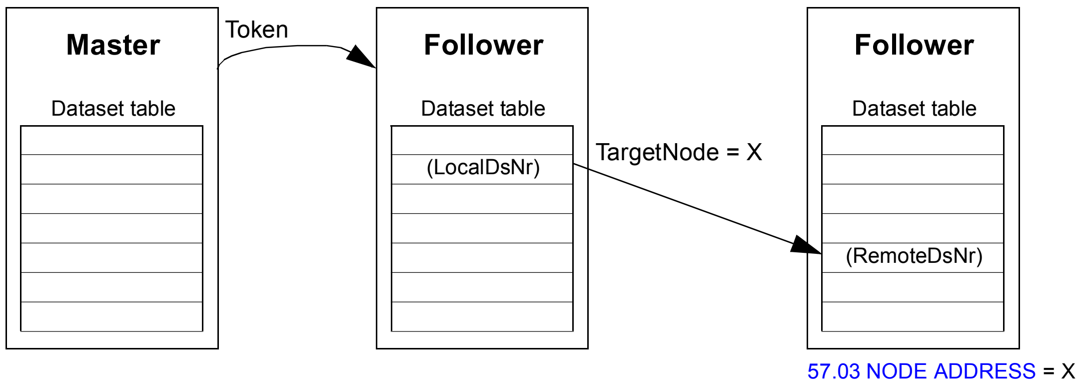
Note: Read remote messaging is only supported at the master because the response is always sent to node address 0 (the master).



Follower multicast messaging (write only)

This type of messaging is for point-to-point communication between followers. After receiving a token from the master, a follower can send one dataset to another follower with a follower multicast message. The target drive is specified using the node address.

Note: The data is not sent to the master.



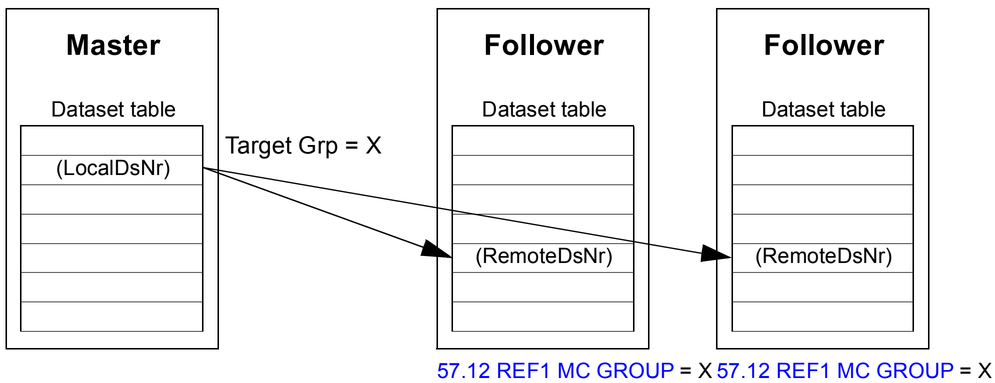
Standard multicast messaging (write only)

In standard multicast messaging, one dataset can be sent to a group of drives having the same standard multicast group address. The target group is defined by the D2D_Conf standard function block (see page 301).

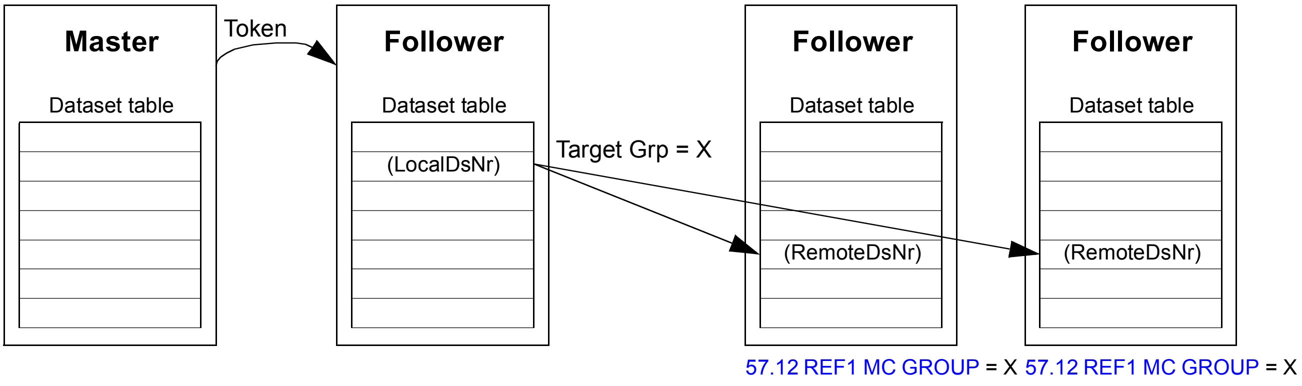
The sending drive can either be the master, or a follower after receiving a token from the master.

Note: The master does not receive the sent data even if it is a member of the target multicast group.

Master-to-follower(s) multicasting



Follower-to-follower(s) multicasting



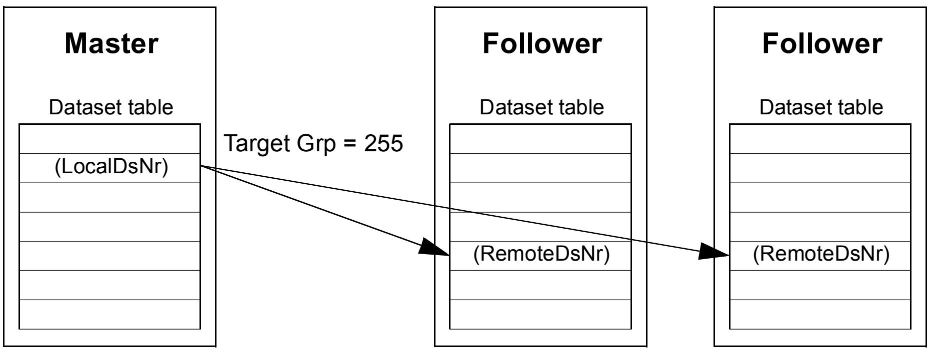
Broadcast messaging (write only)

In broadcasting, the master sends one dataset to all followers, or a follower sends one dataset to all other followers.

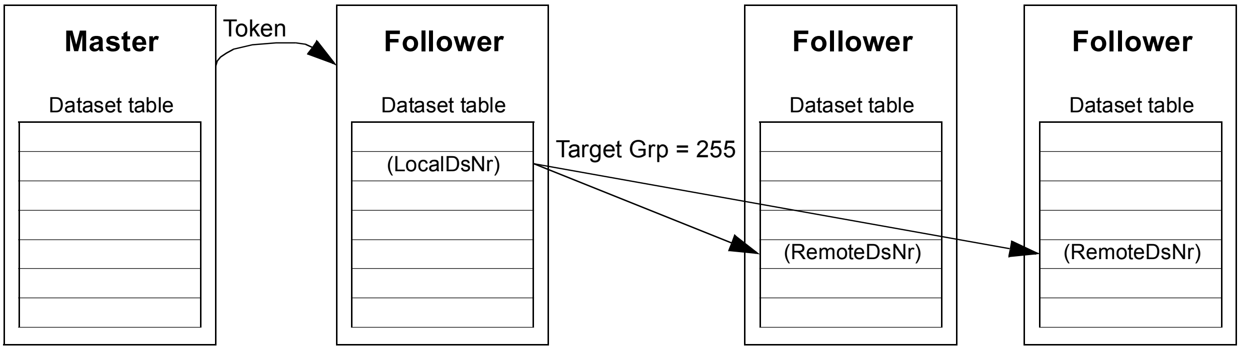
The target (Target Grp) is automatically set to 255 denoting all followers.

Note: The master does not receive any data broadcast by the followers.

Master-to-follower(s) broadcasting



Follower-to-follower(s) broadcasting



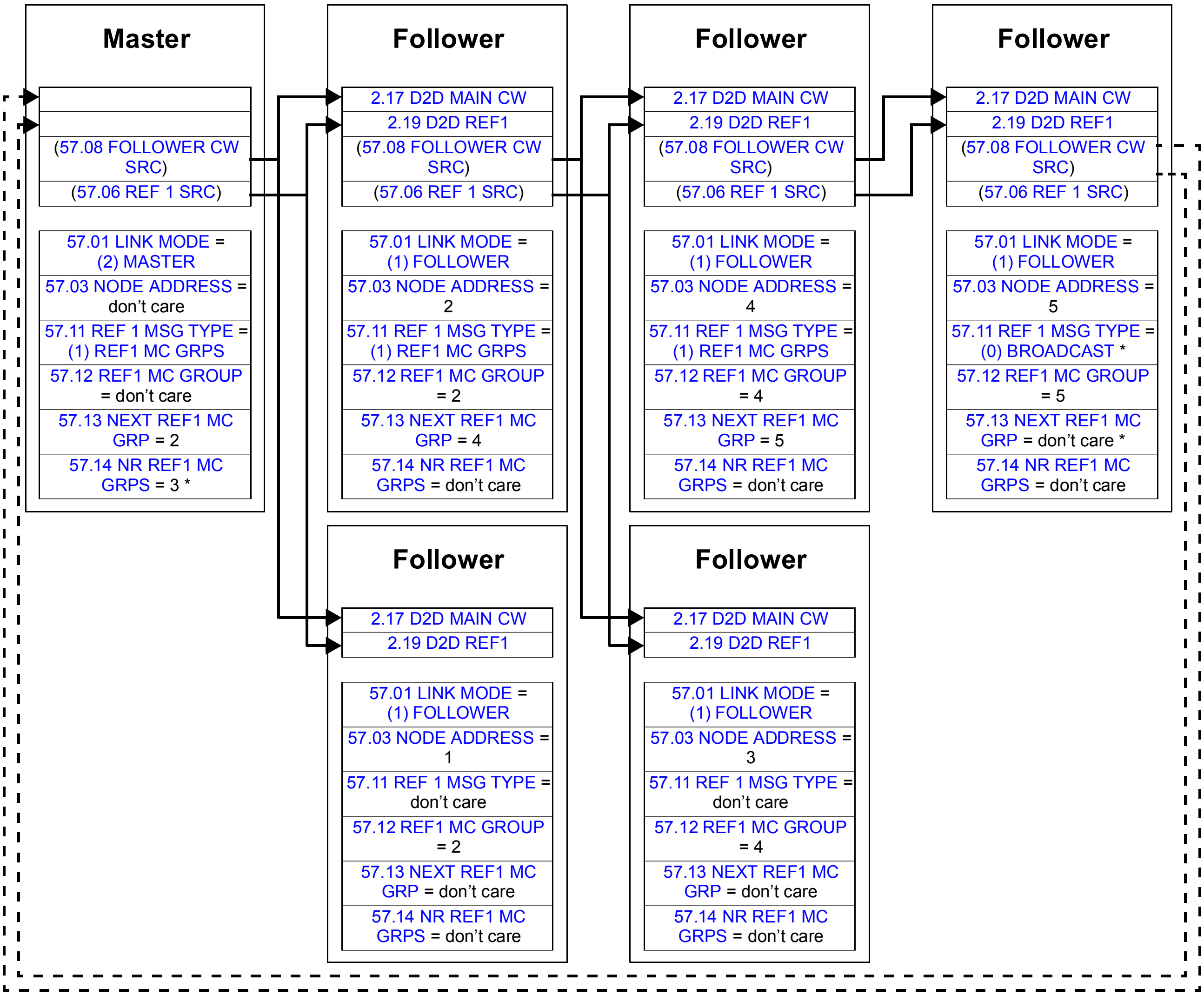
Chained multicast messaging

Chained multicasting is supported only for drive-to-drive reference 1 by the firmware.

The message chain is always started by the master. The target group is defined by parameter 57.13 NEXT REF1 MC GRP. The message is received by all followers that have parameter 57.12 REF1 MC GROUP set to the same value as parameter 57.13 NEXT REF1 MC GRP in the master.

If a follower have parameters 57.03 NODE ADDRESS and 57.12 REF1 MC GROUP set to the same value, it becomes a submaster. Immediately after a submaster receives the multicast message, it sends its own message to the next multicast group defined by parameter 57.13 NEXT REF1 MC GRP.

The duration of the entire message chain is approximately 15 microseconds multiplied by the number of links in the chain (defined by parameter 57.14 NR REF1 MC GRPS in the master).



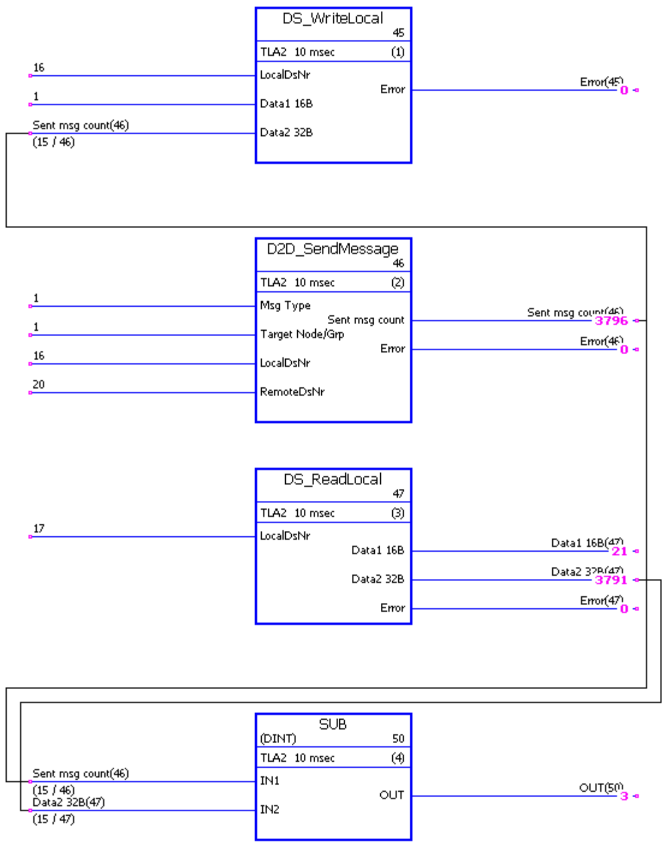
* If the last follower should send an acknowledgement to the master, the following changes would be required: In the master drive, par. 57.14 NR REF1 MC GRPS should be set to 4; in the last follower, par. 57.11 REF 1 MSG TYPE should be set to (1) REF1 MC GRPS and par. 57.13 NEXT REF1 MC GRP to 0. Note that, at the time of printing, the acknowledgement is not used in any way. In the example, sending the acknowledgement is prevented by setting par. 57.11 REF 1 MSG TYPE to (0) BROADCAST in the last follower. Alternatively, parameters 57.03 NODE ADDRESS and 57.12 REF1 MC GROUP could be set to non-equal values.

Examples of using standard function blocks in drive-to-drive communication

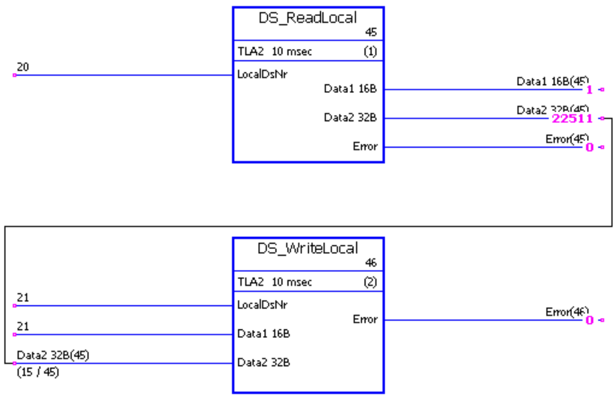
See also the descriptions of the drive-to-drive function blocks starting on page 301.

Example of master point-to-point messaging

Master



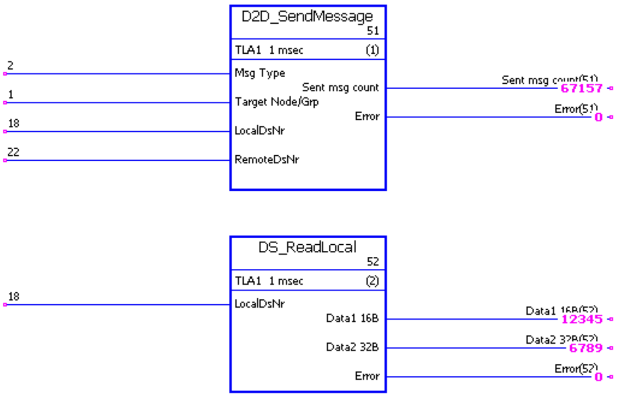
Follower (node 1)



1. The master sends a constant (1) and the value of the message counter into follower dataset 20. Data is prepared to and sent from dataset 16.
2. The follower sends the received counter value and a constant (21) as a reply to the master.
3. The master calculates the difference of the latest message number and received data.

Example of read remote messaging

Master

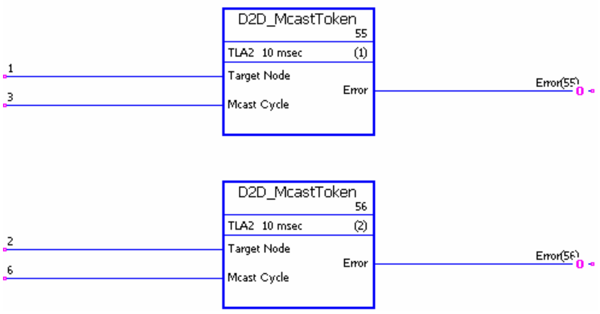


Follower (node 1)

1. The master reads the contents of the follower dataset 22 into its own dataset 18. Data is accessed using the DS_ReadLocal block.
2. In the follower, constant data is prepared into dataset 22.

Releasing tokens for follower-to-follower communication

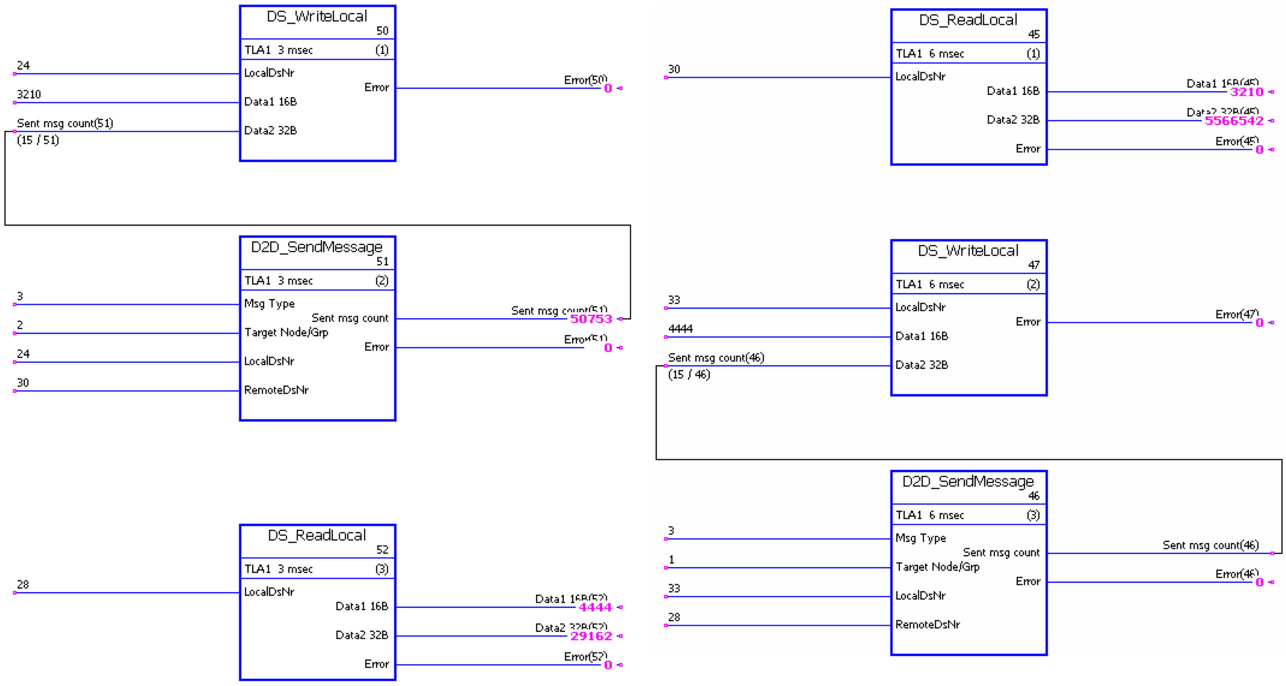
Master



1. This drive-to-drive link consists of three drives (master and two followers).
2. The master operates as a “chairman”. Follower 1 (node 1) is allowed to send one message every 3 milliseconds. Follower 2 (node 2) is allowed to send one message every 6 milliseconds.

Example of follower-to-follower multicasting

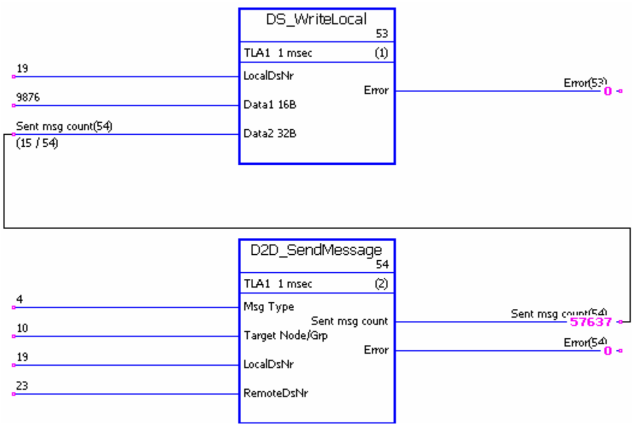
Follower 1 *Follower 2*



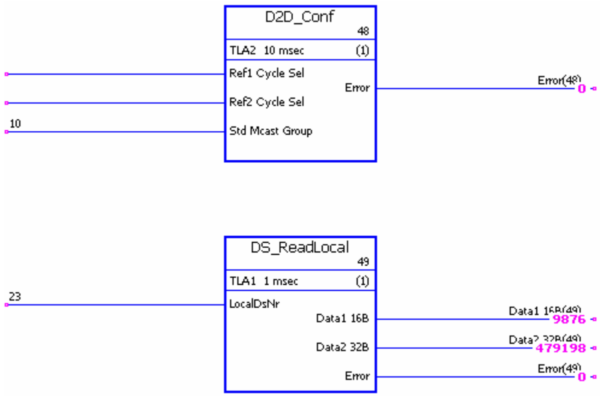
1. Follower 1 writes local dataset 24 to follower 2 dataset 30 (3 ms interval).
2. Follower 2 writes local dataset 33 to follower 1 dataset 28 (6 ms interval).
3. In addition, both followers read received data from local datasets.

Example of standard master-to-follower(s) multicast messaging

Master



Follower(s) in Std Mcast Group 10

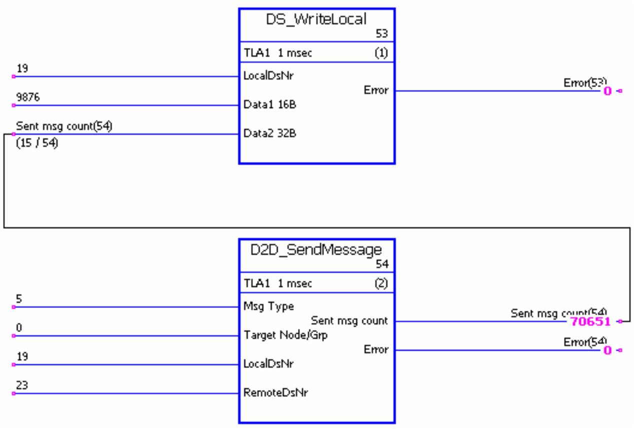


1. The master sends a constant (9876) and the value of the message counter to all followers in standard multicast group 10. The data is prepared into and sent from master dataset 19 to follower dataset 23.
2. Received data is read from dataset 23 of the receiving followers.

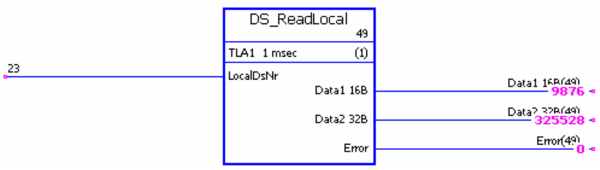
Note: The example application shown for Master above also applies to the sending follower in standard follower-to-follower multicasting.

Example of broadcast messaging

Master



Follower(s)



1. The master sends a constant (9876) and the value of the message counter to all followers. The data is prepared into and sent from master dataset 19 to follower dataset 23.
2. Received data is read from dataset 23 of the followers.

Note: The example application shown for Master above also applies to the sending follower in follower-to-follower broadcasting.

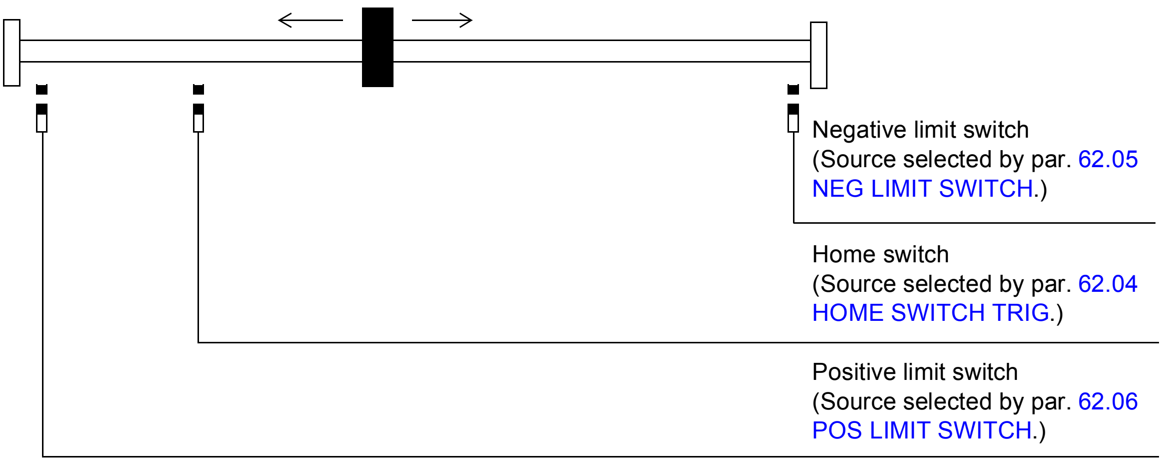
Appendix C – Homing modes

What this chapter contains

This chapter describes homing modes 1…35.

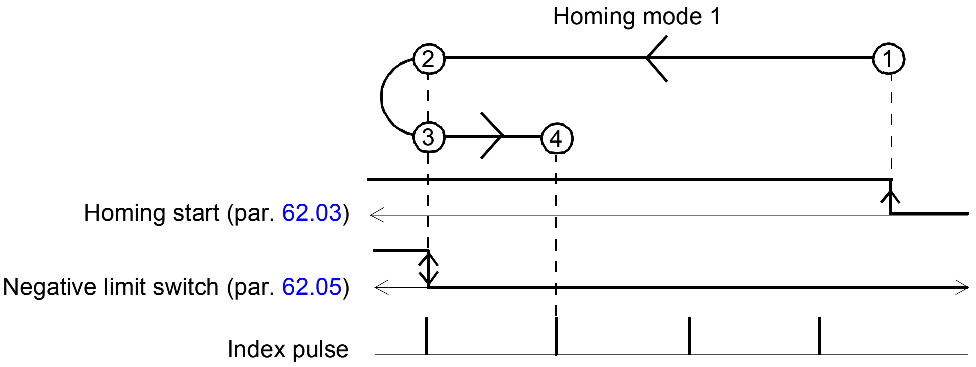
Negative direction means that the movement is to the left and positive direction means that the movement is to the right.

The following picture presents an example of an homing application:



Homing mode 1

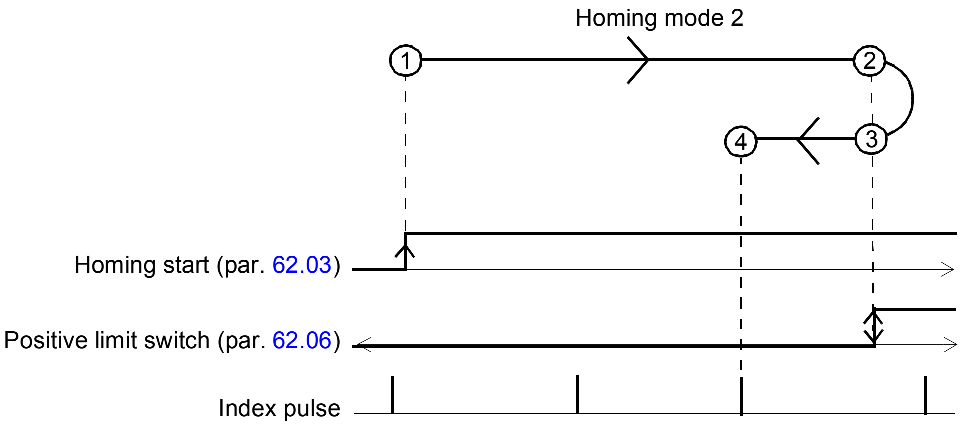
The status of the home switch at start is insignificant.



1	Start in the negative direction (left) by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the signal selected by par. 62.05 NEG LIMIT SWITCH.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the signal selected by par. 62.05 NEG LIMIT SWITCH.
4	Stop by the next index pulse.

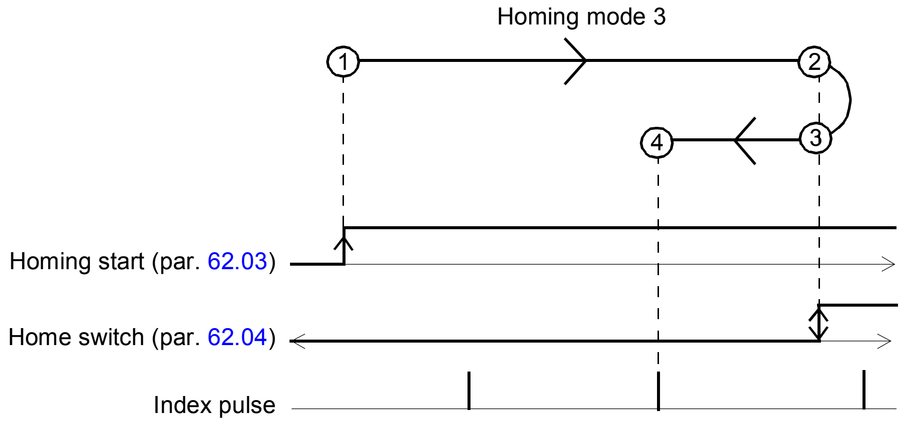
Homing mode 2

The status of the home switch at start is insignificant.

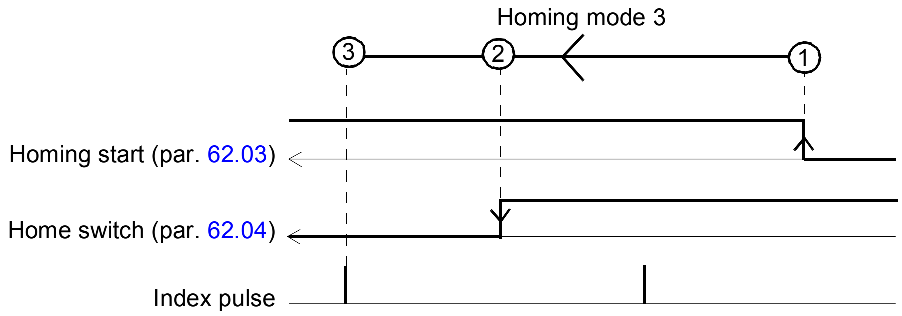


1	Start in the positive direction (right) by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the signal selected by par. 62.06 POS LIMIT SWITCH.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the signal selected by par. 62.06 POS LIMIT SWITCH.
4	Stop by the next index pulse.

Homing mode 3

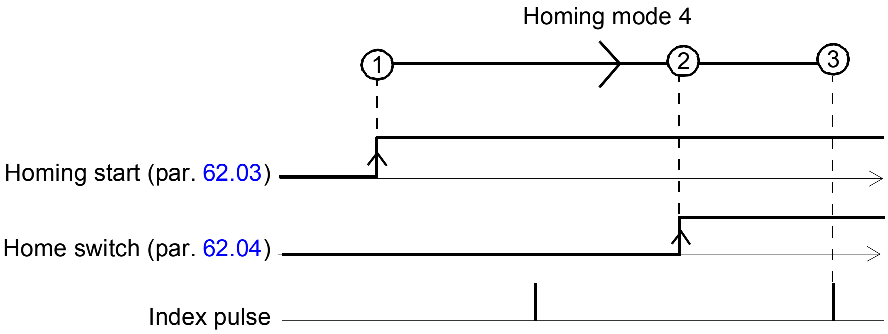


1	If the home switch signal is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

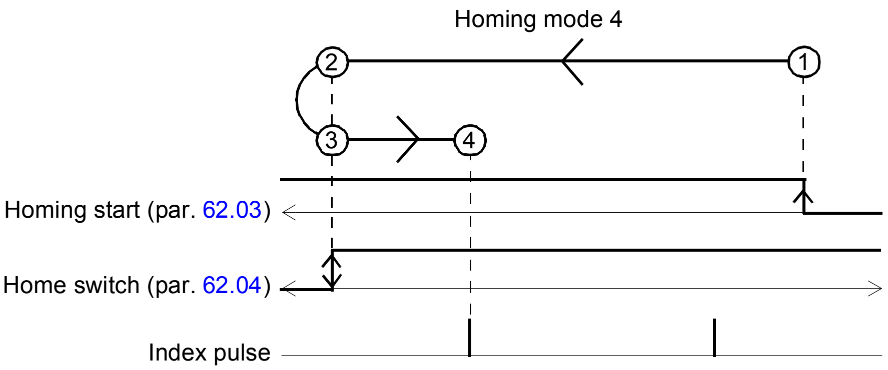


1	If the home switch signal is 1 (par. 62.04 HOME SWITCH TRIG): Start in the negative direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the next index pulse.

Homing mode 4

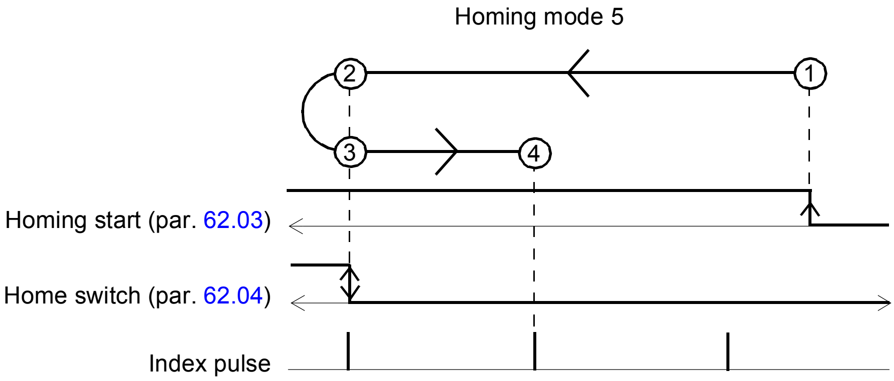


1	If the home switch signal is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the next index pulse.

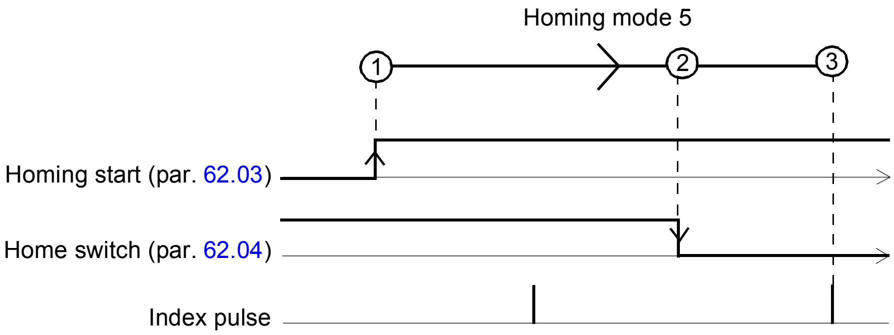


1	If the home switch signal is 1 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

Homing mode 5

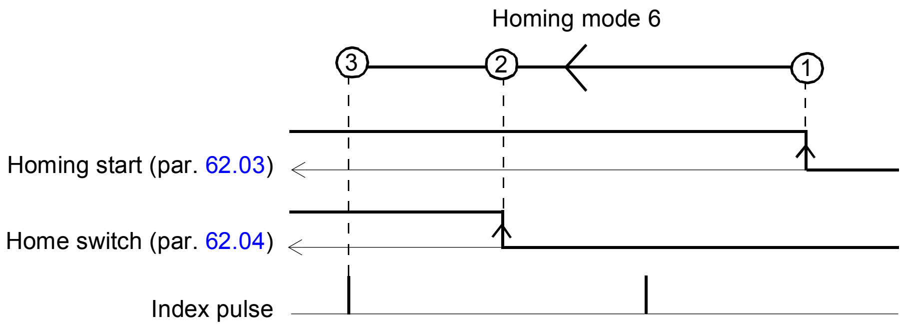


1	If the home switch signal is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

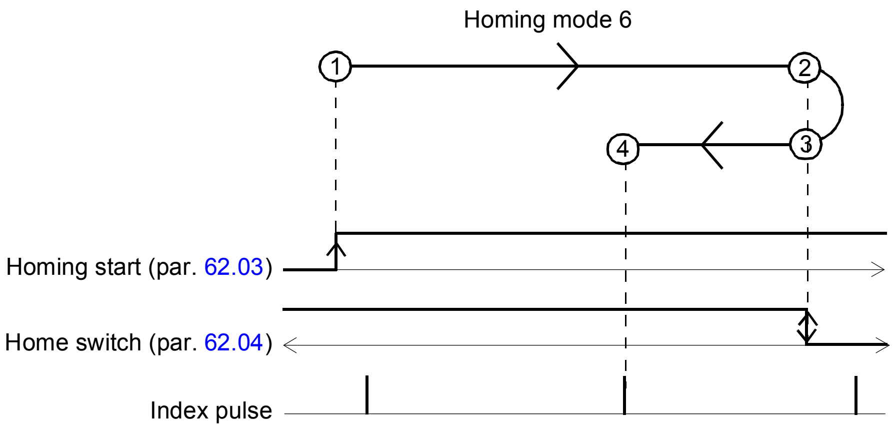


1	If the home switch signal is 1 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right direction) by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the next index pulse.

Homing mode 6

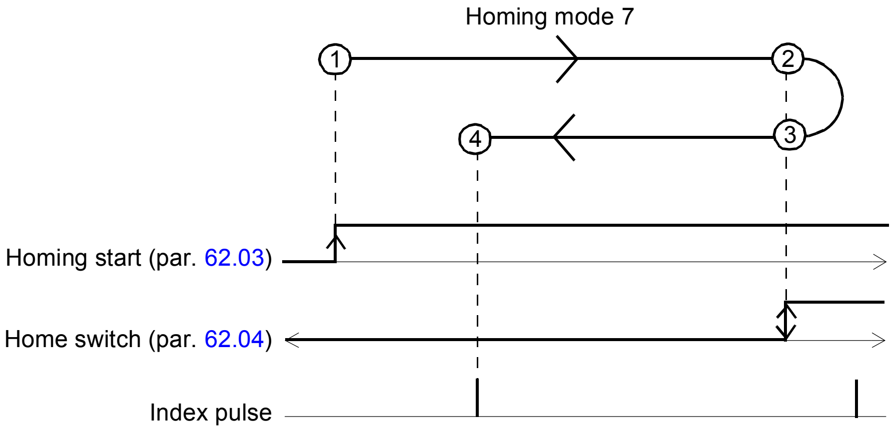


1	If the home switch signal is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1. With homing mode 4, the start direction is positive (right). With homing mode 6, the start direction is negative (left).
2	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the next index pulse.

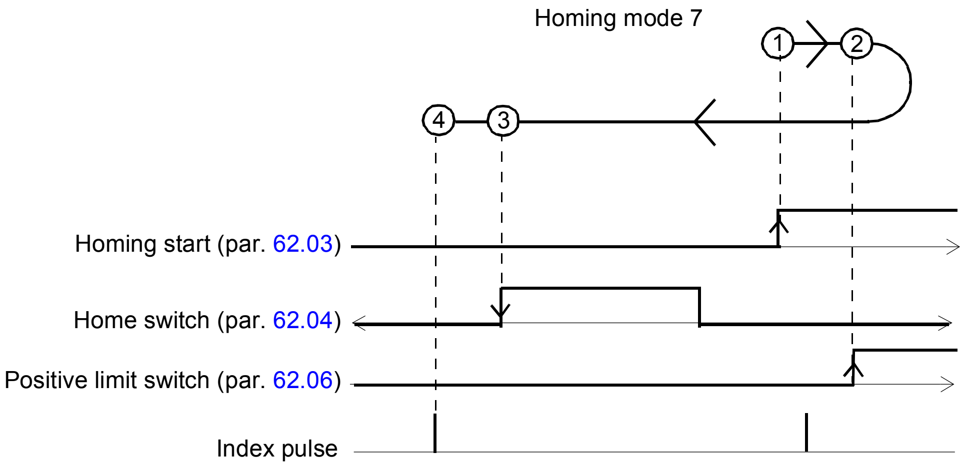


1	If the home switch signal is 1 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

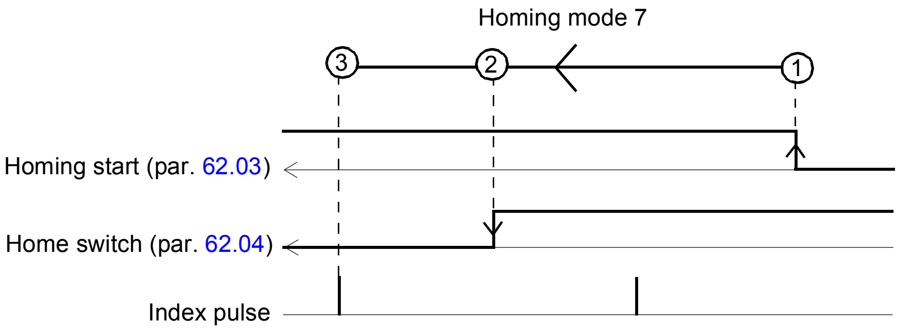
Homing mode 7



1	If the home switch signal is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

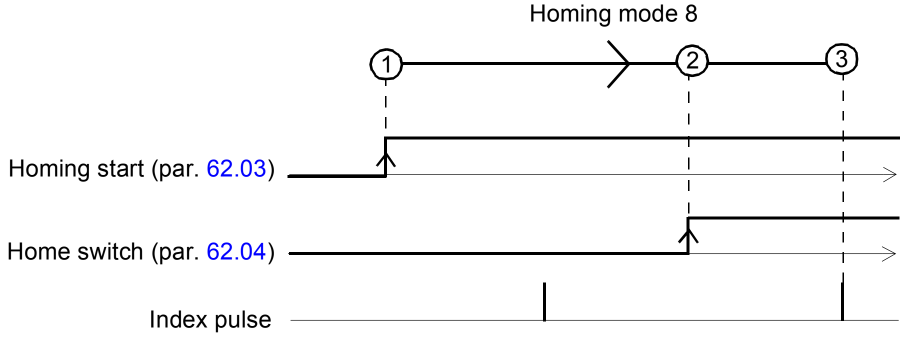


1	If the home switch signal is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the positive limit switch signal selected by par. 62.06 POS LIMIT SWITCH.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

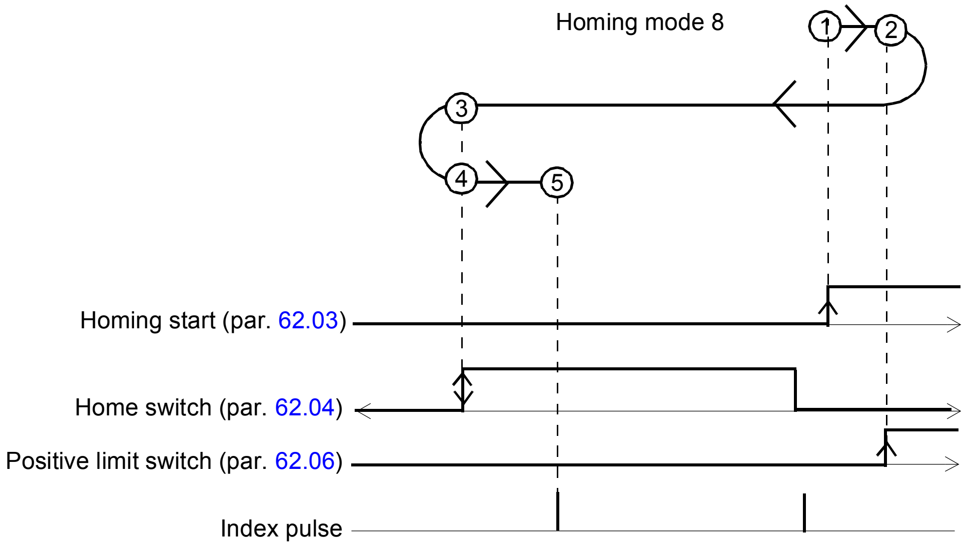


1	If the home switch signal is 1 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the next index pulse.

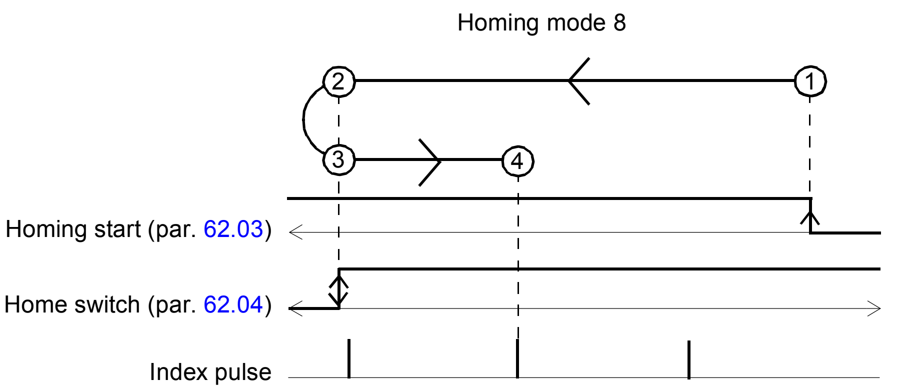
Homing mode 8



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the next index pulse.

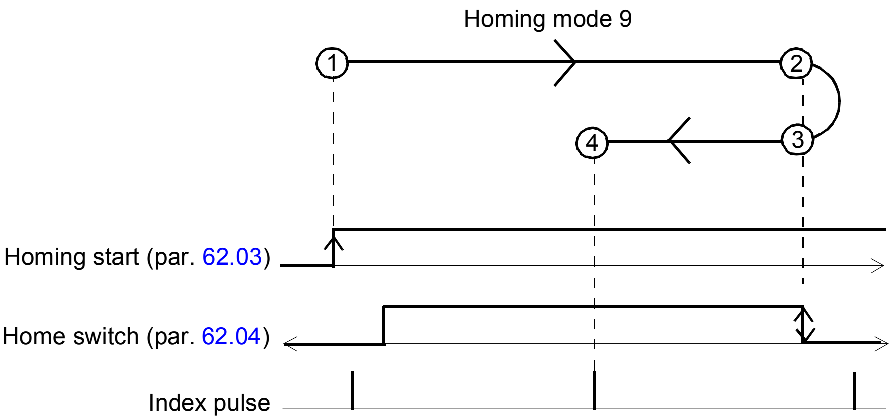


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the positive limit switch signal selected by par. 62.06 POS LIMIT SWITCH.
3	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
5	Stop by the next index pulse.

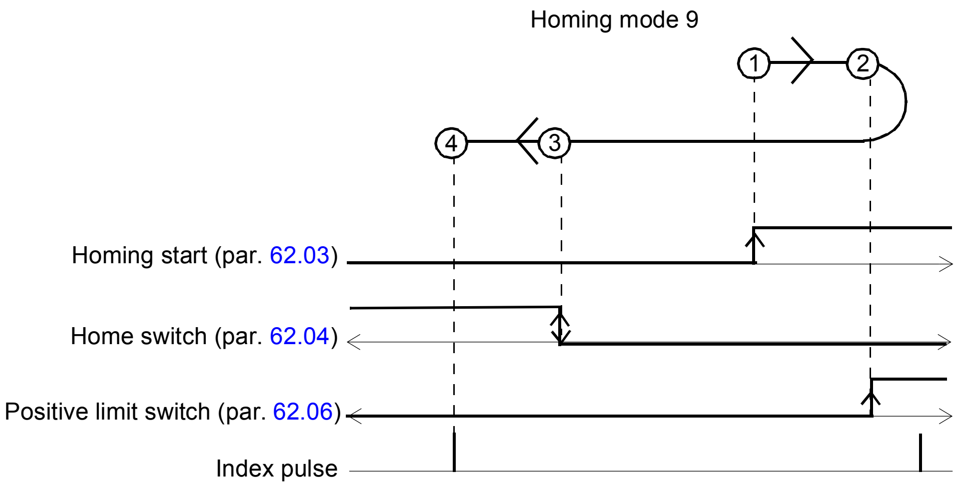


1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

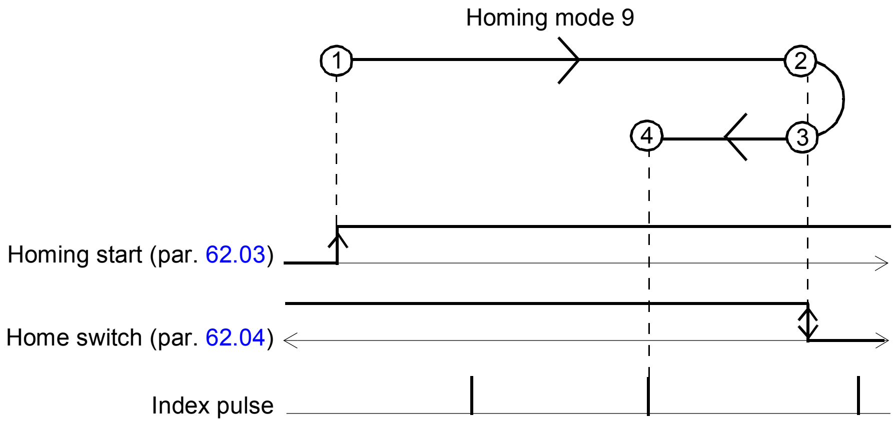
Homing mode 9



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the positive limit switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

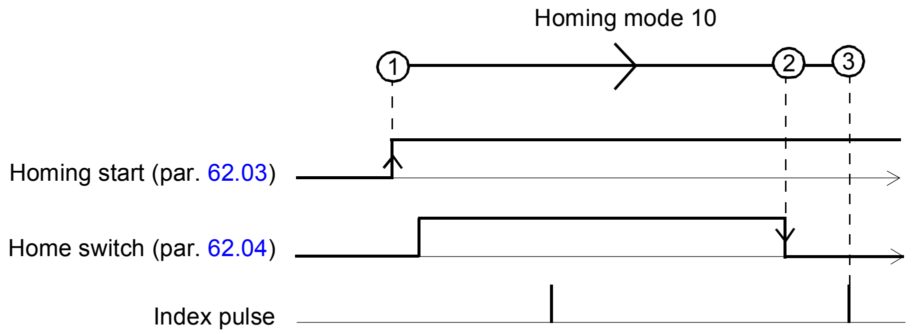


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the home switch signal selected by par. 62.06 POS LIMIT SWITCH.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

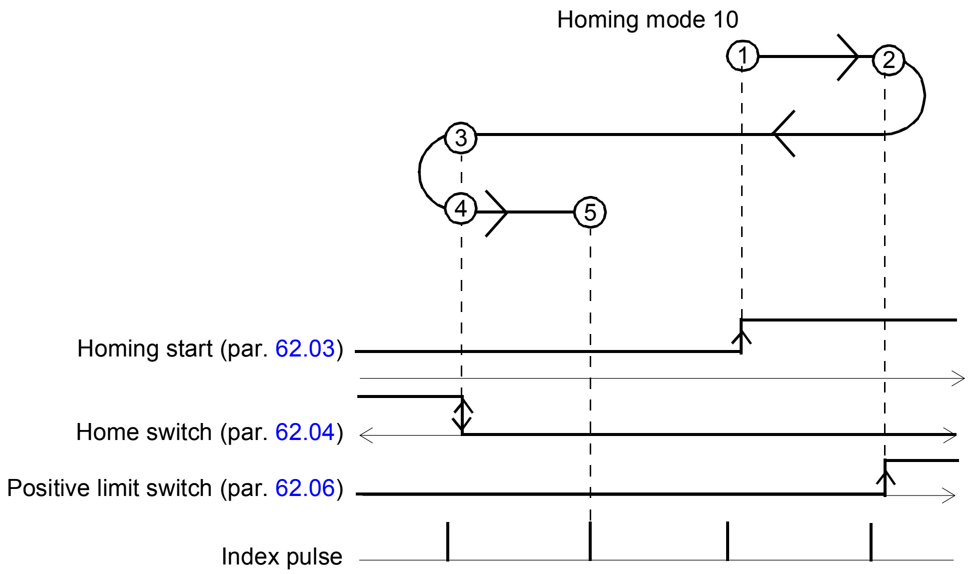


1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the positive limit switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

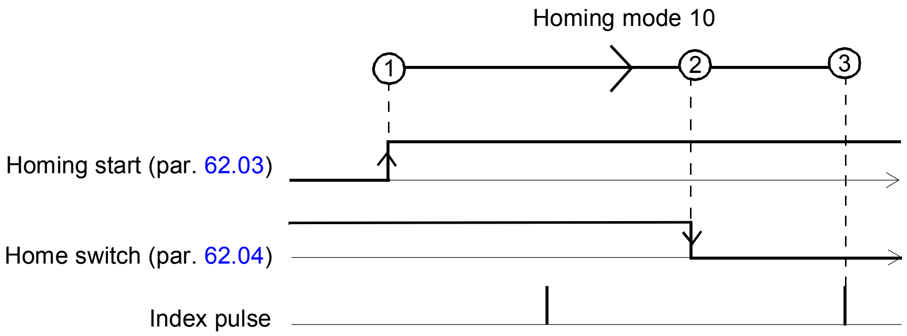
Homing mode 10



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the next index pulse.

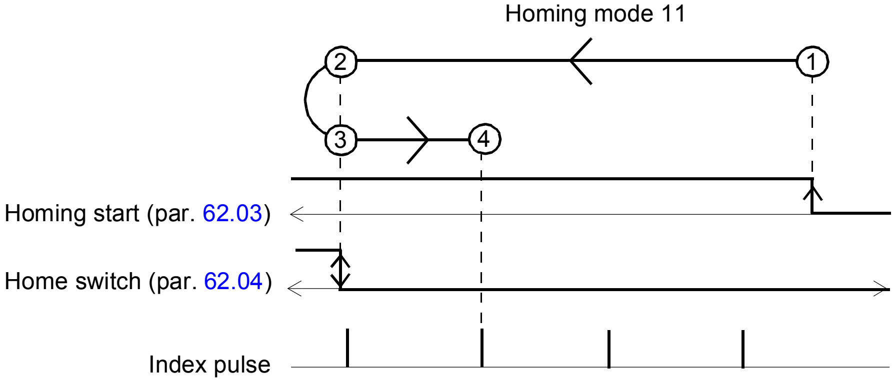


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the positive limit switch signal selected by par. 62.06 POS LIMIT SWITCH.
3	Change direction by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
5	Stop by the next index pulse.

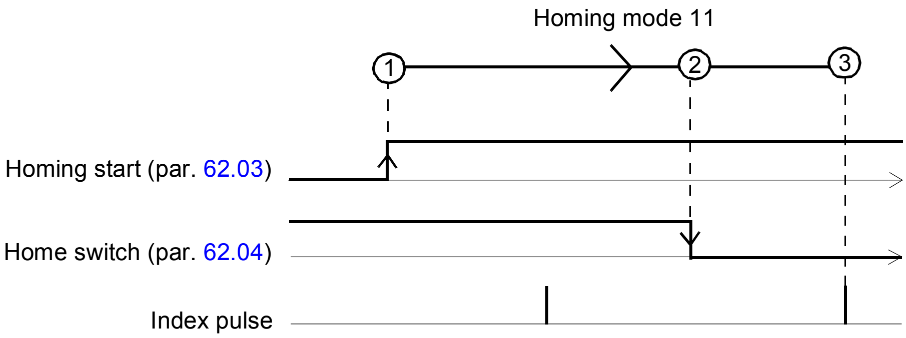


1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the next index pulse.

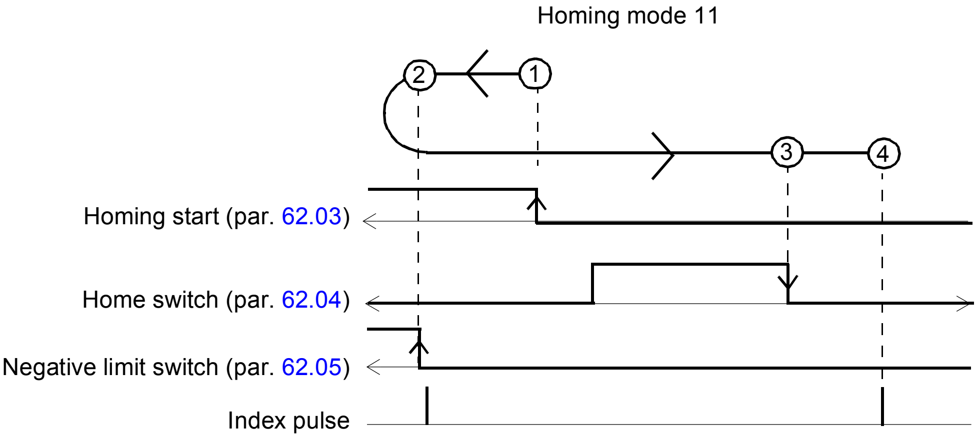
Homing mode 11



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

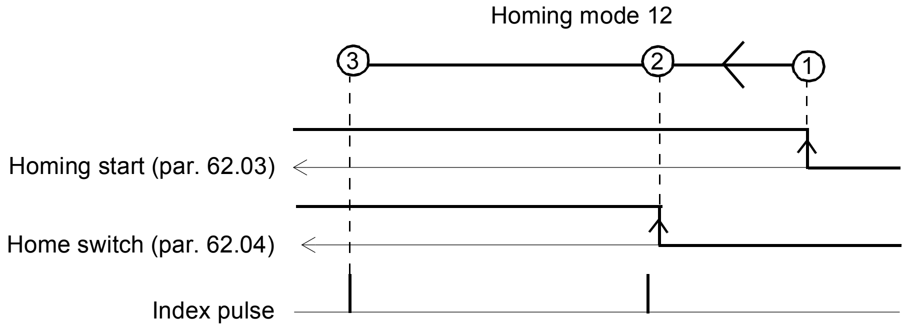


1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the next index pulse.

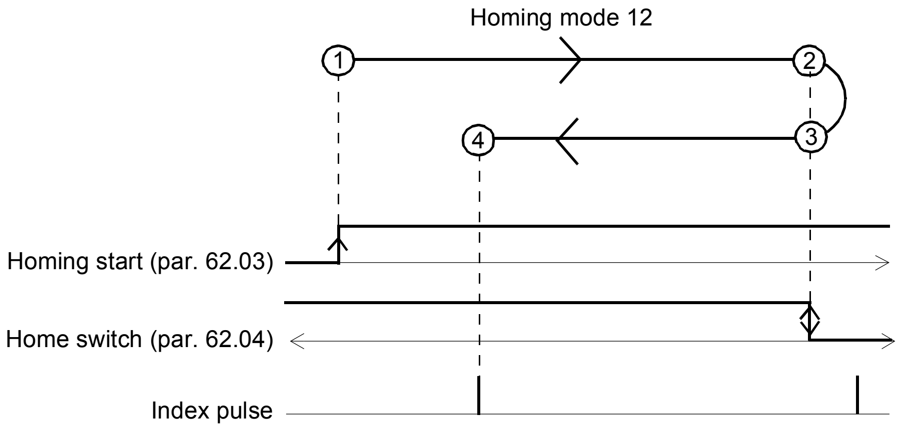


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the negative limit switch signal selected by par. 62.05 NEG LIMIT SWITCH.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

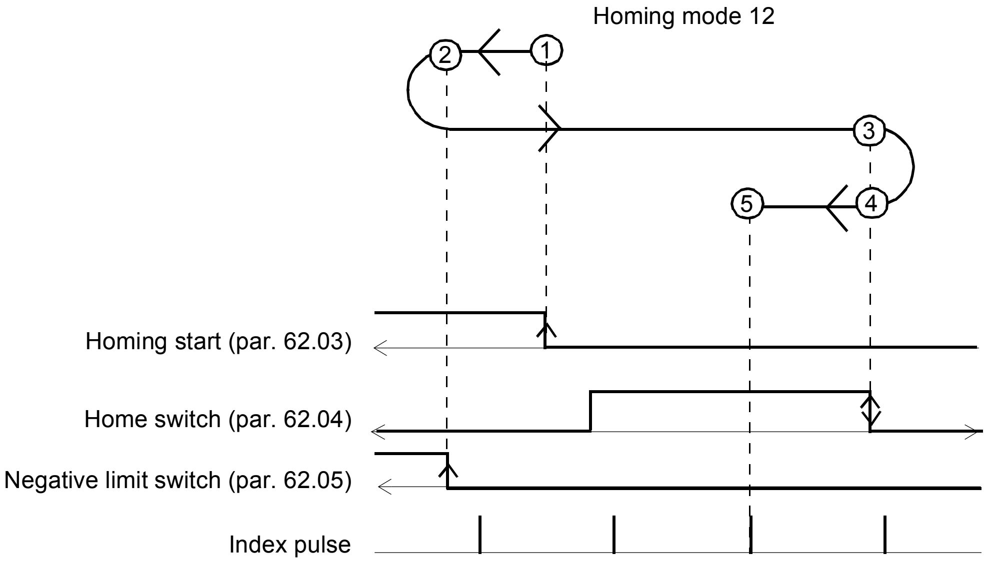
Homing mode 12



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the next index pulse.

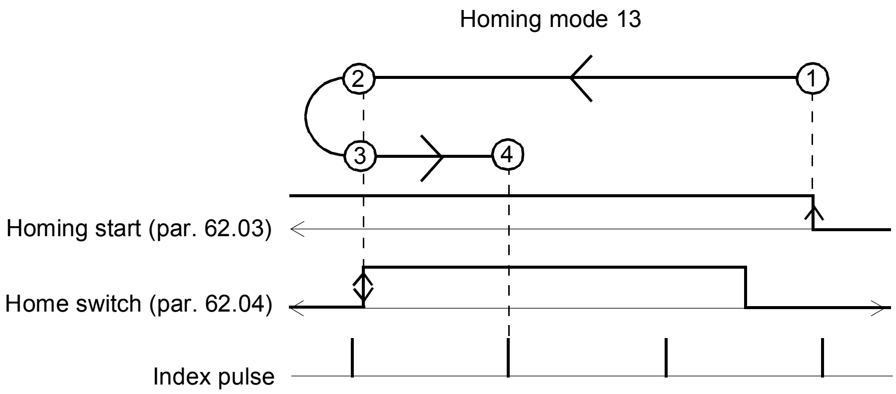


1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

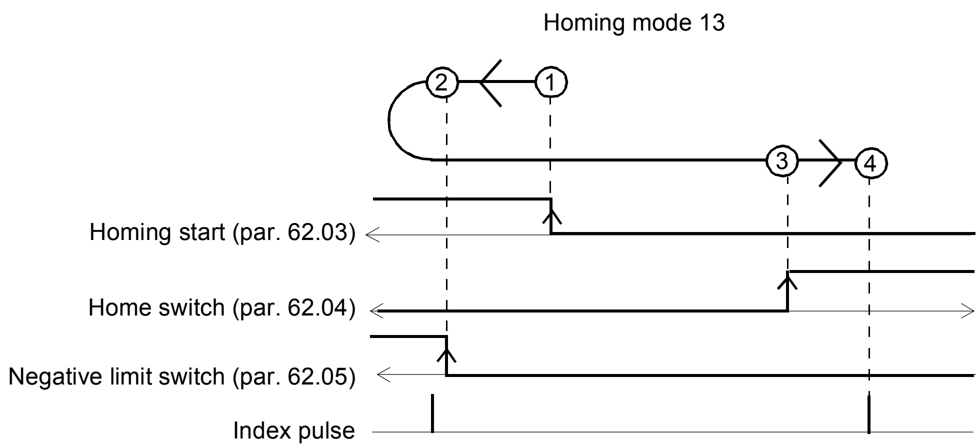


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the negative limit switch signal selected by par. 62.05 NEG LIMIT SWITCH.
3	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
5	Stop by the next index pulse.

Homing mode 13

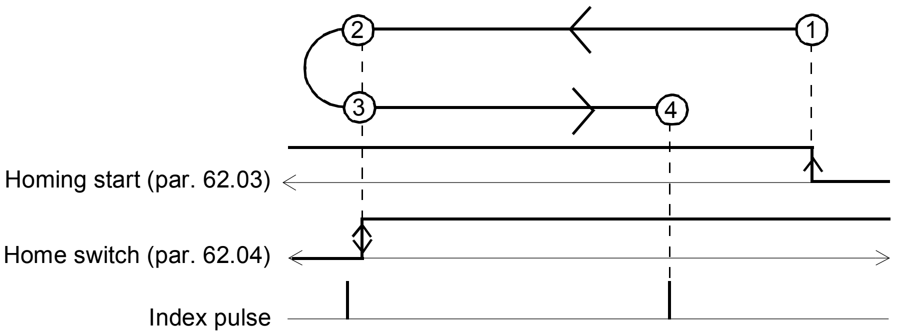


1	If the state of the home switch is 0: Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.



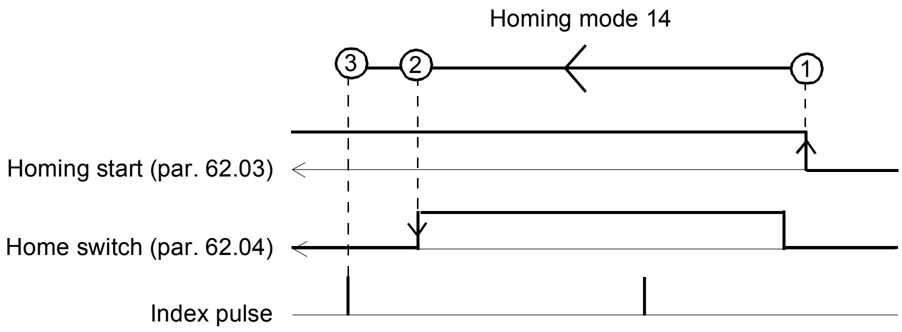
1	If the state of the home switch is 0: Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the negative limit switch signal selected by par. 62.05 NEG LIMIT SWITCH.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

Homing mode 13

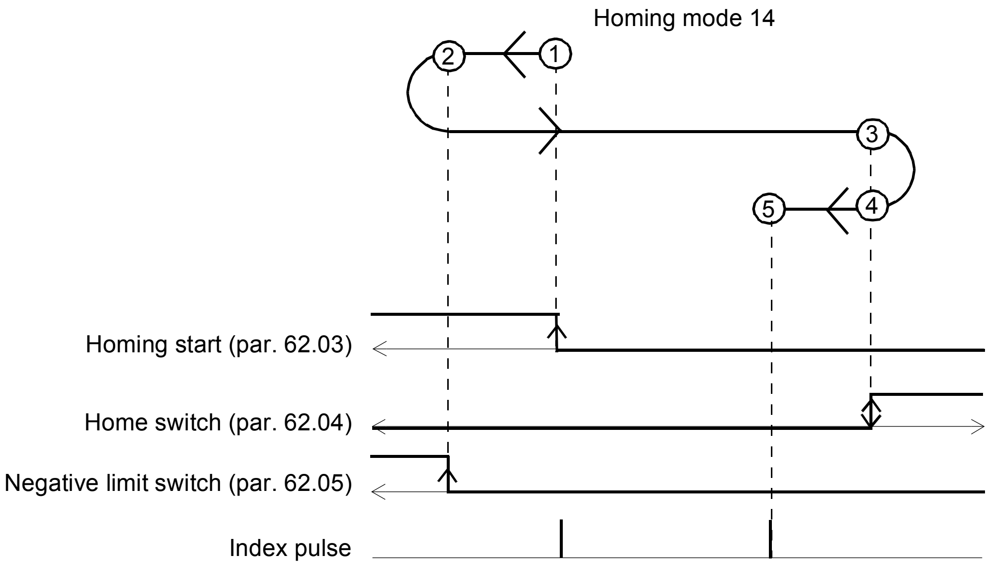


1	If the state of the home switch is 1: Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the next index pulse.

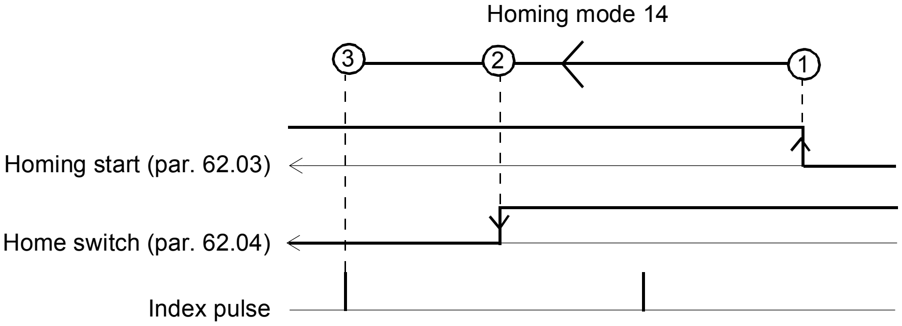
Homing mode 14



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the next index pulse.



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the negative limit switch signal selected by par. 62.05 NEG LIMIT SWITCH.
3	Change direction by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
5	Stop by the next index pulse.



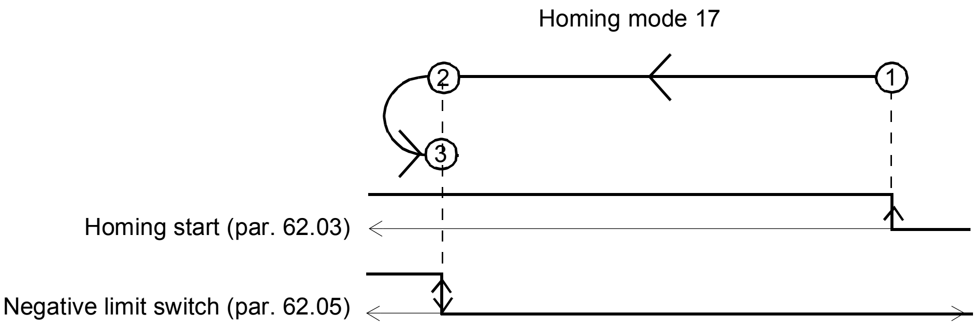
1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change to homing speed 2, par. 62.08 HOMING SPEEDREF2, by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the next index pulse.

Homing modes 15 and 16

Reserved

Homing mode 17

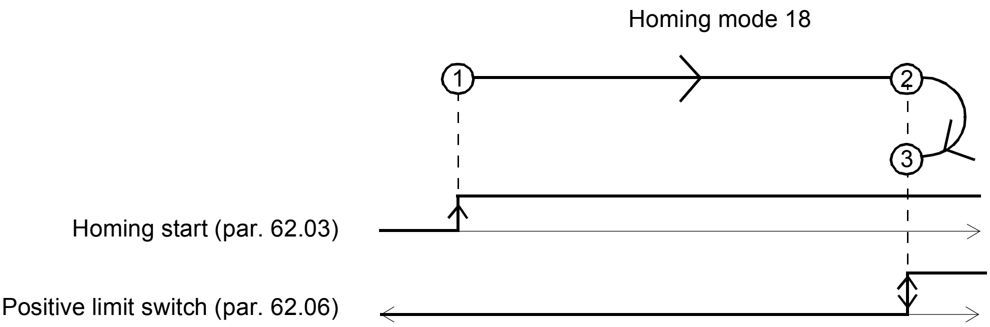
The status of the home switch at start is insignificant.



1	Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the negative limit switch signal selected by par. 62.05 NEG LIMIT SWITCH.
3	Stop by the falling edge of the negative limit switch signal selected by par. 62.05 NEG LIMIT SWITCH.

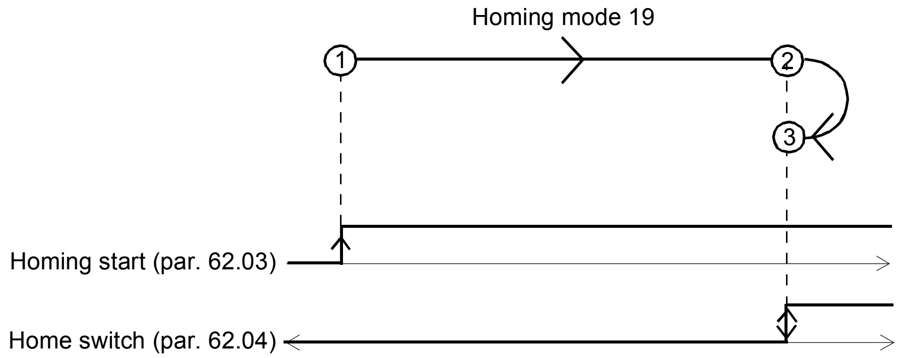
Homing mode 18

The status of the home switch at start is insignificant.

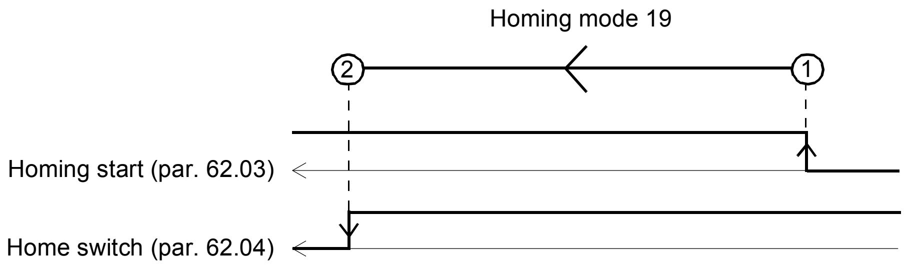


1	Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the positive limit switch signal selected by par. 62.06 POS LIMIT SWITCH.
3	Stop by the falling edge of the positive limit switch signal selected by par. 62.06 POS LIMIT SWITCH.

Homing mode 19

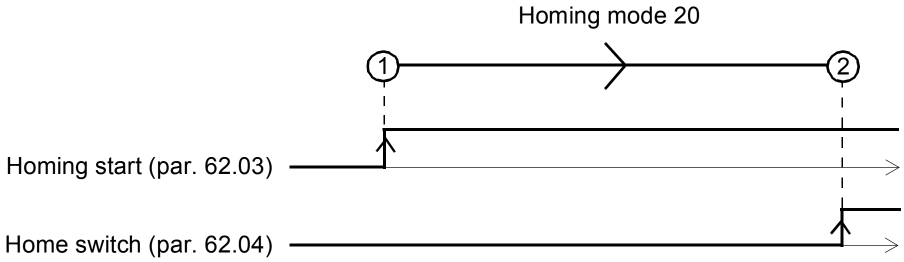


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

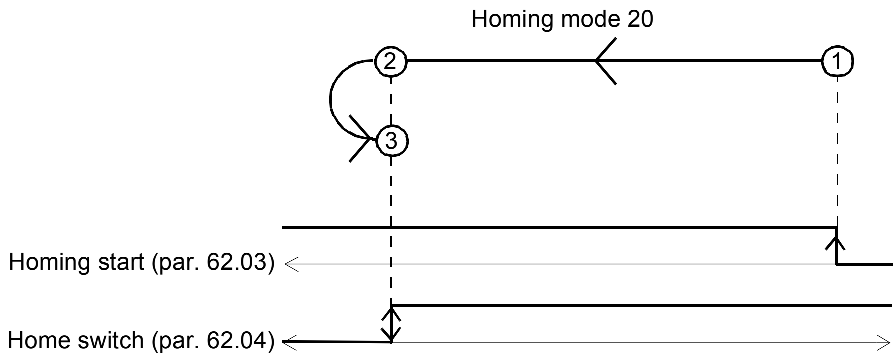


1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

Homing mode 20

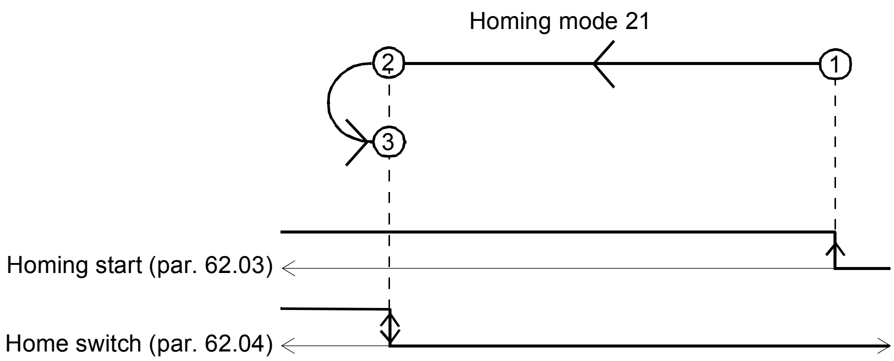


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

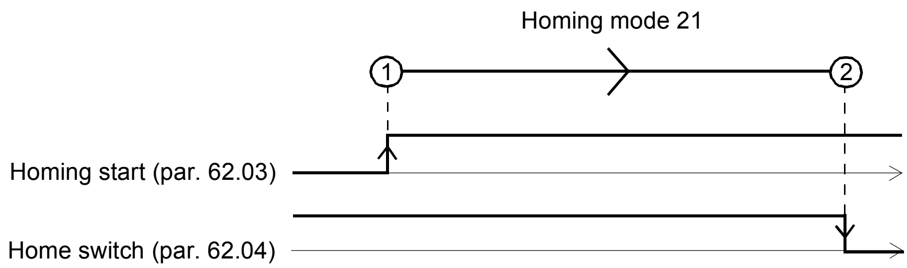


1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

Homing mode 21

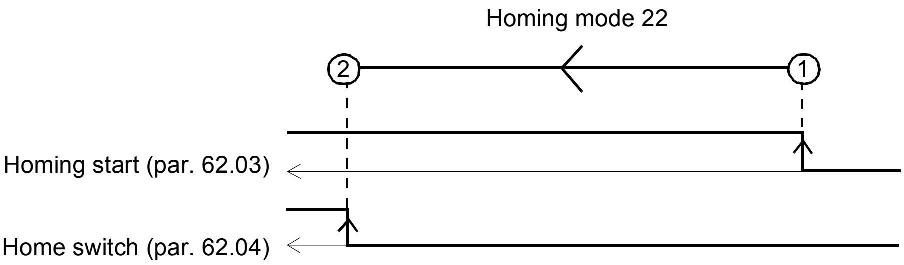


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

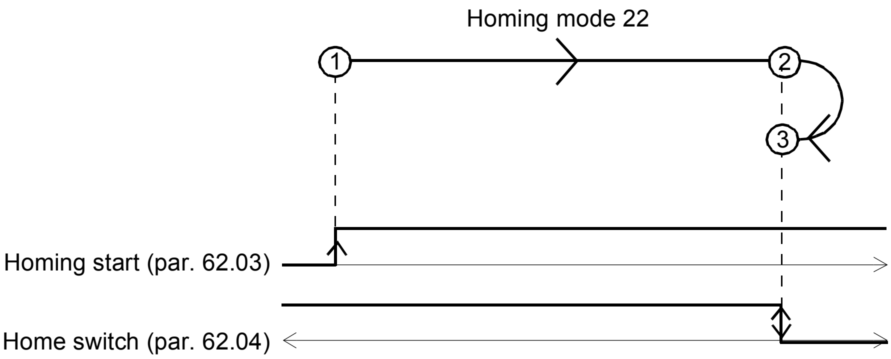


1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

Homing mode 22

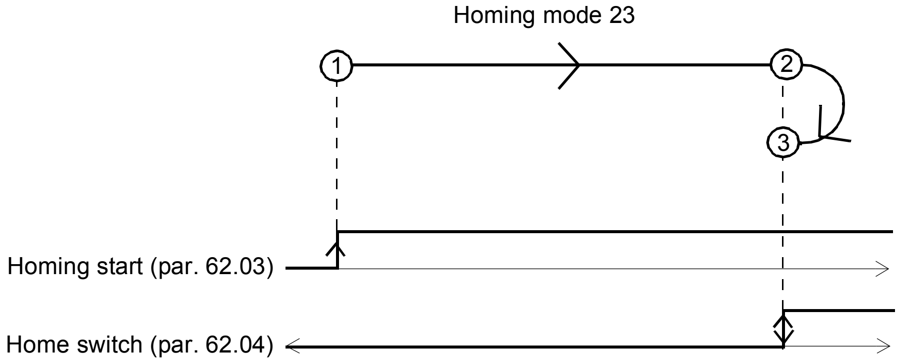


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

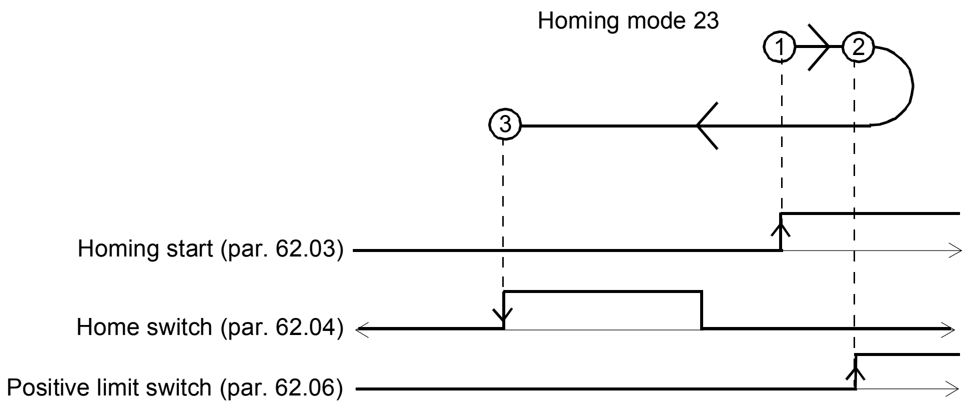


1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

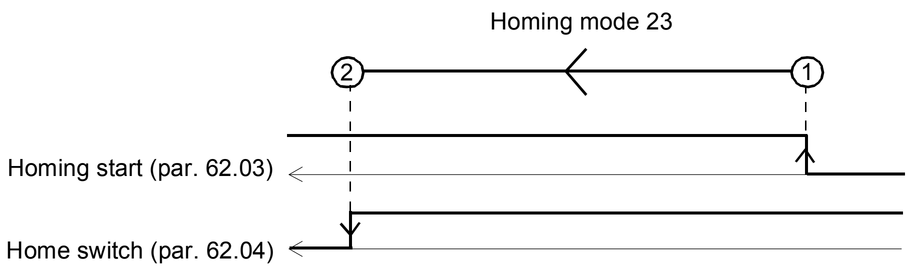
Homing mode 23



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

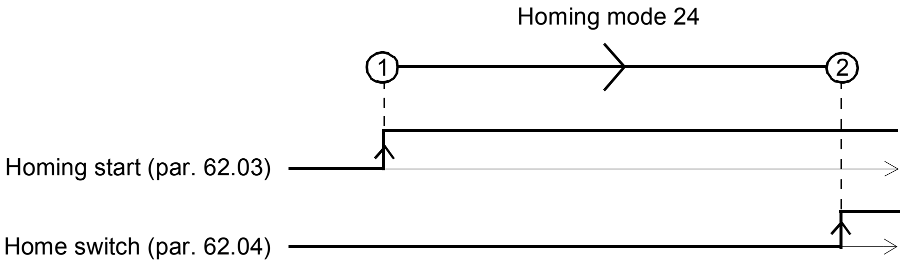


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the positive limit switch selected by par. 62.06 POS LIMIT SWITCH.
3	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

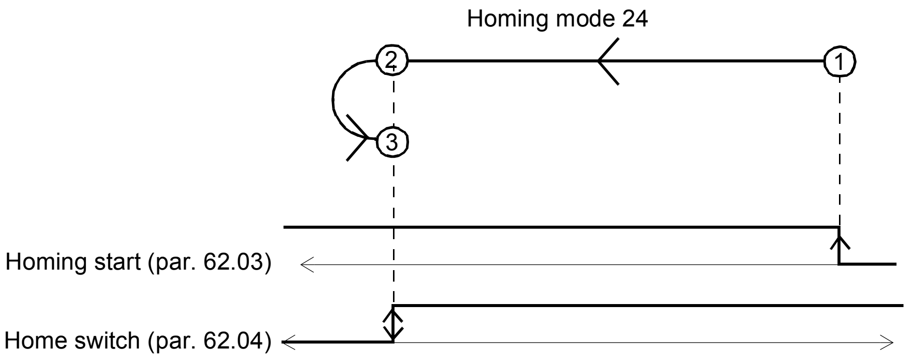


1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

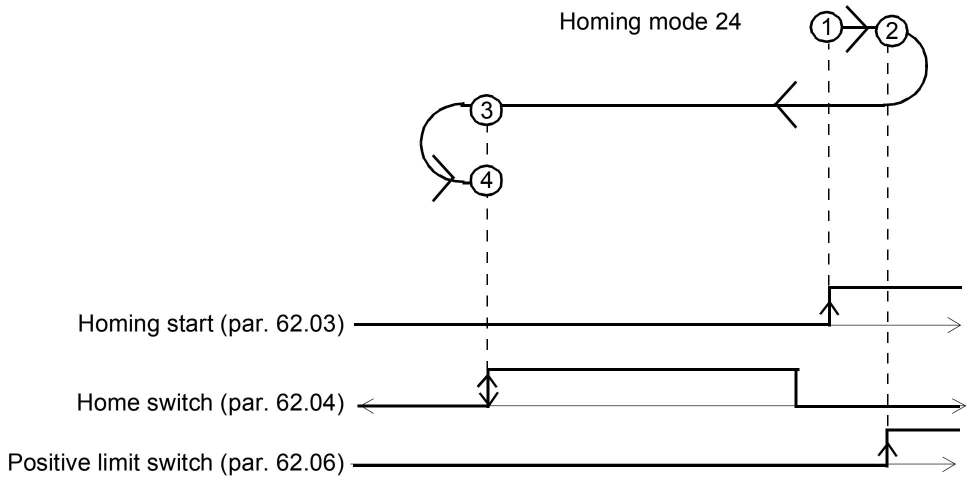
Homing mode 24



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

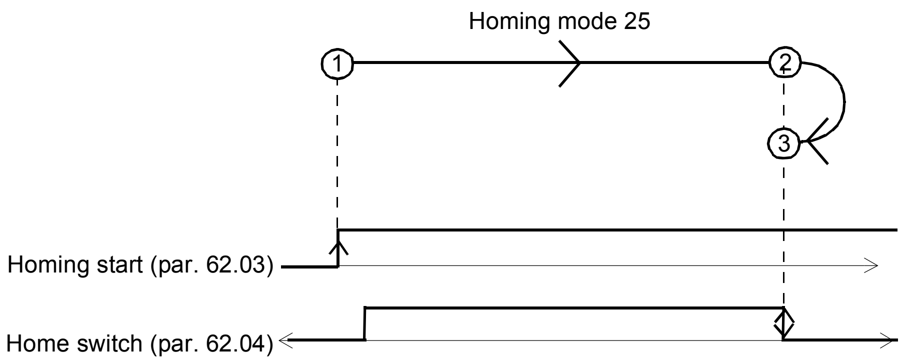


1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the negative (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

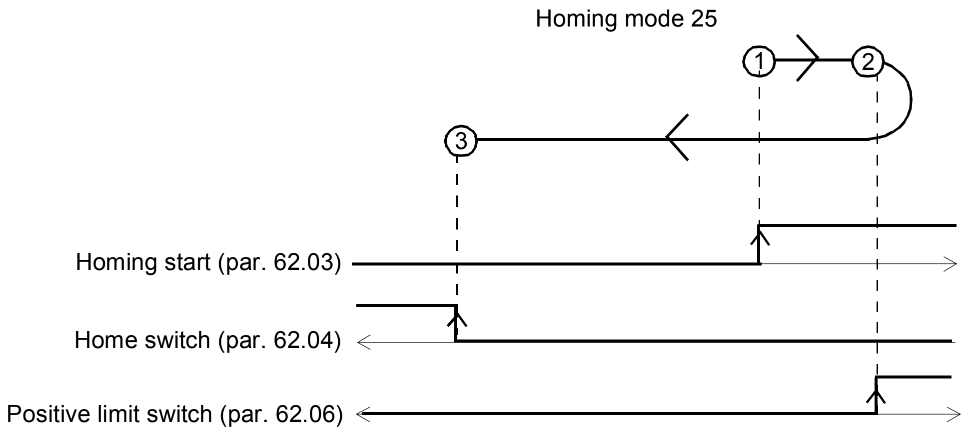


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the positive limit switch signal selected by par. 62.06 POS LIMIT SWITCH.
3	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

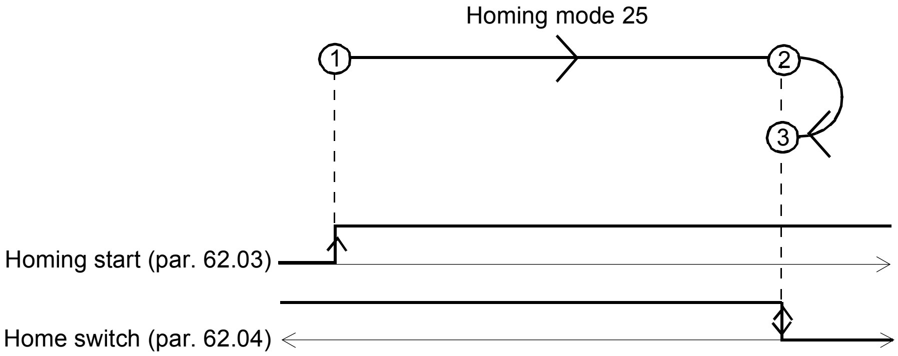
Homing mode 25



1	If the state of the home switch is 0: (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

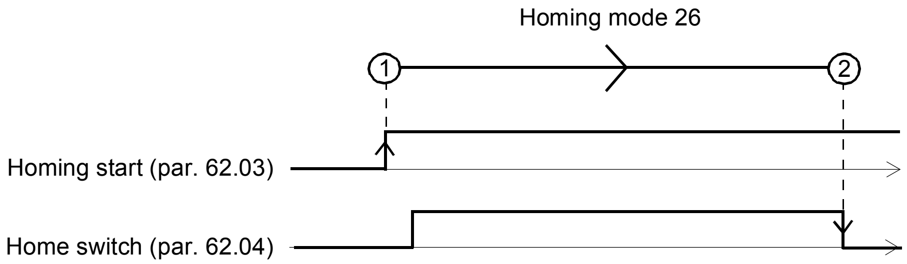


1	If the state of the home switch is 0: (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the positive limit switch signal selected by par. 62.06 POS LIMIT SWITCH.
3	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

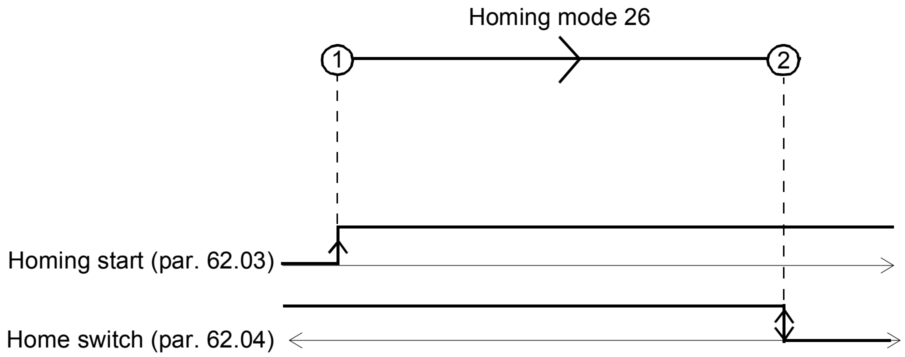


1	If the state of the home switch is 1: (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

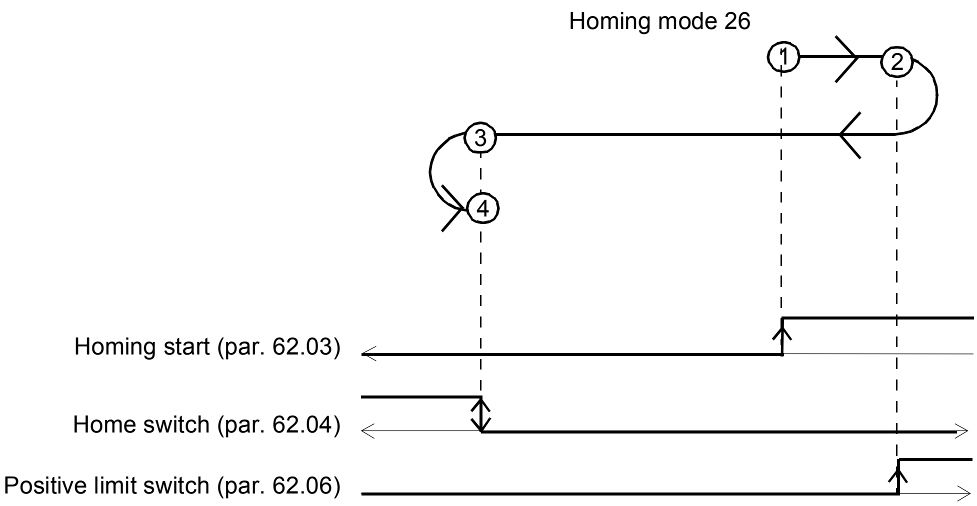
Homing mode 26



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

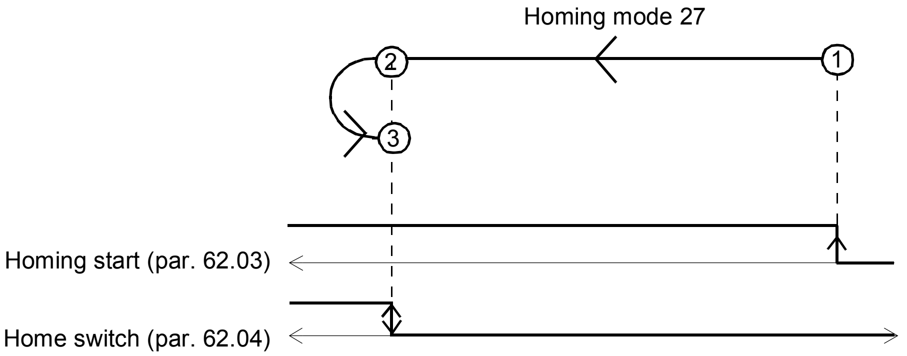


1	If the state of the home switch is 1: (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

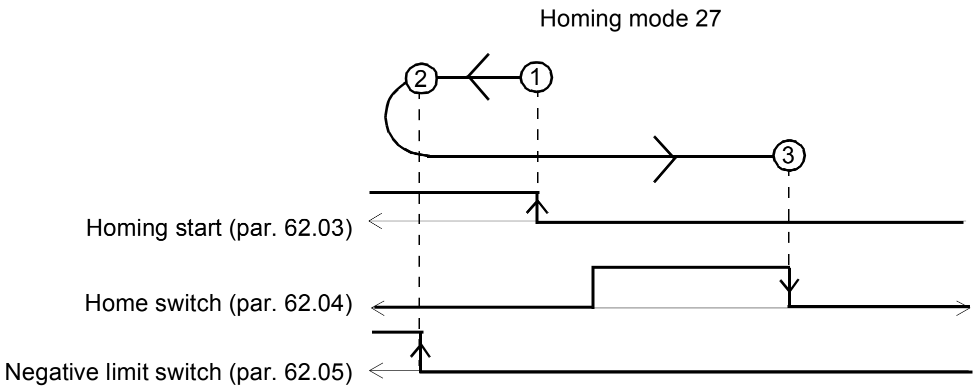


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the positive limit switch signal selected by par. 62.06 POS LIMIT SWITCH.
3	Change direction by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

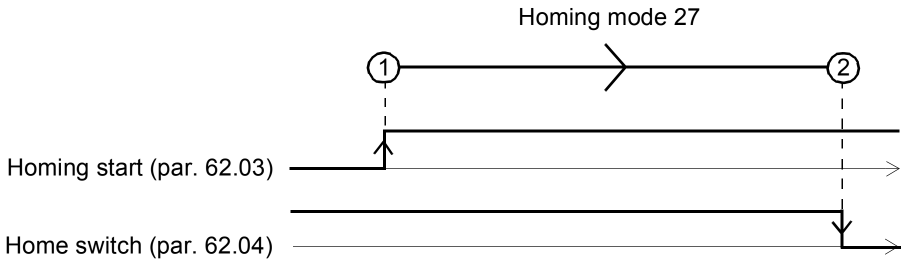
Homing mode 27



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

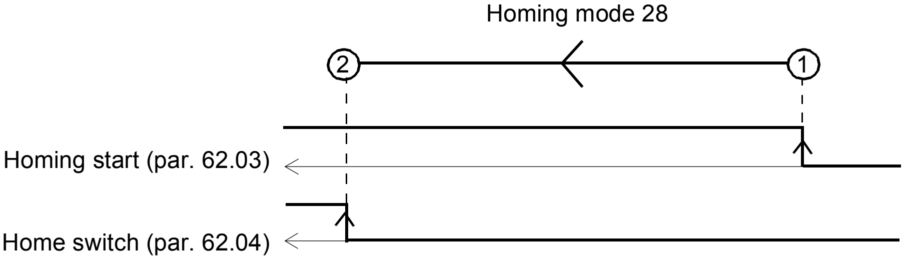


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the negative limit switch signal selected by par. 62.05 NEG LIMIT SWITCH.
3	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

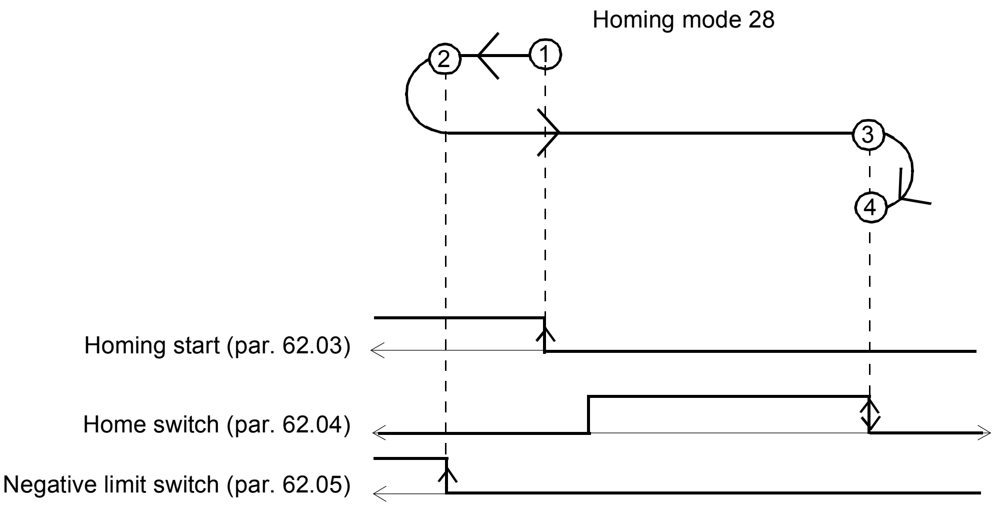


1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

Homing mode 28

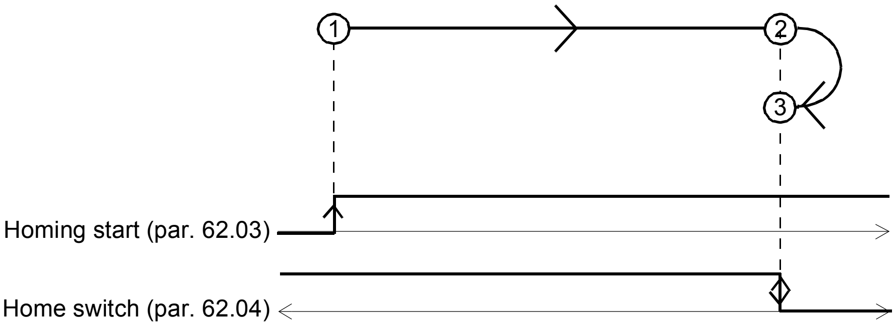


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.



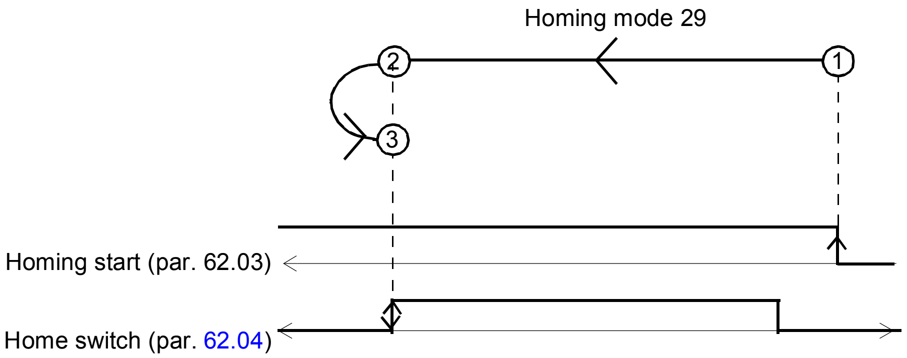
1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the negative limit switch signal selected by par. 62.05 NEG LIMIT SWITCH.
3	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG. **Note:** Stop is only possible after a falling edge of the home switch has been detected.

Homing mode 28

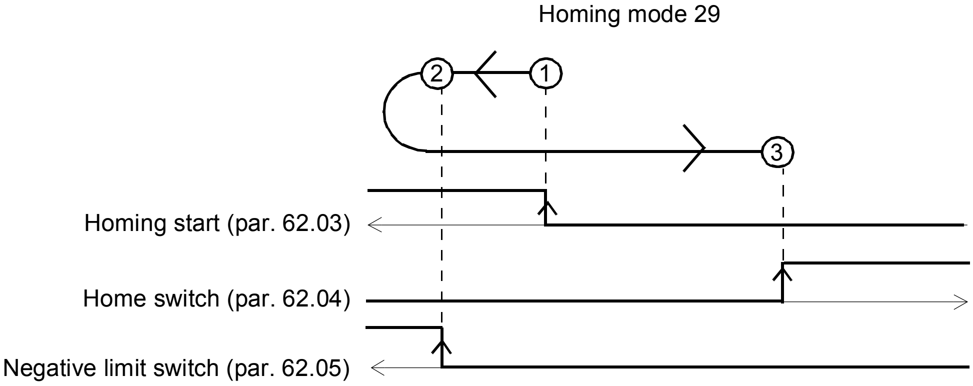


1	If the state of the home switch is 1: (par. 62.04 HOME SWITCH TRIG): Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

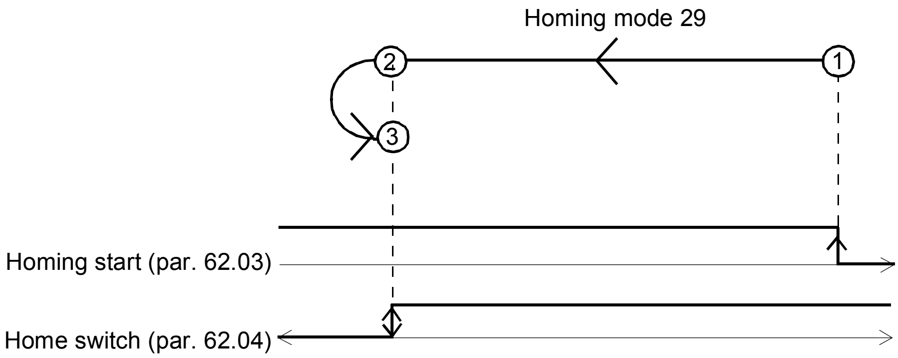
Homing mode 29



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG. **Note:** Stop is only possible after a falling edge of the home switch has been detected.

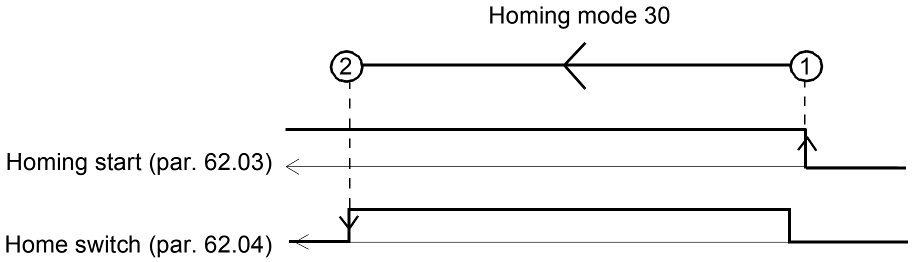


1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the negative limit switch signal selected by par. 62.05 NEG LIMIT SWITCH.
3	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

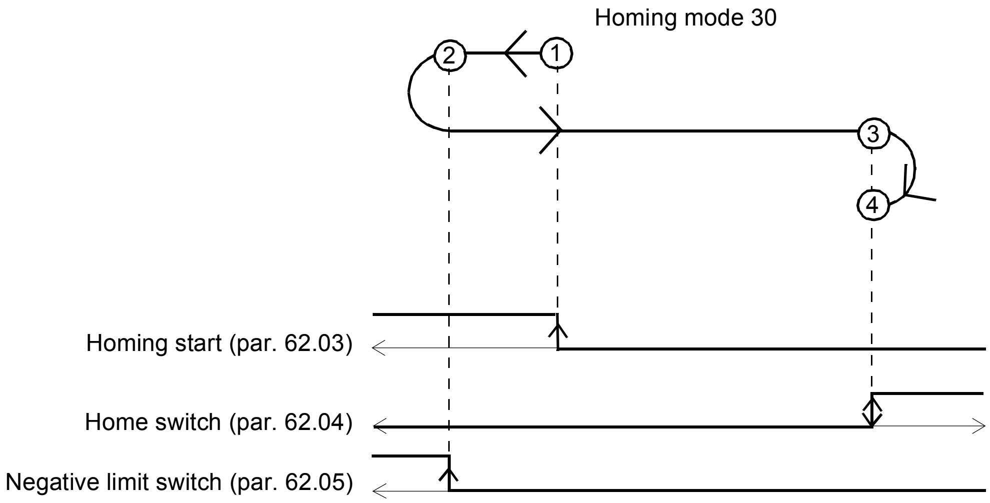


1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
3	Stop by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG. **Note:** Stop is only possible after a falling edge of the home switch has been detected.

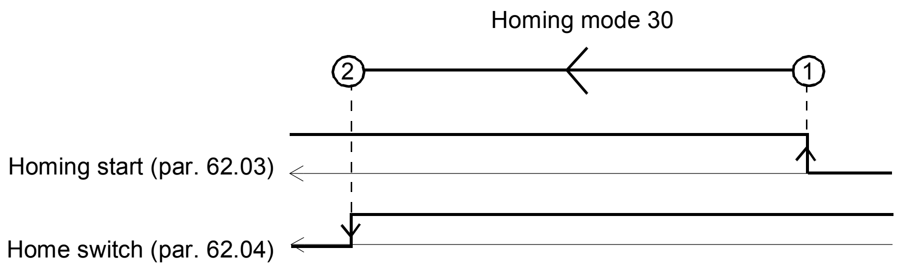
Homing mode 30



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.



1	If the state of the home switch is 0 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Change direction by the rising edge of the negative limit switch signal selected by par. 62.05 NEG LIMIT SWITCH.
3	Change direction by the rising edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.
4	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.



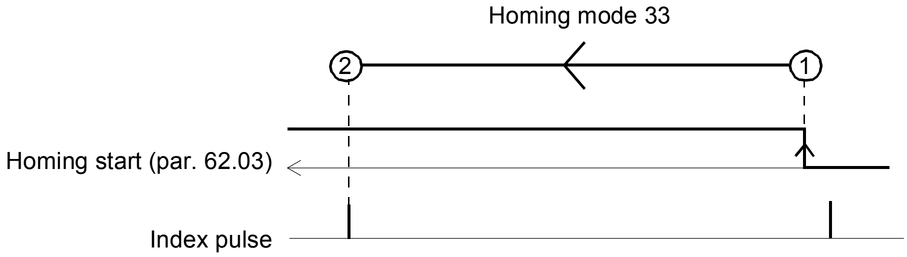
1	If the state of the home switch is 1 (par. 62.04 HOME SWITCH TRIG): Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the falling edge of the home switch signal selected by par. 62.04 HOME SWITCH TRIG.

Homing modes 31 and 32

Reserved

Homing mode 33

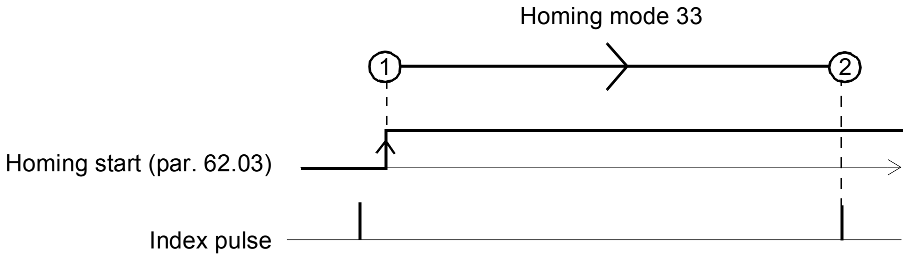
The status of the home switch at start is insignificant.



1	Start in the negative (left) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the next index pulse.

Homing mode 34

The status of the home switch at start is insignificant.



1	Start in the positive (right) direction by the rising edge of the signal selected by par. 62.03 HOMING START with homing speed 1, par. 62.07 HOMING SPEEDREF1.
2	Stop by the next index pulse.

Homing mode 35

In method 35 the current position is used as home position.

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EFFECTIVE: 08.12.2008

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